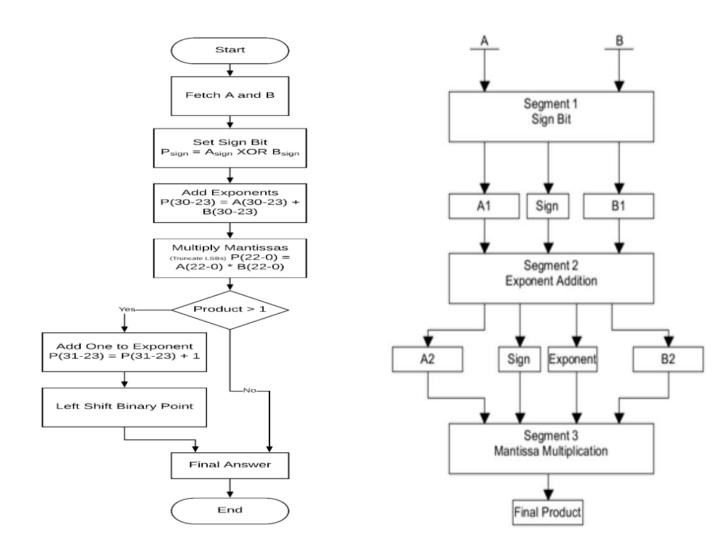
INSTITUTE OF TECHNOLOGY, NIRMA UNIVERSITY 2CS401 COMPUTER ARCHITECTURE INNOVATIVE ASSIGNMENT

Team Members –

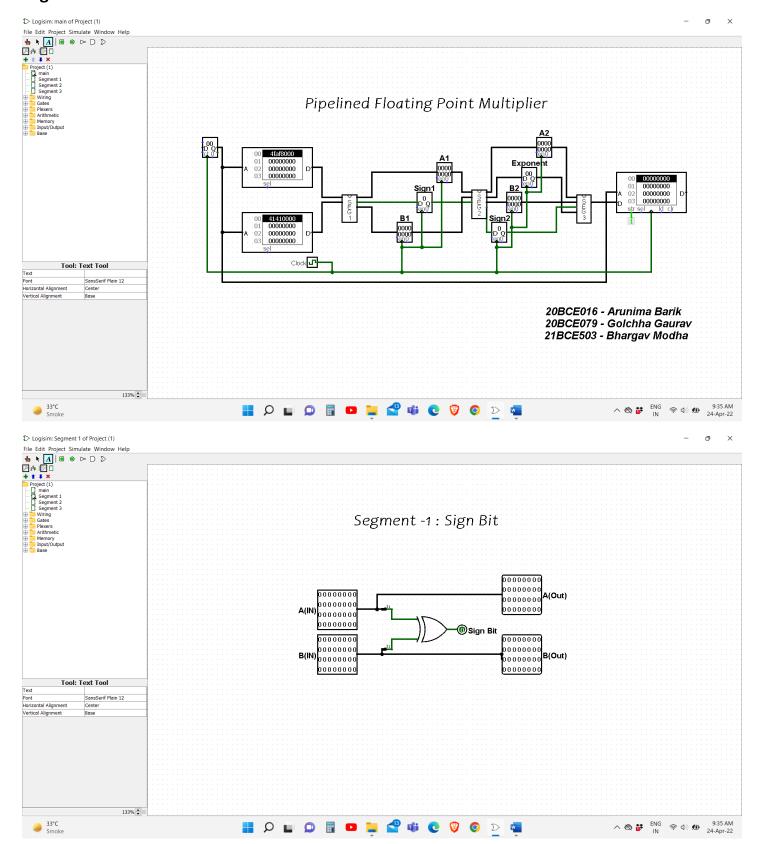
- 1. Arunima Barik (20BCE016)
- 2. Golchha Gaurav (20BCE079)
- 3. Bhargav Modha (21BCE503)

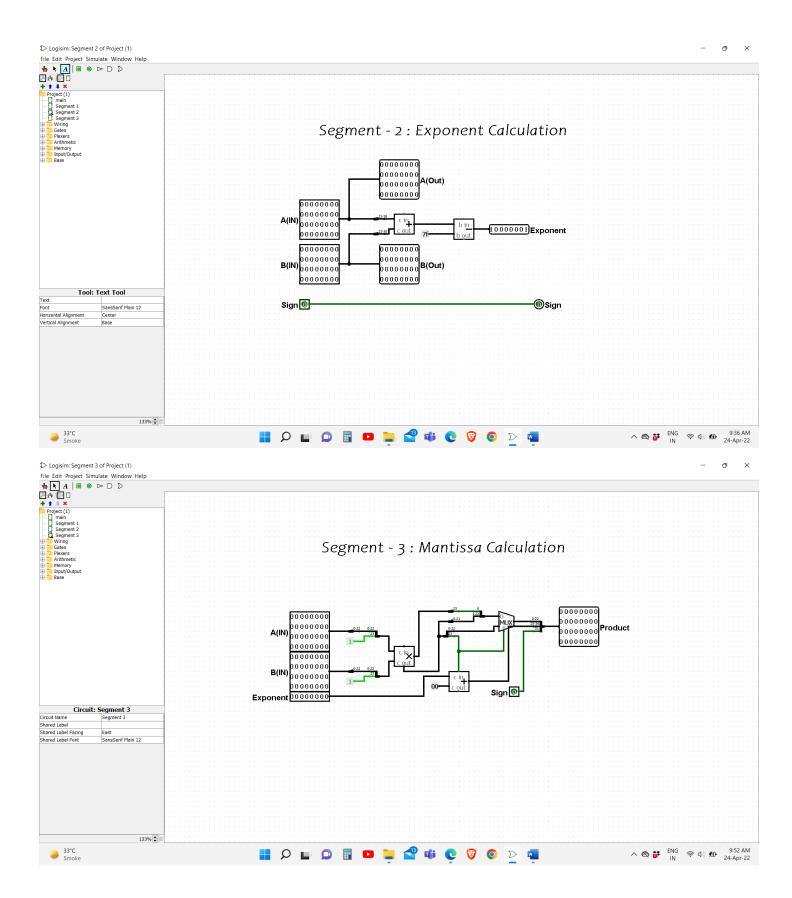
Floating Point Multiplication Using Pipeline

Flowchart -



Logic Circuit -





 $= (1.1000001 \times 2^{3})_{2}$

mantissa of A * mantissa of B
= (11110100100000
000000000 x (100000
= 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
= D/0 1111 00 101010100000000000000000000
carry
sinal representation ->
0 1 0 6 0 1 0 0 1 0 0 1 1 1 1 0 0
10101010000000
heradecimal representation ->
(4 4 B C A A 4 0) 16
) 16
decimal representation ->
= (1.011110010101001
000000 X 2 10)
$\frac{1}{2}$
= (10111100101.010000
000)
= (1509.3203)
(197)0