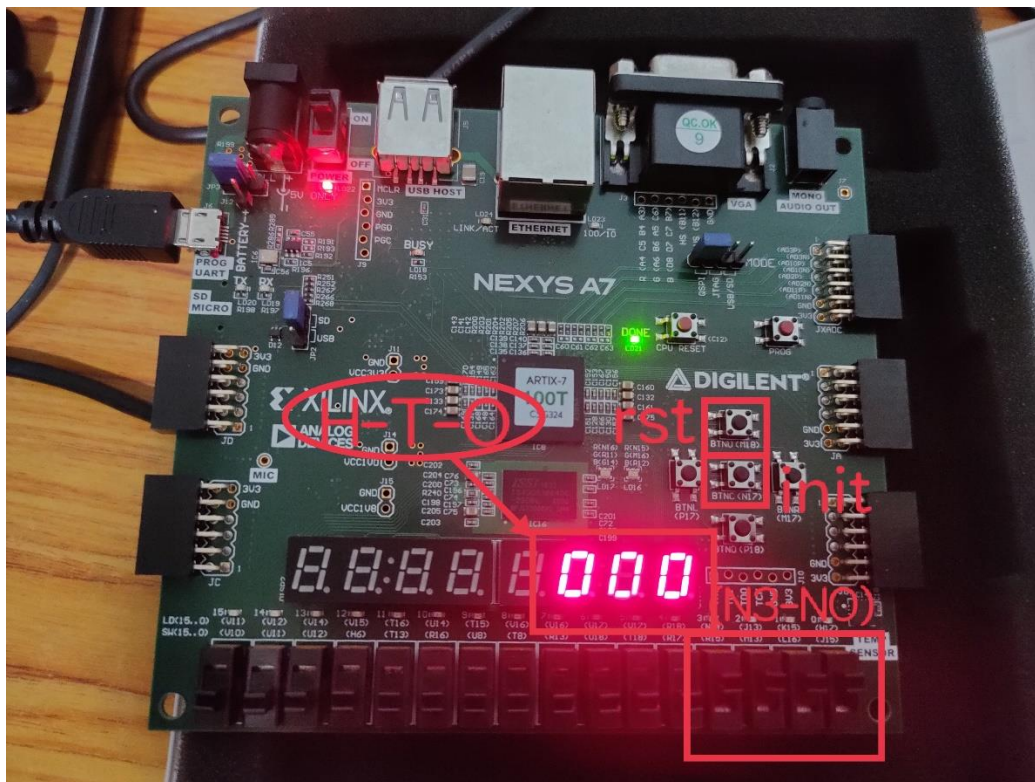


# FPGA Seg-Display Outputs



(N= 0000 to 1111)

