

This document has a list of instructions that are not working. The assembler output in all the mentioned cases is correct.

In all of the given cases, the output registers are showing addresses in the log file.

Instruction	Is it working
ADDBYTER	yes
ANDDBYTER	x
ORDBYTER	yes
XORDBYTER	yes
ZBYTEDPOS	yes
VFADD	x
VFSUB	x
VFMUL	x
VFDIV	x
VFSQRT	x
VADDD8	Lower bits with carry

EXPECTED - E00898F2

ACTUAL- 1F2