This document has a list of instructions that are not working. The assembler output in all the mentioned cases is correct.

In all of the given cases, the output registers are showing addresses in the log file.

Instruction	Is it working
ADDBYTER	x
ANDDBYTER	x
ORDBYTER	x
XORDBYTER	x
ZBYTEDPOS	x
VFADD	x
VFSUB	x
VFMUL	x
VFDIV	x
VFSQRT	x