This document has a list of instructions that are not working. The assembler output in all the mentioned cases is correct.

X signifies that output registers hold trash values.

Instruction	Expected	Actual
VADDD8	E00898F2	E00999F3 (Executing ADDD)
VADDD16	E00899F2	801F2
VADDD32	E00999F2	X
VUMULD8	02020202	03030303
VUMULD16	04020402	3003
VUMULD32	08060402	X
VSMULD8	FAFAFAFA	FFFFFF01 03030303
VSMULD16	F9FAF9FA	FF0101 1020101
VSMULD32	F9F9F9FA	X
VSUBD8	F	E000000F
VSUBD16	F	X
VSUBD32	F	X

For VADDD and VSUBD – following are correct -

opcode
operands
isa_mode
data_type

Incorrect -

itype   0x7fff (32767)
*inst_type   unknown
output result_l and result_h