## Understanding Synchronisation

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#### Abstract

. . .

## 1 Introduction

A common step of many concurrent programs involves two or more threads synchronising: each thread waits until other relevant threads have reached the synchronisation point before continuing; in addition, the threads can exchange data. Reasoning about programs can be easier when synchronisations are used: it helps us to reason about the states that different threads are in.

We study synchronisations in this paper, formalising the requirements of synchronisations, and outlining analysis and testing techniques.

We start by giving some examples of synchronisations in order to illustrate the idea. (We use Scala notation; we explain non-standard aspects of the language in footnotes.) In each case, the synchronisation is mediated by a synchronisation object.

Perhaps the most common form of synchronisation object is a synchronous channel. Such a channel might have signature<sup>1</sup>

```
class SyncChan{
  def send(x: A): Unit
  def receive(): A
}
```

Each invocation of one of the operations must synchronise with an invocation of the other operation: the two invocations must overlap in time. If an invocation send(x) synchronises with an invocation of receive, then the receive returns x.

Sometimes an invocation may synchronise with an invocation of the same operation. For example, an *exchanger* has the following signature.

<sup>&</sup>lt;sup>1</sup>The type Unit is the type that contains a single value, the *unit value*, denoted ().

```
class Exchanger{
  def exchange(x: A): A
}
```

When two threads call exchange, they each receive the value passed in by the other. When invocations of two different operations synchronise, we use the term *heterogeneous*; where two invocations of the same operation synchronise, we use the term *homogeneous*.

For some synchronisation objects, synchronisations might involve more than two threads. For example, an object of the following class

```
class Barrier(n: Int){
  def sync(): Unit
}
```

can be used to synchronise n threads, known as a barrier synchronisation: each thread calls sync, and no invocation returns until all n have called it.

A combining barrier also allows each thread to submit a parameter, and for all to receive back some function of those parameters.<sup>2</sup>

```
class CombiningBarrier(n: Int, f: (A,A) => A){
  def sync(x: A): A
}
```

If n threads call sync with parameters  $x_1, \ldots, x_n$ , in some order, then each receives back  $f(x_1, f(x_2, \ldots f(x_{n-1}, x_n) \ldots))$  (in the common case that f is associative and commutative, this result is independent of the order of the parameters).

In addition, we allow the synchronisations to be mediated by an object that maintains some state between synchronisations. As an example, consider a synchronous channel that, in addition, maintains a sequence counter, and such that both invocations receive the value of this counter.

```
class SyncChanCounter{
    private var counter: Int
    def send(x: A): Int
    def receive(): (A, Int)
}
```

Some synchronisation objects allow different modes of synchronisation. For example, consider a synchronous channel with timeouts: each invocation might synchronise with another invocation, or might timeout without synchronisation. Such a channel might have a signature as follows.

<sup>&</sup>lt;sup>2</sup>The Scala type (A,A) => A represents functions from pairs of A to A.

```
class TimeoutChannel{
  def send(x: A): Boolean
  def receive(u: Unit): Option[A]
}
```

The send operation returns a boolean to indicate whether the send was successful, i.e. whether it synchronised. The receive operation can return a value Some(x) to indicate that it synchronised and received x, or can return the value None to indicate that it failed to synchronise<sup>3</sup>. Thus an invocation of each operation may or may not synchronise with an invocation of the other operation.

A termination-detecting queue can also be thought of as a stateful synchronisation object with multiple modes. Such an object acts like a standard concurrent queue, except if all the threads are attempting to dequeue, but the queue is empty, then they all return a special value to indicate this fact. In many concurrent algorithms, such as a concurrent graph search, this latter outcome indicates that the algorithm should terminate. Such a termination-detecting queue might have the following signature, where a dequeue returns the value None to indicate the termination case.

```
class TerminationDetectingQueue(n: Int){ // n is the number of threads
  def enqueue(x: A): Unit
  def dequeue: Option[A]
}
```

The termination outcome can be seen as a synchronisation between all n threads. This termination-detecting queue combines the functionality of a concurrent datatype and a synchronisation object.

In this paper, we consider what it means for one of these synchronisation objects to be correct, and techniques for testing correctness.

In Section 2 we describe how to specify a synchronisation object. The definition has similarities with the standard definition of *linearisation* for concurrent datatypes, except it talks about synchronisations between invocations, rather than single invocations: we call the property *synchronisation linearisation*.

In Section 3 we consider the relationship between synchronisation linearisation and (standard) linearisation. We show that the two notions are different; but we show that synchronisation linearisation corresponds to a small adaptation of linearisation, where one of the operations of the synchronisation object corresponds to *two* operations of the object used to specify linearisation.

<sup>&</sup>lt;sup>3</sup>The type Option[A] contains the union of such values.

In Section 7 we consider how the property of synchronisation linearisation can be analysed via model checking.

In Section ?? we consider how to build testing frameworks for synchronisation objects. More here.

## 2 Specifying synchronisations

In this section we describe how synchronisations can be formally specified. For ease of exposition, we start by considering *heterogeneous binary* synchronisation in this section, where every synchronisation is between *two* invocations of *different* operations. We generalise at the end of this section.

We assume that the synchronisation object has two operations, each of which has a single parameter, with signatures as follows.

```
def op<sub>1</sub>(x_1: A_1): B_1 def op<sub>2</sub>(x_2: A_2): B_2
```

(We can model a concrete operation that takes  $k \neq 1$  parameters by an operation that takes a k-tuple as its parameter; we identify a 0-tuple with a unit value.) In addition, the synchronisation object might have some state, state: S. Each invocation of  $\mathsf{op}_1$  must synchronise with an invocation of  $\mathsf{op}_2$ , and vice versa. The result of each invocation may depend on the two parameters  $\mathsf{x}_1$  and  $\mathsf{x}_2$  and the current state. In addition, the state may be updated. The external behaviour is consistent with the synchronisation happening atomically at some point within the duration of both operation invocations (which implies that the invocations must overlap): we refer to this point as the  $synchronisation\ point$ .

Each synchronisation object can be specified using a *synchronisation spec*ification object with the following signature.

```
class Spec{ def sync(x_1: A_1, x_2: A_2): (B_1, B_2)}
```

The idea is that if two invocations  $op_1(x_1)$  and  $op_2(x_2)$  synchronise, then the results  $y_1$  and  $y_2$  of the invocations are such that  $sync(x_1, x_2)$  could return the pair  $(y_1, y_2)$ . The specification object might have some private state that is accessed and updated within sync. Note that invocations of sync occur sequentially.

We formalise below what it means for a synchronisation object to satisfy the requirements of a synchronisation specification object. But first, we give some examples to illustrate the style of specification. A generic definition of a specification object might take the following form

The object has some local state, which persists between invocations. The require clause of sync specifies a precondition for the synchronisation to take place. The values  $res_1$  and  $res_2$  represent the results that should be returned by the corresponding invocations of  $op_1$  and  $op_2$ , respectively. The function update describes how the local state should be updated.

For example, consider a synchronous channel with operations

```
def send(x: A): Unit
def receive(u: Unit): A
```

(Note that we model the receive operation as taking a parameter of type Unit, in order to fit our uniform setting.) This can be specified using a synchronisation specification object as follows, with empty state

```
class SyncChanSpec{
  def sync(x: A, u: Unit): (Unit, A) = ((), x)
}
```

If send(x) synchronises with receive(()), then the former receives the unit value (), and the latter receives x.

As another example, consider a filtering channel.

```
class FilterChan{
  def send(x: A): Unit
  def receive(p: A => Boolean): A
}
```

Here the receive operation is passed a predicate  ${\sf p}$  describing a required property of any value received. This can be specified using a specification object with operation

```
def sync(x: A, p: A => Boolean): (Unit, A) = { require(p(x)); ((), x) }
```

The require clause specifies that invocations send(x) and receive(p) can synchronise only if p(x).

As an example illustrating the use of state in the synchronisation object, recall the synchronous channel with a sequence counter, SyncChanCounter, from the introduction. This can be specified using the following specification object.

```
class SyncChanCounterSpec{
  private var counter = 0
  def sync(x: A, u: Unit): (Int, (A, Int)) = {
    counter += 1; (counter, (x, counter))
  }
}
```

## 2.1 Linearisability

We formalise below precisely the allowable behaviours captured by a particular synchronisation specification object. Our definition has much in common with the well known notion of *linearisation* [?], used for specifying concurrent datatypes; so we start by reviewing that notion. There are a number of equivalent ways of defining linearisation: we choose a way that will be convenient subsequently.

A concurrent history of an object o (either a concurrent datatype or a synchronisation object) records the calls and returns of operation invocations on o. It is a sequence of events of the following forms:

- call. $op^{i}(x)$ , representing a call of operation op with parameter x;
- return. $op^i$ :y, representing a return of an invocation of op, giving result y.

In each case, op is an operation of o. Here i is a invocation identity, used to identify a particular invocation, and to link the call and corresponding return. In order to be well formed, each invocation identity must appear on at most one call event and at most one return event; and for each event return  $op^i:y$ , the history must contain an earlier event  $call.op^i(x)$ , i.e. for the same operation and invocation identity. We consider only well formed histories from now on. We say that a call event and a return event match if they have the same invocation identifier. A concurrent history is complete if for every call event, there is a matching return event, i.e. no invocation is still pending at the end of the history.

For example, consider the following complete concurrent history of a concurrent object that is intended to implement a queue, with operations enq and deq.

```
\begin{array}{lll} h &=& \langle \mathsf{call.enq}^1(5), \; \mathsf{call.enq}^2(4), \; \mathsf{call.deq}^3(), \\ && \mathsf{return.enq}^1{:}(), \; \mathsf{return.deq}^3{:}4, \; \mathsf{return.enq}^2{:}() \rangle. \end{array}
```

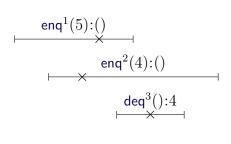


Figure 1: Timeline representing the linearisation example.

This history is illustrated by the timeline in Figure 1: here, time runs from left to right; each horizontal line represents an operation invocation, with the left-hand end representing the call event, and the right-hand end representing the return event.

Linearisability is specified with respect to a specification object Spec, with the same operations (and signatures) as the concurrent object in question. A history of the specification object is a sequence of events of the form:

•  $op^i(x)$ : y representing an invocation of operation op with parameter x, returning result y; again i is an invocation identity, which must appear at most once in the history.

A history is legal if it is consistent with the definition of Spec, i.e. for each invocation, the precondition is satisfied, and the return value is as for the definition of the operation in Spec.

For example, consider the history

$$h_s = \langle enq^2(4):(), enq^1(5):(), deq^3:4 \rangle.$$

This is a legal history for a specification object that represents a queue. This history is illustrated by the "×"s in Figure 1.

Let h be a complete concurrent history, and let  $h_s$  be a legal history of the specification object corresponding to the same invocations, i.e., for each  $call.op^i(x)$  and  $return.op^i:y$  in h,  $h_s$  contains  $op^i(x):y$ , and vice versa. We say that h and  $h_s$  are compatible if there is some way of interleaving the two histories (i.e. creating a history containing the events of h and  $h_s$ , preserving the order of events) such that each  $op^i(x):y$  occurs between  $call.op^i(x)$  and columntarrelation opi:y. Informally, this indicates that the invocations of h appeared to take place in the order described by  $h_s$ , and that this order is consistent with the specification object.

Continuing the running example, the histories h and  $h_s$  are compatible,

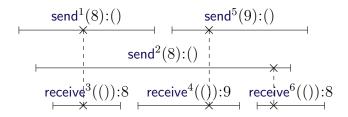


Figure 2: Timeline representing the synchronisation example.

as evidenced by the interleaving

```
\begin{split} &\langle \mathsf{call}.\mathsf{enq}^1(5),\ \mathsf{call}.\mathsf{enq}^2(4),\ \mathsf{enq}^2(4){:}(),\ \mathsf{enq}^1(5){:}(),\ \mathsf{call}.\mathsf{deq}^3(),\\ &\mathsf{return}.\mathsf{enq}^1{:}(),\ \mathsf{deq}^3{:}4,\ \mathsf{return}.\mathsf{deq}^3{:}4,\ \mathsf{return}.\mathsf{enq}^2{:}()\rangle, \end{split}
```

which is again illustrated in Figure 1.

We say that a complete history h is linearisable with respect to Spec if there is a corresponding valid history  $h_s$  of Spec such that h and  $h_s$  are compatible.

A concurrent history might not be complete, i.e. it might have some pending invocations. An *extension* of a history h is formed by adding zero or more return events corresponding to pending invocations. We write complete(h) for the subsequence of h formed by removing all call events corresponding to pending invocations. We say that a (not necessarily complete) concurrent history h is linearisable if there is an extension h' of h such that complete(h') is linearisable. We say that a concurrent object is linearisable if all of its histories are linearisable.

## 2.2 Synchronisation linearisability

We now adapt the definition of linearisability to synchronisations. We consider a concurrent history of the synchronisation object Sync, as with linearisability; in the case of binary synchronisation, this will contain events corresponding to the operations  $op_1$  and  $op_2$ .

For example, the following is a complete history of the synchronous channel from earlier, and is illustrated in Figure 2:

```
\begin{array}{lll} h &=& \langle \mathsf{call.send}^1(8), \; \mathsf{call.send}^2(8), \; \mathsf{call.receive}^3(()), \; \mathsf{return.receive}^3{:}(), \\ & & \mathsf{call.receive}^4(()), \; \mathsf{return.send}^1{:}(), \; \mathsf{call.send}^5(9), \; \mathsf{return.receive}^4{:}9, \\ & & \mathsf{call.receive}^6(()), \; \mathsf{return.send}^2{:}(), \; \mathsf{return.send}^5{:}(), \; \mathsf{return.receive}^6{:}8 \rangle. \end{array}
```

A history of a synchronisation specification object Spec is a sequence of events of the form  $sync^{i_1,i_2}(x_1,x_2):(y_1,y_2)$ , representing an invocation of sync

with parameters  $(x_1, x_2)$  and result  $(y_1, y_2)$ . The event's invocation identity is  $(i_1, i_2)$ : each of  $i_1$  and  $i_2$  must appear at most once in the history. Informally, an event  $\operatorname{sync}^{i_1,i_2}(x_1, x_2):(y_1, y_2)$  corresponds to a synchronisation between invocations  $\operatorname{op}_1^{i_1}(x_1):y_1$  and  $\operatorname{op}_2^{i_2}(x:2):y_2$  in a history of the corresponding synchronisation object.

A history is *legal* if is consistent with the specification object. For example, the following is a legal history of SyncChanSpec.

$$h_s = \langle \operatorname{sync}^{1,3}(8,()):((),8), \operatorname{sync}^{5,4}(9,()):((),9), \operatorname{sync}^{2,6}(8,()):((),8) \rangle.$$

The history is illustrated by the "x"s in Figure 2: each event corresponds to the synchronisation of two operations, so is depicted by two "x"s on the corresponding operations, linked by a dashed vertical line. This particular synchronisation specification object is stateless, so in fact any permutation of this history would also be legal (but not all such permutations will be compatible with the history of the synchronisation object); but the same will not be true in general of a specification object with state.

Let h be a complete history of the synchronisation object Sync. We say that a legal history  $h_s$  of  $Spec\ corresponds$  to h if:

- For each sync event with identity  $(i_1, i_2)$  in  $h_s$ , h contains an invocation of  $op_1$  with identity  $i_1$  and an invocation of  $op_2$  with identity  $i_2$ ;
- For each invocation of  $op_1$  with identity  $i_1$  in h,  $h_s$  contains a sync event with identity  $(i_1, i_2)$  for some  $i_2$ ;
- For each invocation of  $op_2$  with identity  $i_2$  in h,  $h_s$  contains a sync event with identity  $(i_1, i_2)$  for some  $i_1$ .

We say that a complete history h of Sync and a corresponding legal history  $h_s$  of Spec are synchronisation-compatible if there is some way of interleaving them such that each event  $\mathsf{sync}^{i_1,i_2}(x_1,x_2):(y_1,y_2)$  occurs between  $\mathsf{call.op}_1^{i_1}(x_1)$  and  $\mathsf{return.op}_1^{i_1}:y_1$ , and between  $\mathsf{call.op}_2^{i_2}(x_2)$  and  $\mathsf{return.op}_2^{i_2}:y_2$ . In the running example, the histories h and  $h_s$  are synchronisation compatible, as evidenced by the interleaving illustrated in Figure 2.

We say that a complete history h of Sync is synchronisation-linearisable if there is a corresponding legal history  $h_s$  of Spec such that h and  $h_s$  are synchronisation compatible.

We say that a (not necessarily complete) concurrent history h is synchronisation-linearisable if there is an extension h' of h such that complete(h) is synchronisation-linearisable. We say that a synchronisation object is synchronisation-linearisable if all of its histories are synchronisation-linearisable.

Is the definition compositional? I think so.

#### 2.3 Variations

Above we considered heterogeneous binary synchronisations, i.e. two invocations of different operations, with a single mode of synchronisation.

It is straightforward to generalise to synchronisations between an arbitrary number of invocations, some of which might be invocations of the same operation. Consider a k-way synchronisation between operations

```
def op<sub>i</sub>(x<sub>i</sub>: A<sub>i</sub>): B<sub>i</sub> for j = 1, ..., k,
```

where the  $op_j$  might not be distinct. The specification object will have a corresponding operation

```
def sync(x_1: A_1, ..., x_k: A_k): (B_1, ..., B_k)
```

For example, for the combining barrier CombiningBarrier(n, f) of the Introduction, the corresponding specification object would be

A history of the specification object will have corresponding events  $\operatorname{sync}^{i_1,\dots,i_k}(x_1,\dots,x_k):(y_1,\dots,y_k)$ . The definition of synchronisation compatibility is an obvious adaptation of earlier: in the interleaving of the complete history of the synchronisation history and the history of the specification object, each  $\operatorname{sync}^{i_1,\dots,i_k}(x_1,\dots,x_k):(y_1,\dots,y_k)$  occurs between  $\operatorname{call.op}_1^{i_j}(x_j)$  and  $\operatorname{return.op}_j^{i_j}:y_j$  for each  $j=1,\dots,k$ . The definition of synchronisation-linearisability follows in the obvious way.

It is also straightforward to adapt the definitions to deal with multiple modes of synchronisation: the specification object has a different operation for each mode. For example, recall the TimeoutChannel from the Introduction, where sends and receives may timeout and return without synchronisation. The corresponding specification object would be:

```
 \begin{split} &\textbf{class} \; \mathsf{TimeoutChannelSpec} \{ & \; \textbf{def} \; \mathsf{sync}_s(\mathsf{x} \colon \mathsf{A}) = \mathsf{false} \\ & \; \textbf{def} \; \mathsf{sync}_r(\mathsf{u} \colon \mathsf{Unit}) = \mathsf{None} \\ & \; \textbf{def} \; \mathsf{sync}_{s,r}(\mathsf{x} \colon \mathsf{A}, \; \mathsf{u} \colon \mathsf{Unit}) = (\mathsf{true}, \; \mathsf{Some}(\mathsf{x})) \\ & \} \end{aligned}
```

The operation  $\mathsf{sync}_s$  corresponds to a send returning without synchronising; likewise  $\mathsf{sync}_r$  corresponds to a receive returning without synchronising; and  $\mathsf{sync}_{s,r}$  corresponds to a send and receive synchronising. The formal defini-

tion of synchronisation linearisation is the obvious adaptation of the earlier definition.

# 3 Relating synchronisation linearisation and linearisation

In this section we describe the relationship between synchronisation linearisation and standard linearisation.

It is clear that standard linearisation is equivalent to synchronisation linearisation in the (rather trivial) case that no operations synchronise, so each operation of the synchronisation specification object corresponds to a single operation of the concurrent object.

However, linearisability and synchronisation linearisability are not equivalent in general: we show that, given a synchronisation linearisability specification object SyncSpec, it is not always possible to find a linearisability specification Spec such that for every history h, h is synchronisation linearisable with respect to Spec.

For example, consider the example of a synchronous channel from Section 2, where synchronisation linearisation is captured by SyncChanSpec. Assume (for a contradiction) that the same property can be captured by linerisation with respect to linearisability specification Spec. Consider the history

$$h = \langle \mathsf{call.send}^1(3), \mathsf{call.receive}^2(), \mathsf{return.send}^1(1), \mathsf{return.receive}^2(1) \rangle$$

This is synchronisation linearisable with respect to SyncChanSpec. By the assumption, it must also be linearisable with respect to Spec; so there must be a legal history  $h_s$  of Spec such that h and  $h_s$  are compatible. Without loss of generality, suppose the send in  $h_s$  occurs before the receive, i.e.

$$h_s = \langle \operatorname{send}^1(3) : (), \operatorname{receive}^2() : 3 \rangle.$$

But the history

$$h' \ = \ \langle \mathsf{call.send}^1(3), \mathsf{return.send}^1 : (), \mathsf{call.receive}^2(), \mathsf{return.receive}^2() : 3 \rangle$$

is also compatible with  $h_s$ , so h' is linearisable with respect to Spec. But then the assumption would imply that h' is synchronisation linearisable with respect to SyncChanSpec. This is clearly false, because the operations do not overlap. Hence no such linearisability specification Spec exists.

## 3.1 Two-step linearisability

We now show that synchronisation linearisability corresponds to a small adaptation of linearisability, where one of the operations on the concurrent object corresponds to *two* operations of the linearisability specification object. We define what we mean by this, and then prove the correspondence in the next subsection. In the definitions below, we describe just the differences from standard linearisation, to avoid repetition.

Given a synchronisation object with operations  $op_1$  and  $op_2$ , we will consider a linearisability specification object with signature

```
class TwoStepLinSpec { def op<sub>1</sub>(x<sub>1</sub>: A<sub>1</sub>): Unit def \overline{op}_1(): B<sub>1</sub> def op<sub>2</sub>(x<sub>2</sub>: A<sub>2</sub>): B<sub>2</sub> }
```

The idea is that the operation  $op_1$  on the concurrent object will be linearised by the composition of the two operations  $op_1$  and  $op_1$ ; but operation  $op_2$  on the concurrent object will be linearised by just the operation  $op_2$  of the specification object, as before. We call such an object a two-step linearisability specification object.

We define a history  $h_s$  of such a two-step specification object much as in Section 2.1, with the addition that for each event  $\overline{op}_1^i():y$  in  $h_s$ , we require that there is an earlier event  $op_1^i(x):()$  in  $h_s$  with the same invocation identity; other than in this regard, invocation identities are not repeated in  $h_s$ .

Let h be a complete concurrent history of a synchronisation object, and let  $h_s$  be a legal history of a two-step specification object corresponding to the same invocations in the following sense:

- For every call.op<sub>1</sub><sup>i</sup>(x) and return.op<sub>1</sub><sup>i</sup>:y in h, h<sub>s</sub> contains op<sub>1</sub><sup>i</sup>(x):() and  $\overline{op}_1^i$ ():y; and vice versa;
- For every call.op<sub>2</sub><sup>i</sup>(x) and return.op<sub>2</sub><sup>i</sup>:y in h, h<sub>s</sub> contains op<sub>2</sub><sup>i</sup>(x):y; and vice versa.

We say that h and  $h_s$  are two-step-compatible if there is some way of interleaving the two histories such that

- Each  $\operatorname{\sf op}_1^i(x)$ :() and  $\operatorname{\overline{\sf op}}_1^i()$ :y occur between  $\operatorname{\sf call.op}_1^i(x)$  and  $\operatorname{\sf return.op}_1^i$ :y, in that order;
- Each  $op_2^i(x):y$  occurs between call. $op_2^i(x)$  and return. $op_2^i:y$ .

For example, consider a synchronous channel, with send corresponding to  $\mathsf{op}_1$ , and receive corresponding to  $\mathsf{op}_2$ . Then the following would be an interleaving of two-step-compatible histories of the synchronisation object and the corresponding specification object.

$$\langle \text{call.send}^1(3), \text{ send}^1(3):(), \text{ call.receive}^2(), \text{ receive}^2():3, \overline{\text{send}}^1():(), \text{ return.send}^1:(), \text{ return.receive}^2:3 \rangle.$$

This is represented by the following timeline, where the horizontal lines represent the interval between the call and return events, and the "x"s represent the corresponding operations of the specification object.

The definition of two-step linearisability then follows from this definition of two-step compatability, precisely as in Section 2.1.

## 3.2 Proving the relationship

We now prove the relationship between synchronisation linearisation and two-step linearisation.

Consider a synchonisation specification object SyncSpec. We build a corresponding two-step linearisation specification object TwoStepLinSpec such that synchronisation linearisation with respect to SyncSpec is equivalent to two-step linearisation with respect to TwoStepLinSpec. The definition we choose is not the simplest possible, but it is convenient for the testing framework we use in Section 5.

The definition of TwoStepLinSpec is below. We assume that each thread has an identity in some range [0...NumThreads). For simplicity, we arrange for this identity to be included in the call events written to the log for operations  $op_1$  and  $op_1$ . (Alternatively, we could implement thread identities internally to the object, for example using the technique from [?, Appendix A.2.4].)

This object requires that corresponding invocations of  $\mathsf{op}_1$  and  $\mathsf{op}_2$  are linearised consecutively: it does this by encoding the automaton on the right. However, it allows the corresponding  $\overline{\mathsf{op}}_1$  to be linearised later (but before the next operation invocation by the same thread). It uses an array returns, indexed by thread identities, to record the value that should be returned by an  $\overline{\mathsf{op}}_1$  operation by each thread.

```
// Thread identifiers
type ThreadID = Int
val NumThreads: ThreadID = ... // Number of threads
trait State
case class Zero extends State
case class One(t: ThreadID, x_1: A_1) extends State
object TwoStepLinSpec{
  private var state: State = Zero
  private val returns = new Array[Option[B<sub>1</sub>]](NumThreads)
  for(t < 0 \text{ until } NumThreads) \text{ returns}(t) = None
  def op<sub>1</sub>(t: ThreadID, x_1: A_1): Unit = {
    require(state.isInstanceOf[Zero]); state = One(t, x_1); ()
  def op<sub>2</sub>(x<sub>2</sub>: A<sub>2</sub>): B<sub>2</sub> = {
    require(state.isInstanceOf[One]); val One(t, x_1) = state
    val (y_1, y_2) = SyncSpec.sync(x_1, x_2); returns(t) = Some(y_1); state = Zero; y_2
  def \overline{op}_1(t: ThreadID): B_1 = \{
    require(returns(t).isInstanceOf[Some]); val Some(y_1) = returns(t)
    returns(t) = None; y_1
}
```

Do we want to talk about *complete* histories of TwoStepDelayedLinSpec?

– containing all three events of a set

The following lemma identifies important properties of TwoStepDelayed-LinSpec. It follows immediately from the definition.

**Lemma 1** Within any legal history of TwoStepDelayedLinSpec, events op<sub>1</sub> and op<sub>2</sub> alternate. Let op<sub>1</sub><sup>i<sub>1</sub></sup>(t, x<sub>1</sub>):() and op<sub>2</sub><sup>i<sub>2</sub></sup>(x<sub>2</sub>):y<sub>2</sub> be a consecutive pair of such events. Then op<sub>2</sub> makes a call SyncSpec.sync(x<sub>1</sub>, x<sub>2</sub>) obtaining result (y<sub>1</sub>, y<sub>2</sub>). The next event for thread t (if any) will be  $\overline{op}_1^{i_1}(t)$ :y<sub>1</sub>; and this will be later in the history than op<sub>2</sub><sup>i<sub>2</sub></sup>(x<sub>2</sub>):y<sub>2</sub>. Further, the corresponding history of events sync<sup>i<sub>1</sub>,i<sub>2</sub></sup>(x<sub>1</sub>, x<sub>2</sub>):(y<sub>1</sub>, y<sub>2</sub>) is a legal history of SyncSpec.

Conversely, each history with events ordered in this way will be a legal history of TwoStepDelayedLinSpec if the corresponding history of events  $sync^{i_1,i_2}(x_1,x_2):(y_1,y_2)$  is a legal history of SyncSpec.

The following proposition reduces synchronisation linearisability to twostep linearisability.

**Proposition 1.1** Let SyncObj be a synchronisation object, SyncSpec be a synchronisation specification object, and let TwoStepLinSpec be built from

SyncSpec as above. Then SyncObj is two-step linearisable with respect to Two-StepLinSpec if and only if it is synchronisation linearisable with respect to SyncSpec.

**Proof:** ( $\Rightarrow$ ). Let h be a concurrent history of SyncObj. By assumption, there is an extension h' of h, and a legal history  $h_s$  of TwoStepLinSpec such that h'' = complete(h') and  $h_s$  are two-step-compatible. Build a history  $h'_s$  of SyncSpec by replacing each pair  $\operatorname{op}_1^{i_1}(x_1)$ :(),  $\operatorname{op}_2^{i_2}(x_2)$ : $y_2$  in  $h_s$  by the event  $\operatorname{sync}_1^{i_1,i_2}(x_1,x_2)$ :( $y_1,y_2$ ), where  $y_1$  is the value returned by the corresponding  $\operatorname{op}_1^{i_1}()$ . This is illustrated by the example timeline below, where h'' is represented by the horizontal lines and the labels above;  $h_s$  is represented by the " $\times$ " and the labels below; and  $h'_s$  is represented by the " $\bullet$ " and the label below.

$$\begin{array}{c} \operatorname{send}^1(3) \colon () \\ \\ \operatorname{send}^1(3) \colon () \\ \\ \operatorname{send}^1(3) \colon () \\ \\ \operatorname{receive}^2(()) \colon 3 \\ \\ \operatorname{receive}^2(()) \colon 3 \\ \\ \operatorname{sync}^{1,2}(3,()) \colon ((),3) \end{array}$$

The history  $h_s'$  is legal by Lemma 1. It is possible to interleave h'' and  $h_s'$  by placing each event  $\mathsf{sync}^{i_1,i_2}(x_1,x_2){:}(y_1,y_2)$  in the same place as the corresponding event  $\mathsf{op}_2^{i_2}(x_2){:}y_2$  in the interleaving of h'' and  $h_s$ ; by construction, this is between  $\mathsf{call.op}_1^{i_1}(x_1)$  and  $\mathsf{return.op}_1^{i_1}{:}y_1$ , and between  $\mathsf{call.op}_2^{i_2}(x_2)$  and  $\mathsf{return.op}_2^{i_2}{:}y_2$ . Hence h'' and  $h_s$  are synchronisation-compatible; so h'' is synchronisation-lineariable; and so h is synchronisation-lineariable.

( $\Leftarrow$ ). Let h be a complete history of SyncObj. By assumption, there is an extension h' of h, and a legal history  $h_s$  of SyncSpec such that h'' = complete(h') and  $h_s$  are synchronisation compatible. Build a history  $h'_s$  of TwoStepLinSpec by replacing each event  $\operatorname{sync}^{i_1,i_2}(x_1,x_2):(y_1,y_2)$  in  $h_s$  by the three events  $\operatorname{op}_1^{i_1}(x_1):()$ ,  $\operatorname{op}_2^{i_2}(x_2):y_2$ ,  $\overline{\operatorname{op}}_1^{i_1}():y_1$ .

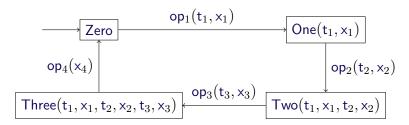
The history  $h'_s$  is legal by Lemma 1. It is possible to interleave h'' and  $h'_s$  by placing each triple  $\operatorname{op}_1^{i_1}(x_1)$ :(),  $\operatorname{op}_2^{i_2}(x_2)$ : $y_2$ ,  $\overline{\operatorname{op}}_1^{i_1}()$ : $y_1$  in the same place as the corresponding event  $\operatorname{sync}^{i_1,i_2}(x_1,x_2)$ : $(y_1,y_2)$  in the interleaving of h'' and  $h_s$ ; by construction, each  $\operatorname{op}_1^{i_1}(x_1)$ :() and  $\overline{\operatorname{op}}_1^{i_1}()$ : $y_1$  are between  $\operatorname{call.op}_1^{i_1}(x_1)$  and  $\operatorname{return.op}_1^{i_1}$ : $y_1$ ; and each  $\operatorname{op}_2^{i_2}(x_2)$ : $y_2$  is between  $\operatorname{call.op}_2^{i_2}(x_2)$  and  $\operatorname{return.op}_2^{i_2}$ : $y_2$ . Hence h'' and  $h_s$  are two-step-compatible; so h'' is two-step-linearisable; and so h is two-step-linearisable.

The two-step linearisation specification object can often be significantly

simplified from the template definition above. Here is such a specification object for a synchronous channel.

#### 3.3 Variations

The results of this section carry across to non-binary synchronisations, in a straightforward way. For a synchronisation object with k operations,  $\mathsf{op}_1$ , ...,  $\mathsf{op}_k$ , the operations  $\mathsf{op}_1$ , ...,  $\mathsf{op}_{k-1}$  are each linearised in two steps. The two-step linearisation specification object has 2k-1 operations,  $\mathsf{op}_1$ , ...,  $\mathsf{op}_k$ ,  $\overline{\mathsf{op}}_1$ , ...,  $\overline{\mathsf{op}}_{k-1}$ . Each of the first k-1 op operations takes a thread identity as a parameter. The specification object encodes an automaton with k states. The figure below gives the automaton in the case k=4.



The final op operation,  $op_4$  in the above figure, applies the sync method of the synchronisation specification object to the parameters  $x_1, \ldots, x_k$  to obtain the results  $y_1, \ldots, y_k$ ; it stores the first k-1 in appropriate returns<sub>i</sub> arrays, and returns  $y_k$  itself. In the case k=4, it has definition:

```
\begin{array}{l} \textbf{def} \ \mathsf{op}_4(\mathsf{x}_4 \colon \mathsf{A}_4) \colon \mathsf{B}_4 = \{ \\ \ \mathsf{require}(\mathsf{state}.\mathsf{isInstanceOf}[\mathsf{Three}]); \ \textbf{\textit{val}} \ \mathsf{Three}(\mathsf{t}_1, \, \mathsf{x}_1, \, \mathsf{t}_2, \, \mathsf{x}_2, \, \mathsf{t}_3, \, \mathsf{x}_3) = \mathsf{state} \\ \ \textbf{\textit{val}} \ (\mathsf{y}_1, \, \mathsf{y}_2, \, \mathsf{y}_3, \, \mathsf{y}_4) = \mathsf{SyncSpec}.\mathsf{sync}(\mathsf{x}_1, \, \mathsf{x}_2, \, \mathsf{x}_3, \, \mathsf{x}_4) \\ \ \mathsf{returns}_1(\mathsf{t}_1) = \mathsf{Some}(\mathsf{y}_1); \ \mathsf{returns}_2(\mathsf{t}_2) = \mathsf{Some}(\mathsf{y}_2); \ \mathsf{returns}_3(\mathsf{t}_3) = \mathsf{Some}(\mathsf{y}_3) \\ \ \mathsf{state} = \mathsf{Zero}; \, \mathsf{y}_4 \\ \} \end{array}
```

## 4 Linearisability testing

Most of the techniques that we describe for testing synchronisation linearisation are influenced by the techniques for testing (standard) linearisation testing [?], so we begin by sketching those techniques.

The idea of linearisability testing is as follows. We run several threads, performing operations (typically chosen randomly) upon the concurrent datatype that we are testing, and logging the calls and returns. More precisely, a thread that performs a particular operation  $\operatorname{op}^i(x)$ : (1) writes  $\operatorname{call.op}^i(x)$  into the  $\operatorname{log}$ ; (2) performs  $\operatorname{op}(x)$  on the synchonisation object, obtaining result y, say; (3) writes  $\operatorname{return.op}^i : y$  into the  $\operatorname{log}$ .

Once all threads have finished, we can use an algorithm to test whether the history is linearisable with respect to the specification object. Informally, the algorithm searches for an order to linearise the invocations, consistent with what is recorded in the log, and such that the order represents a legal history of the specification object. See [?] for details of the algorithms.

This process can be repeated many times, so as to generate and analyse many histories. Our experience is that the technique works well. It seems effective at finding bugs, where they exist, typically within a few seconds; for example, we used it to find an error in the concurrent priority queue of [?], which we believe had not previously been documented. Further, the technique is easy to use: we have taught it in our undergraduate Concurrent Programming course at Oxford, and students have used it effectively.

Note that this testing concentrates upon the safety property of linearisation, rather than liveness properties such as deadlock-freedom. However, if the concurrent object can deadlock, it is likely that the testing will discover this. Related to this point, it is the responsibility of the tester to define the threads in a way that all invocations will eventually return, so the threads terminate. For example, consider a partial stack where a pop operation blocks while the stack is empty; here, the tester would need to ensure that threads collectively perform at least as many pushes as pops, to ensure that each pop does eventually return.

Another thing to note is that there is potentially a delay between a thread writing the call event into the log and actually calling the operation; and likewise there is potentially a delay between the operation returning and the thread writing the return event into the log. However, these delays do not generate false errors: if a history without such delays is linearisable, then so is a corresponding history with delays. We believe that it is essential that

the technique does not give false errors: an error reported by testing should represent a real error; testing of a correct implementation should be able to run unsupervised, maybe for a long time. Further, our experience is that the delays do not prevent the detection of bugs when they exist (although might require performing the test more times). This means that a failure to find any bugs, after a large number of tests, can give us good confidence in the correctness of the concurrent datatype.

## 5 Hacking the linearisablity framework

In this section we investigate how to use the existing linearisation testing framework for testing synchronisation linearisation, using the ideas of Section 3.2. This is not a use for which the framework was intended, so we consider it a hack. However, it has the advantage of not requiring the implementation of any new algorithms.

Recall, from the introduction of Section 3, that a straightforward approach won't work. Instead we adapt the idea of two-step linearisation from later in that section. We aim to obtain a log history that can be tested for linearisability against TwoStepLinSpec.

We start by considering the case of binary heterogeneous synchronisation.

generalise

As with standard linearisability testing, we run several threads, calling operations on the synchronisation object, and logging the calls and returns.

- A thread that performs the concrete operation  $\mathsf{op}_1(x_1)$ : (1) writes  $\mathsf{call.op}_1^i(x_1)$  into the log; (2) performs  $\mathsf{op}_1(x_1)$  on the synchonisation object, obtaining result  $y_1$ , say; (3) writes  $\mathsf{return.op}_1^i$ :(),  $\mathsf{call.op}_1^i$ () and  $\mathsf{return.op}_1^i$ : $y_1$  into the log (in that order).
- A thread that performs operation op<sub>2</sub> acts as for standard linearisability testing.

Figure 3 illustrates a possible log. Note there might be delays involved in writing to the log. We refer to the *log history*, to distinguish it from the history of calls and returns on the synchronisation object.

Note that (as with standard linearisation) the tester needs to define the threads so that all invocations will eventually return, i.e. that each will be able to synchronise. For a binary synchronisation with no precondition, we can achieve this by half the threads calling one operation, and the other half calling the other operation (with the same number of calls by each).

Once all threads have finished, we test whether the log history is linearisable (i.e. standard linearisation) with respect to TwoStepLinSpec from

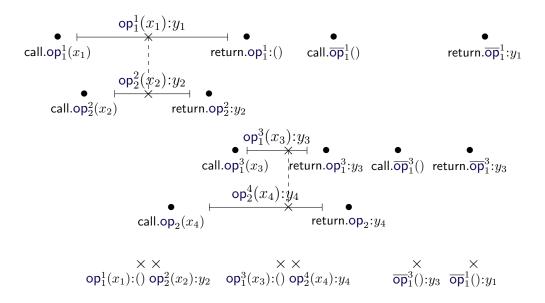


Figure 3: Illustration of a log for two-step synchronisation linearisation testing. Horizontal lines represent the operation calls themselves. Bullets (read from left to right) represent the log history. The crosses on operation calls, lined by dashed lines, represent the synchronisation points. The bottom row illustrates the history of the two-step synchronisation object constructed in the proof of Lemma 2.

Section 3. The following lemma shows that this approach does not find false errors.

**Lemma 2** Suppose the synchronisation object is synchronisation-linearisable with respect to SyncSpec. Then each history obtained by the above process is linearisable with respect to TwoStepLinSpec.

**Proof:** Let h by a history of actual calls and returns, and let  $h_l$  be a corresponding log history. By assumption, h is synchronisation-linearisable, so let  $h_s$  be the history of SyncSpec that is synchronisation-compatible with h. Consider the interleaving of all three: i.e. h and  $h_l$  are interleaved corresponding to temporal ordering; and h and  $h_s$  are interleaved, as required for synchronisation compatibility. Figure 3 gives an example.

We construct a history  $h_t$  of TwoStepLinSpec such that  $h_l$  and  $h_t$  are compatible. We define  $h_t$  by interleaving it with the previous histories as follows.

• For each synchronisation point  $s = \operatorname{sync}^{i_1,i_2}(x_1,x_2):(y_1,y_2)$  in  $h_s$ , we add an event  $\operatorname{op}_1^{i_1}(x_1):()$  immediately before s, and an event  $\operatorname{op}_2^{i_2}(x_2):y_2$ 

immediately after s (i.e. such that there is no other event between these two events).

• For each pair of events call. $\overline{op}_1^{i_1}()$  and return. $\overline{op}_1^{i_1}:y_1$ , we insert an event  $\overline{op}_1^{i_1}():y_1$  at an arbitrary place in between (but not between a pair of events inserted under the previous item).

The bottom row of Figure 3 illustrates this construction.

The histories  $h_l$  and  $h_t$  are compatible by construction: in the above interleaving, each event of  $h_t$  is between the corresponding call and return events of  $h_l$ , and has the appropriate parameter and return value. Further,  $h_t$  is a legal history of TwoStepLinSpec, by Lemma 1 and the fact that  $h_s$  is a legal history of SyncSpec. Hence  $h_l$  is linearisable with respect to TwoStepLinSpec.

The converse of the above lemma does not hold. A history of the synchronisation object might not be synchronisation-linearisable, but the corresponding log history might be linearisable with respect to TwoStepLinSpec. This is because of delays in logging: two invocations might not overlap in reality, but might appear to overlap in the log, and so appear to be a valid synchronisation. Alternatively, the delays in logging might make it appear that two synchronisations can occur in the opposite order to what is possible with the actual history. We suspect such cases are rare in practice.

Nevertheless, the following lemma shows that any non-synchronisation-linearisable history may give rise to a non-linearisable log history, informally if the logging is done fast enough.

**Lemma 3** Let h be a complete a history of operation invocations that is not synchronisation-linearisable with respect to SyncSpec. Then there is a corresponding log history  $h_l$  that is not linearisable with respect to TwoStepLinSpec.

**Proof:** We construct  $h_l$  by interleaving with h, so that each event of  $h_l$  occurs as close as possible to the corresponding call or return in h, i.e.:

- Each call. $op_j^i(x)$  in  $h_l$  occurs immediately before the corresponding call in h, for j = 1, 2;
- Each return.op<sub>1</sub><sup>i</sup>:(), call. $\overline{op}_1^i$ () and return. $\overline{op}_1^i$ : $y_1$  in  $h_l$  occur immediately after the corresponding return in h;
- Each return.op $_2^i$ : $y_2$  occurs immediately after the corresponding return in h.

Here "immediately before" or "immediately after" means there are no intermediate events. Figure 4 gives an illustrative example.

We show that  $h_l$  is not linearisable with respect to TwoStepLinSpec. We argue by contradiction: we assume that  $h_l$  is linearisable, and deduce that h

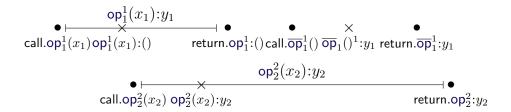


Figure 4: Illustration of the construction in the proof of Lemma 3. Horizontal lines represent the operation calls themselves. Bullets represent the log history. Crosses represent the linearisation points of the two-step synchronisation object.

is synchronisation-linearisable. So let  $h_t$  be a history of TwoSpecLinSpec such that  $h_l$  and  $h_t$  are compatible.

We interleave  $h_t$  with the interleaving of  $h_l$  and h, by inserting each event of  $h_t$  in a way that is consistent with the interleaving of  $h_l$  and  $h_t$ , but consistent with the above construction of  $h_l$ , maintaining the "immediately before" and "immediately after" properties, so not between corresponding call/return events from h and  $h_l$ . This means:

- Each  $op_1^{i_1}(x_1)$ :() from  $h_t$  occurs between the corresponding call and return events of  $op_1$  in h;
- Each  $op_2^{i_2}(x_2):y_2$  from  $h_t$  occurs between the corresponding call and return events of  $op_2$  in h;
- Each  $\overline{op}_1^{i_1}():y_1$  from  $h_t$  necessarily occurs between the corresponding call. $\overline{op}_1^{i_1}()$  and return. $\overline{op}_1^{i_1}:y_1$ , with these three events being consecutive.

Further, for a matching pair  $i_1$  and  $i_2$  of invocations (using Lemma 1):

- $\operatorname{op}_2^{i_2}(x_2):y_2$  occurs after the corresponding  $\operatorname{op}_1^{i_1}(x_1):()$  event, and so after the corresponding call of  $\operatorname{op}_1$  in h.
- $\operatorname{op}_2^{i_2}(x_2):y_2$  occurs before the corresponding  $\overline{\operatorname{op}}_1^{i_1}():y_1$  event, and so before the corresponding return of  $\operatorname{op}_1$  in h.

Figure 4 illustrates. We linearise each synchronisation at the point of the  $\operatorname{op}_2^{i_2}(x_2)$ : $y_2$  event. We have shown that this is within the period of each invocation. Further, by Lemma 1, this represents a legal history of SyncSpec. Hence h is synchronisation-linearisable with respect to SyncSpec: we have reached our contradiction.

Generalisations

## 6 Direct testing of synchronisation linearisation

We now consider how to test for synchronisation linearisation more directly. We perform logging precisely as for standard linearisation: a thread that performs a particular operation  $op^{i}(x)$ : (1) writes  $call.op^{i}(x)$  into the log;

- (2) performs op(x) on the synchonisation object, obtaining result y, say;
- (3) writes return.op $^{i}$ :y into the log.

In this section we consider algorithms for determining if the resulting log history is synchronisation linearisable. It turns out that the appropriate algorithm, and corresponding complexity results, differ depending upon the nature of the synchronisation object: whether synchronisations are binary, or may involve more than two threads; and whether the object is stateful or stateless.

Main ideas: general algorithm; NP-complete in general case; quadratic in stateless binary heterogeneous case; polynomial in binary homogeneous case; NP-complete for synchronisations of arity more than 2 even in stateless case.

## 6.1 The general case

I'm not sure if this matches the implemented algorithm

Suppose the specification object has non-trivial state.

I think it will be more efficient to give a more direct implementation. Define a configuration to be: (1) a point in the log reached so far; (2) the set of pending operation invocations that have not synchronised; (3) the set of pending operation invocations that have synchronised (but not returned); and (4) the state of the sequential synchronisation object. In any configuration, can: synchronise a pair of pending operations (and update the synchronisation object); advance in the log if the next event is a return that is not pending; or advance in the log if the next event is a call. Then perform DFS.

Partial order reduction: a synchronisation point must follow either the call of one of the concurrent operations, or another synchronisation point. Any synchronisation history can be transformed into this form, by moving synchronisation points earlier, but not before any of the corresponding call events, and preserving the order of synchronisations. This means that after advancing past the call of an invocation, we may synchronise that invocation, and then an arbitrary sequence of other invocations.

Alternatively, a synchronisation point must precede either the return of one of the concurrent operations, or another synchronisation point. This is more like the JIT technique in the linearisability testing paper. This means that before advancing in the log to the return of an invocation that has not synchronised, we synchronise some invocations, ending with the one in question. And we only synchronise in these circumstances.

My intuition is that the former is more efficient: in the latter, we might investigate synchronising other invocations even though the returning operation can't be synchronised with any invocation.

## 6.2 Complexity

Consider the problem of testing whether a given concurrent history has synchronisations consistent with a given sequential specification object.

We make use of a result from [?] concerning the complexity of the corresponding problem for linearizability. Let Variable be a linearizability specification object corresponding to a variable with get and set operations. Then the problem of deciding whether a given concurrent history is linearisable with respect to Variable is NP-complete.

Let ConcVariable be a concurrent object that represents a variable.

We consider concurrent synchronisation histories on an object with the following signature.

```
object VariableSync{
  def op<sub>1</sub>(op: String, x: Int): Int
  def op<sub>2</sub>(u: Unit): Unit
}
```

The intention is that  $op_1("get", x)$  acts like get(x), and  $op_1("set", x)$  acts like set(x) (but returns -1). The  $op_2$  invocations do nothing except synchronise. This can be captured formally by the following synchronisation specification object.

Let ConcVariable be a concurrent object that represents a variable. Given a concurrent history h of ConcVariable, we build a concurrent history h' of VaraibleSync as follows. We replace every call or return of get(x) by (respectively) a call or return of  $op_1("get", x)$ ; and we do similarly with sets. If there are k calls of get or set in total, we prepend k calls of  $op_2$ , and append k corresponding returns (in any order). Then it is clear that h is linearis-

able with respect to Variable if and only if h' is linearisable with respect to VariableSyncSpec.

## 6.3 The binary heterogeneous stateless case

Consider a binary synchronisation object, whose specification object is stateless. Note that in this case we do not need to worry about the order of synchronisations: if each individual synchronisation is correct, then any permutation of them will be synchronisation-linearisable.

Define two invocations to be *compatible* if they could be synchronised, i.e. they overlap and the return values agree with those for the specification object. For n invocations of each operation (so a history of length 4n), this can be calculated in  $O(n^2)$ .

Consider the bipartite graph where the two sets of nodes are invocations of  $\mathsf{op}_1$  and  $\mathsf{op}_2$ , respectively, and there is an edge between two invocations if that are compatible. A synchronisation linearisation then corresponds to a total matching of this graph: given a total matching, we build a synchronisation-compatible history of the synchronisation specification object by including events  $\mathsf{sync}^{i_1,i_2}(x_1,x_2):(y_1,y_2)$  (in an arbitrary order) whenever there is an edge between  $\mathsf{op}_1^{i_1}(x_1):y_1$  and  $\mathsf{op}_2^{i_2}(x_2):y_2$  in the matching; and conversely, each synchronisation-compatible history corresponds to a total matching.

Thus we have reduced the problem to that of deciding whether a total matching exits, for which standard algorithms exist. We use the Ford-Fulkerson method, which runs in time  $O(n^2)$ .

It is straightforward to extend this to a mix of binary and unary synchronisations, again with a stateless specification object: the invocations of unary operations can be considered in isolation.

## 6.4 The non-binary heterogeneous stateless case

It turns out that for synchronisations of arity greater than 2, the problem of deciding whether a history is synchronisation linearisable is NP-complete in general, even in the stateless case. We prove this fact by reduction from the following problem, which is known to be NP-complete ??.

**Definition 3.1** The problem of finding a complete matching in a 3-partite hypergraph is as follows: given disjoint finite sets X, Y and Z of the same cardinality, and a set  $T \subseteq X \times Y \times Z$ , find  $U \subseteq T$  such that each member of X, Y and Z is included in precisely one element of T.

Suppose we are given an instance (X, Y, Z, T) of the above problem. We construct a synchronisation specification and a corresponding history h such that h is synchronisation linearisable if and only if a complete matching exists. The synchronisations are between operations as follows:

```
\label{eq:def_op_1(x: X): Unit} \begin{aligned} & \textbf{def} \ \mathsf{op}_2(\mathsf{y} \colon \mathsf{X}) \colon \mathsf{Unit} \\ & \textbf{def} \ \mathsf{op}_3(\mathsf{z} \colon \mathsf{Z}) \colon \mathsf{Unit} \\ & \textbf{def} \ \mathsf{op}_3(\mathsf{z} \colon \mathsf{Z}) \colon \mathsf{Unit} \\ \end{aligned} \\ & \mathsf{The} \ \mathsf{synchronisations} \ \mathsf{are} \ \mathsf{specified} \ \mathsf{by} \colon \\ & \textbf{def} \ \mathsf{sync}(\mathsf{x} \colon \mathsf{X}, \ \mathsf{y} \colon \mathsf{Y}, \ \mathsf{z} \colon \mathsf{Z}) \colon (\mathsf{Unit}, \ \mathsf{Unit}, \ \mathsf{Unit}) = \{ \\ & \mathsf{require}((\mathsf{x}, \mathsf{y}, \mathsf{z}) \in T); \ ((), \ (), \ ()) \\ \} \end{aligned}
```

The history h starts with calls of  $\operatorname{op}_1(x)$  for each  $x \in X$ ,  $\operatorname{op}_2(y)$  for each  $y \in Y$ , and  $\operatorname{op}_3(z)$  for each  $z \in Z$  (in any order); and then continues with returns of the same invocations (in any order). It is clear that any synchronisation linearisation corresponds to a complete matching, i.e. the invocations that synchronise correspond to the complete matching U.

## 6.5 Binary, stateless, homogeneous

This non-necessarily corresponds to finding matching in  $O(n^{2.5})$ . bipartite graph. This be done can in https://en.wikipedia.org/wiki/Maximum\_cardinality\_matching We haven't implemented this.

## 7 Model checking for synchronisation linearisation

In this section we describe how to analyse a synchronisation object using model checking, to gain assurance that it satisfies synchronisation linearisation. We present our approach within the framework of the process algebra CSP [?] and its model checker FDR [?, ?]. We assume some familiarity with the syntax of CSP.

In particular, we use checks within the traces model of CSP. This model represents a process P by its traces, denoted traces(P), i.e. the finite sequences of visible events that P can perform. Given processes P and Q, FDR can test whether  $traces(P) \subseteq traces(Q)$ . Here P is typically a model of some system that we want to analyse, and Q is a specification process that has precisely the traces that correspond to the desired property.

Limitations of model checking.

We describe how to test for synchronisation linearisation within this framework. We start with the case of heterogeneous binary synchronisations; we describe how to generalise at the end of this section.

We build a CSP model of the synchronisation object. Such modelling of a concurrent object is well understood, so we don't elaborate in detail. Typically CSP processes representing threads perform events to read or write shared variables, acquire or release locks, etc. The shared variables, locks, etc., are also represented by CSP processes. An example for a synchronous channel can be found in [?].

We assume that the model includes the following events:

- call. t.op.x to represent thread t calling operation op with parameter x;
- return.t.op.y to represent thread t returning from operation op with result y.

We assume that all other events, describing the internal operation of the synchronisation object, are hidden, i.e. converted into internal events.

We now describe how to test whether the model satisfies synchronisation linearisation with respect to a specification object. We construct a specification process (Spec, below) that allows precisely traces of call and return events that are synchronisation linearisable. We construct this specification process from several components.

We build a process SyncSpec corresponding to the specification object. We assume this process uses events of the form  $\operatorname{sync} t_1.t_2.x_1.x_2.y_1.y_2$  to represent a synchronisation between threads  $t_1$  and  $t_2$ , calling  $\operatorname{op}_1(x_1)$  and  $\operatorname{op}_2(x_2)$ , and receiving results  $y_1$  and  $y_2$ , respectively. For example, for the synchronous channel, we would have

```
SyncSpec = sync?t1?t2?x?u!u!x \rightarrow SyncSpec
```

If the synchronisation object or specification object has unbounded state, we have no chance of modelling it using finite-state model checking. However, we can often build approximations. For example, we could approximate (in an informal sense) the synchronous channel with sequence counter by one where the sequence counter is stored mod 5. Then the specification object can be modelled by

```
SyncSpec = SyncSpec'(1) \\ SyncSpec'(ctr) = sync?t1?t2?x?u!ctr!(x,ctr) \rightarrow SyncSpec'((ctr+1)\%5)
```

We then build a *lineariser* process for each thread as follows.

```
Lineariser (t) = call .t.op<sub>1</sub>?x<sub>1</sub> \rightarrow sync.t?t<sub>2</sub>!x<sub>1</sub>?x<sub>2</sub>?y<sub>1</sub>?y<sub>2</sub> \rightarrow return.t.op<sub>1</sub>.y<sub>1</sub> \rightarrow Lineariser(t) \Box call .t.op<sub>2</sub>?x<sub>2</sub> \rightarrow sync?t<sub>1</sub>!t?x<sub>1</sub>!x<sub>2</sub>?y<sub>1</sub>?y<sub>2</sub> \rightarrow return.t.op<sub>2</sub>.y<sub>2</sub> \rightarrow Lineariser(t) alpha(t) = {| call .t, return .t, sync.t.t<sub>1</sub>, sync.t<sub>1</sub>.t | t<sub>1</sub> \leftarrow ThreadID, t<sub>1</sub> \neq t |}
```

This process ensures that between each call and return event of t, there is a corresponding sync event.

We then combine together the specification process with the linearisers, synchronising on shared events: this means that each  $sync.t_1.t_2$  event will be a three-way synchronisation between SyncSpec, Lineariser( $t_1$ ) and Lineariser( $t_2$ ).

```
\mathsf{Spec}_0 = \mathsf{SyncSpec} \; [|\; \{|\; \mathsf{sync}\; \}\; |] \; (||\; \mathsf{t} \leftarrow \mathsf{ThreadID} \bullet [\mathsf{alpha}(\mathsf{t})] \; \; \mathsf{Lineariser} \; (\mathsf{t}))
```

Every trace of  $Spec_0$  represents an interleaving between a possible history of the concurrent object (call and return events) and a compatible legal history of the specification object (sync events).

Finally, we hide the sync events.

```
Spec = Spec_0 \setminus \{ | sync | \}
```

Each trace of the resulting process represents a history for which there is a compatible legal history of the specification object; i.e. it has precisely the traces that correspond to histories that are synchronisation linearisable. It is therefore enough to test whether the traces of the model of the synchronisation object are a subset of the traces of Spec this can be discharged using FDR.

We now generalise this approach. For a synchronisation involving k threads, the corresponding sync event contains k thread identities, k parameters, and k return values; each such event will be a synchronisation (in the CSP specification) between k linearisers and the specification process.

For homogeneous synchronisations the identities of the threads (and corresponding parameters and return values) may appear in either order within the sync events. The following definition of the lineariser allows this (for k=2).

```
Lineariser (t) = 

let others = ThreadID-\{t\} within 

call .t.op?x \rightarrow ( 

sync.t?t':others !x?x'?y?y' \rightarrow return.t.op.y \rightarrow Lineariser(t) 

\Box 

sync?t':others !t?x'!x?y'!y \rightarrow return.t.op.y \rightarrow Lineariser(t) )
```

Finally, for synchronisation objects with multiple synchronisation modes, the specification process should have a different branch (with different sync events) for each mode.

## 7.1 Progress conditions

A simple adaptation of the above check allows us to capture an interesting progress condition, which we now describe. We make the assumption that the scheduler in the implementation schedules each operation infinitely often. This is different from the assumption corresponding to the standard property of lock freedom [?], which allows threads to be suspended forever; however, it is consistent with how real schedulers behave. Under this assumption, we require that if a synchronisation is possible, such a synchronisation can happen, and the relevant threads are able to return: in other words, the return events become available.

Part of our progress check is that the model of the system is divergencefree, which can be tested by FDR. Recall, that a divergence (in CSP) is an infinite sequence of consecutive internal events. In the case of the model of a synchronisation object, this would represent a livelock, i.e. where one or more threads perform infinitely many steps without reaching a point where they can return. The check forbids such livelocks.

The other part of our progress check concerns stable failures. Recall that a stable failure of a process is a pair (tr, X) representing that the process can perform trace tr to reach a stable state (i.e. where no internal event is possible), where no event from X can be performed. We test whether the stable failures of the model of the synchronisation object are a subset of the stable failures of the above Spec process. We explain the property this test captures via examples.

Consider a model of the synchronous channel, and the trace  $\langle \mathsf{call.t_1.send.4}, \, \mathsf{call.t_2.receive.unit} \rangle$ . After this trace, Spec (internally) performs  $\mathsf{sync.t_1.t_2.4.unit.unit.4}$ , and reaches a state where both return.t\_1.unit and return.t\_2.4 are available. The test of the previous paragraph requires that both of these events are also available in the model of the system, i.e. both threads are able to return.

In some cases, it might be nondeterministic which synchronisation, out of two or more possibilities, occurs. For example, consider the synchronous channel, again, and the trace  $\langle call.t_1.send.4, call.t_2.send.5, call.t_3.receive.unit \rangle$ . After this trace, Spec may nondeterministically perform either sync.t<sub>1</sub>.t<sub>3</sub>. 4.unit.unit.4 or sync.t<sub>2</sub>.t<sub>3</sub>.5.unit.unit.5. Subsequently, either return.t<sub>1</sub>.unit and return.t<sub>3</sub>.4 or, respectively, return.t<sub>2</sub>.unit and return.t<sub>3</sub>.5 are available. The check ensures that in each case t<sub>3</sub> can return, and that either t<sub>1</sub> or t<sub>2</sub> can return

(with  $t_3$  returning the corresponding value).

## 7.2 Alternative approach

The approach described above, using lineariser processes to ensure that the sync events are between the relevant call and return events, can be expensive. However, we can do better in some cases.

By way of an analogy, testing a concurrent datatype for (standard) linearisation is often easier when one can identify explicit linearisation points: the specification can be written in terms of those linearisation points. We use a similar technique with synchronisation linearisation.

Suppose we are considering a binary synchronisation object involving operations  $op_1$  and  $op_2$ . Our approach requires the analyst to identify points  $p_1$  and  $p'_1$  within  $op_1$ , and a point  $p_2$  within  $op_2$ , which we call *signal points*. These signal points must satisfy the following conditions (which the test below verifies):

- 1. When particular invocations of  $op_1$  and  $op_2$  synchronise, point  $p_1$  is reached before point  $p_2$ , and point  $p_2$  is reached before point  $p'_1$  (for the corresponding signal points);
- 2. The return values of the two invocations are available at points  $p'_1$  and  $p_2$ , respectively;
- 3. No other invocation reaches a signal point between points  $p_1$  and  $p'_1$ .

Typically,  $p_1$  will be at or before  $op_1$  signals to  $op_2$ ;  $p_2$  will be at or after  $op_2$  receives that signal, and at or before it signals back to  $op_1$ ; and  $p'_1$  will be at or after  $op_1$  receives that signal back. Figure 5 gives an example.

Note that condition 1 and the fact that the signal points occur within the corresponding invocations imply that  $p_2$  occurs within *both* invocations. Thus we can use  $p_2$  as the synchronisation point.

We augment the CSP models of the threads with the following events:

- signal<sub>1</sub>.t<sub>1</sub>.x<sub>1</sub> performed by thread t<sub>1</sub> at point  $p_1$ , where x<sub>1</sub> is its parameter;
- signal<sub>2</sub>.t<sub>2</sub>.x<sub>2</sub>.y<sub>2</sub> performed by thread t<sub>2</sub> at point  $p_2$ , where x<sub>2</sub> is its parameter and y<sub>2</sub> is its return value;
- signal'<sub>1</sub>.t<sub>1</sub>.y<sub>1</sub> performed by thread t<sub>1</sub> at point  $p'_1$ , where y<sub>1</sub> is its return value.

Figure 5: An implementation of a synchronous channel, using semaphores. Signal points are indicated by comments.

Note that the events representing the calls and returns of operations are no longer necessary.

We can then test whether the model of the synchronisation object refines the following specification (with a suitable initial state).

```
SyncSpec(state) = signal<sub>1</sub>?t<sub>1</sub>?x<sub>1</sub> \rightarrow signal<sub>2</sub>?t<sub>2</sub>?x<sub>2</sub>!f<sub>2</sub>(state,x<sub>1</sub>,x<sub>2</sub>) \rightarrow signal'<sub>1</sub>.t<sub>1</sub>! f<sub>1</sub>(state,x<sub>1</sub>,x<sub>2</sub>) \rightarrow SyncSpec(update(state,x<sub>1</sub>,x<sub>2</sub>))
```

where  $f_1$  and  $f_2$  give the expected return values for the two invocations, and update describes how the state is updated. The specification ensures that the above condition 1 is satisfied. Hence, as described above, this ensures that the synchronisations can be linearised in the order of the corresponding signal<sub>2</sub> events.

The above condition 2 is necessary to ensure the return values can be included in the signal events. If this is not true of  $op_2$ , we could arrange for the CSP model of this operation to perform a later signal, on channel  $signal_2'$  with that value, and to use the following specification:

```
SyncSpec(state) = signal<sub>1</sub>?t_1?t_1?x_1 \rightarrow signal_2?t_2?x_2 \rightarrow
```

$$\mathsf{signal}_1' \mathsf{.t}_1 ! \mathsf{f}_1(\mathsf{state}\,,\mathsf{x}_1,\mathsf{x}_2) \to \mathsf{signal}_2' \mathsf{.t}_2 ! \mathsf{f}_2(\mathsf{state}\,,\mathsf{x}_1,\mathsf{x}_2) \to \mathsf{SyncSpec}(\mathsf{update}(\mathsf{state},\mathsf{x}_1,\mathsf{x}_2))$$

Condition 3 is necessary to avoid false positives with the above specification process. The specification process handles the signals for a single synchronisation at a time.

This approach can be extended to a synchronisation between k>2 operations, with signal points occurring in the order

$$p_1, p_2, \dots p_{k-1}, p_k, p'_{k-1}, p'_{k-2}, \dots p'_1,$$

(where the subscripts correspond to the indices of the operations), or may be some other permutation of the  $p_i'$  events.