

74 LS/HC 00	Quad 2-input NAND gate
74 LS/HC 02	Quad 2-input NOR gate
74 LS/HC 04	Hex inverter
74 LS/HC 08	Quad 2-input AND gate
74 LS/HC 10	Triple 3-input NAND gate
74 LS/HC 11	Triple 3-input AND gate
74 LS/HC 12	Triple 3-input NAND gate OCT
74 LS/HC 20	Dual 4-input NAND gate
74 LS/HC 27	Triple 3-input NOR gate
74 LS/HC 32	Quad 2-input OR gate
74 LS/HC 40	DUAL 4-INPUT POSITIVE-NAND BUFFERS
74 LS/HC 47	BCD to 7-Segment Decoder /Driver with Open-Collector Outputs
74 LS/HC 48	BCD TO 7-SEGMENT DECODER
74 LS/HC 74	ON Semiconductor - Dual D Flip-Flop
74 LS/HC 138	3-to-8 line decoder/demultiplexer
74 LS/HC 163	NXP Semiconductors - Presettable synchronous 4-bit binary counter
74 LS/HC 244	Octal buffer/line driver; 3-state