

# 王烁宇 WANG SHUOYU

✉ 2018303330@mail.nwpu.edu.cn · ☎ (+86) 135 8702 3317 · 🌐 Gaw4Gura

## 🎓 EDUCATION BACKGROUND

**Northwestern Polytechnical University, Xi'an** 08/2018 – Present

*National Demonstrative School of Microelectronics*, Microelectronics Science and Engineering

**Areas of Courses:** Analog Electronic Technology, Digital Circuit and Verilog HDL, Signal and System, Digital Signal Process, Digital Integrated Circuit Design, Analog IC Design, Semiconductor Device

**Overall Result:** 83.2/100.0

## 👥 PROGRAMS

### Practice of Analog Integrated Circuits Design & Experiment of Digital Integrated Circuit Design

Fall Term 2020 & Spring Term 2021

*Cadence Virtuoso, Linux*

Build basic CMOS digital and analog circuits in Cadence, get the simulation data to see if the circuits meet the expectation (for example, 80dB DC gain for an amplifier).

- **Digital Part:** Inverter, Combinational Logic Design, Dynamic Combinational Logic Design, Temporal Logic Design, Adder
- **Analog Part:** different designs of OPAMP(Cascade, Cascode and so on), Comparator

### SoC Design Practice

Spring Term 2021

*Synopsys DC & ICC, Linux*

Realize a Sequential Detector by Automaton, write I/O constraints, timing constraints and physical constraints for an Inter-Integrated Circuit ( $I^2C$ ).

- **Part I:** Design an **Mealy automaton** to detect sequence **10100**, write Verilog code, and compile the project to get schematic diagram in **dc\_shell**.
- **Part II:** Write timing and physical constraints for an open source  $I^2C$  design according to the manual, use **dc\_shell** to compile the design, and assign I/O pads for every input and output, use **icc\_shell** to get the layout.

## 🔧 SKILLS

- **Language Skills:** CET-6 542, IELTS 6.5
- **Software:** C/C++ > Python = Verilog HDL > MATLAB & SIMULINK (currently learning)
- **Hardware:** Arduino UNO, 8051 Microcontroller
- **Platforms:** Windows, Linux, Mac OS

## 🏹 EXTRACURRICULAR EXPERIENCES

*Judge Panel*, The 2019 ICPC Asia-East Continent Final Xi'an

12/2019

- Monitor working conditions of computers, ensuring no one was cheating.

*Defending Middle Fielder*, quarter final, College Football Match

12/2018

*Deputy Minister*, Science and Technology Innovation Department of Tencent Club of NWPU 9/2019 – 9/2020

## 💎 AWARDS

*Silver Medal*, The 2019 ICPC China Shaanxi Provincial Programming Contest

06/2019

*Bronze Award*, Group Programming Ladder Tournament 2019

03/2019