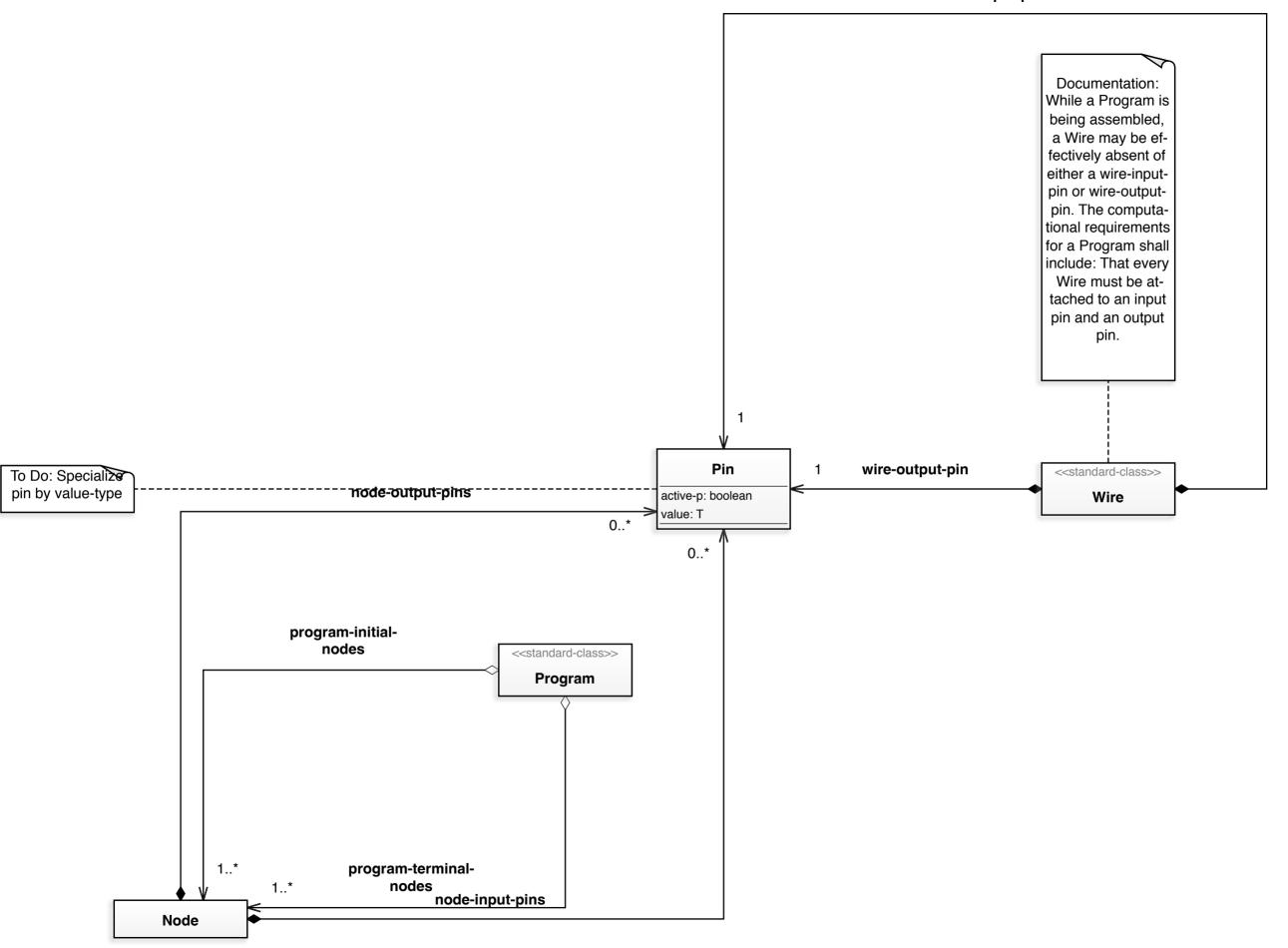
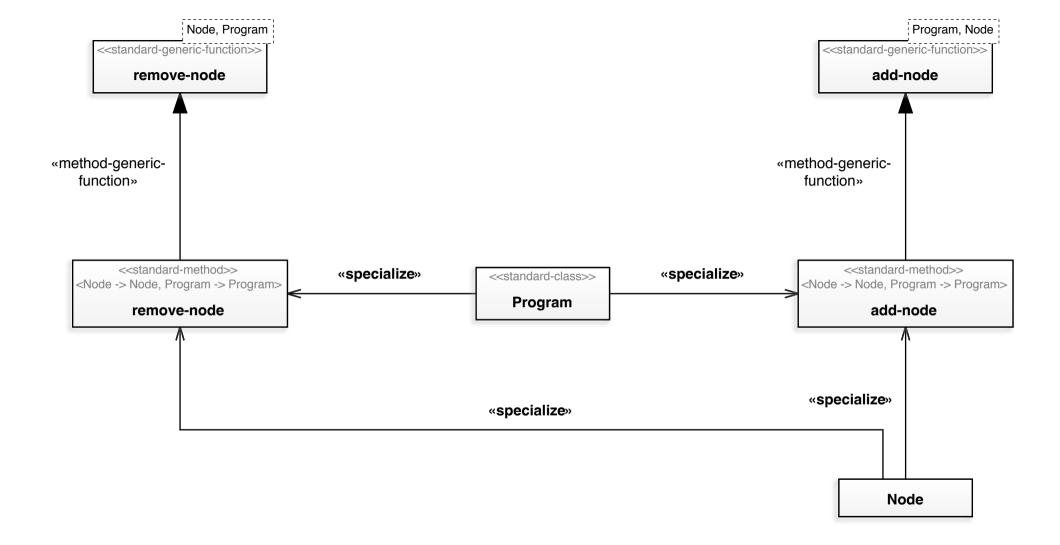
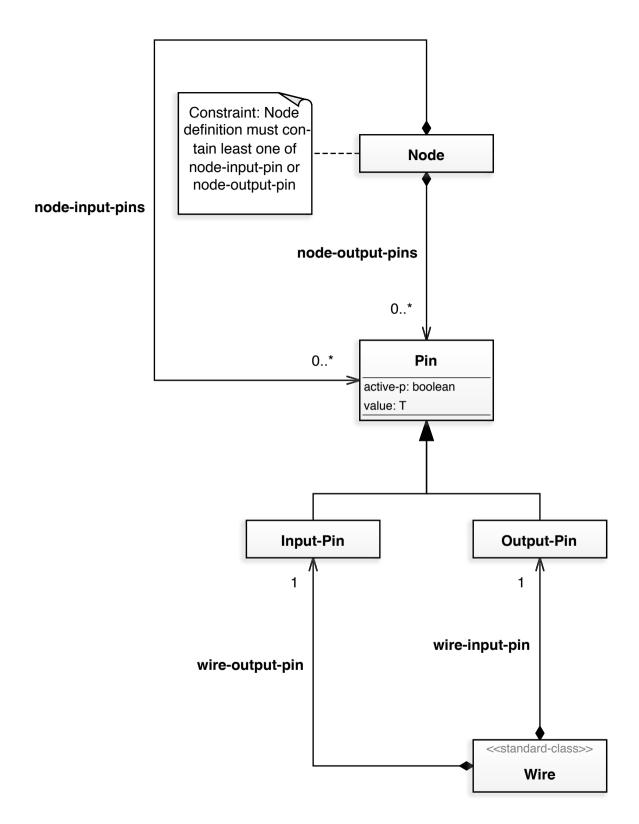
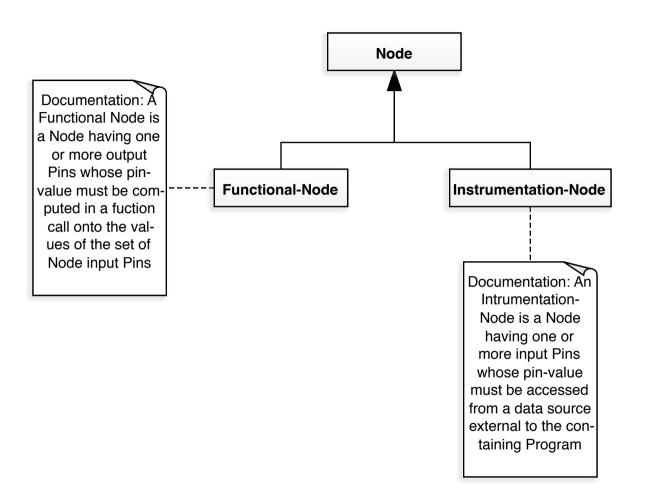
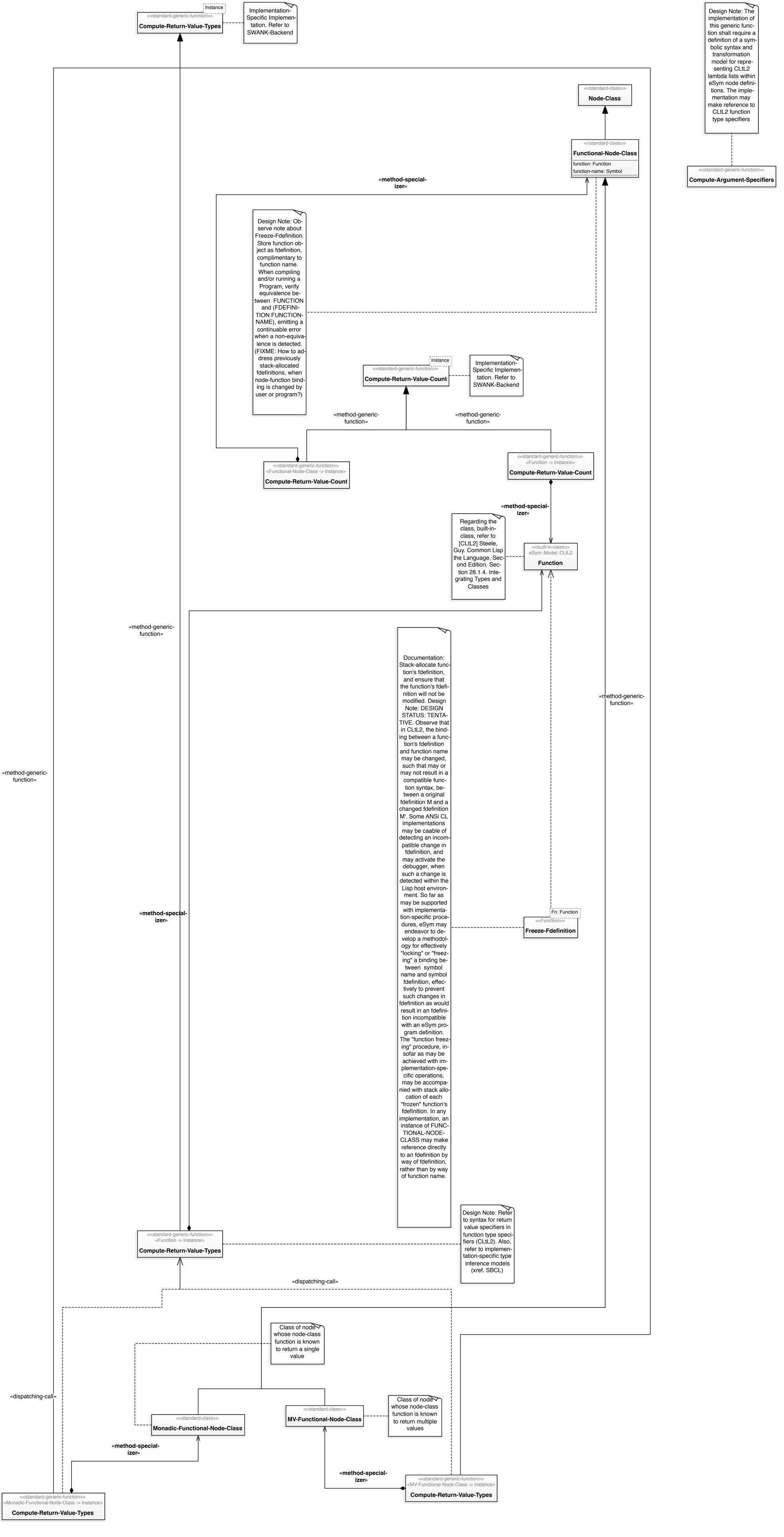
wire-input-pin

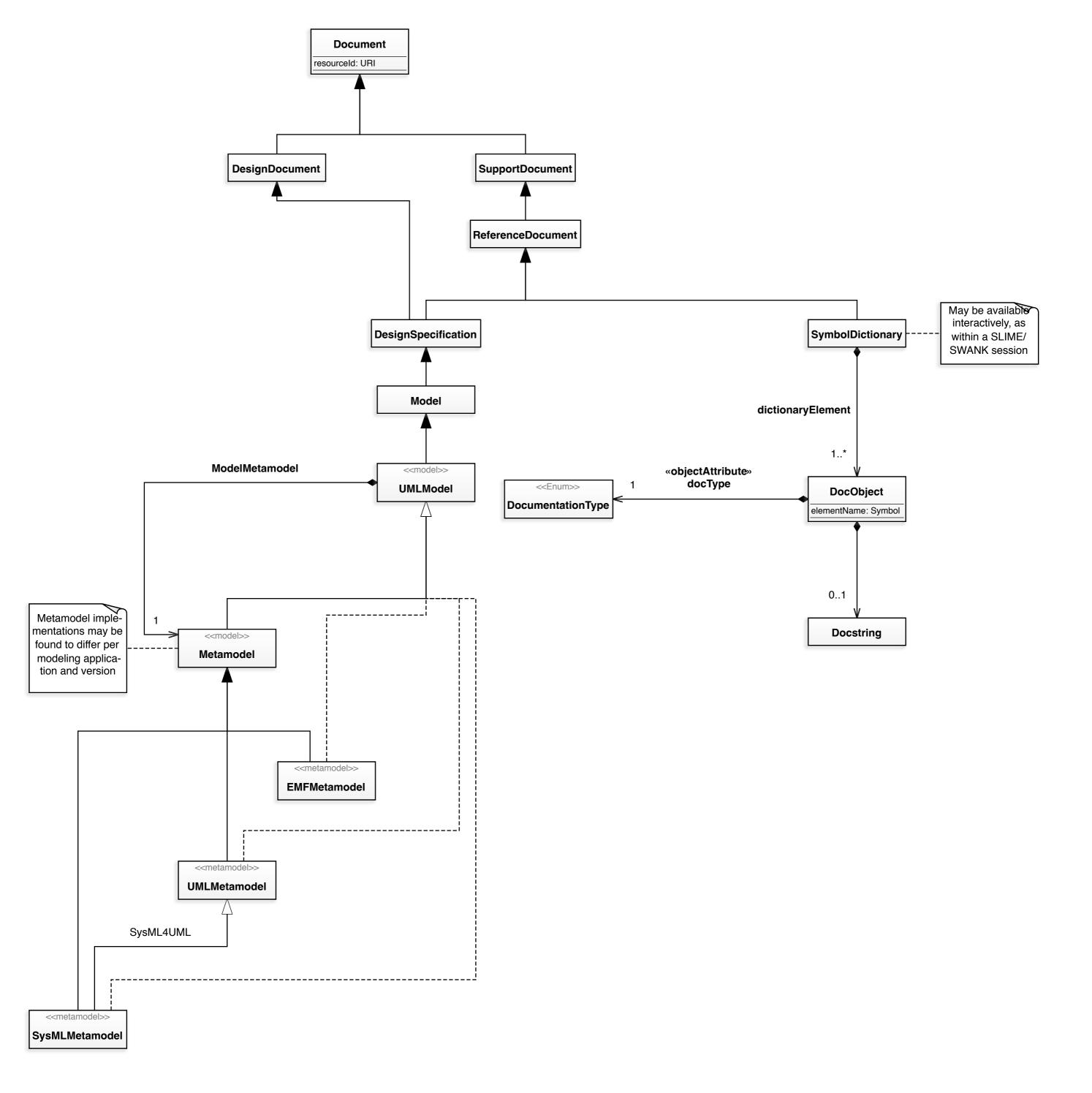












eSym_Model_1.0 Use Case