| Name               | Level of contribution | Responsibilities            |
|--------------------|-----------------------|-----------------------------|
| Ge Jintian 葛劲天(Me) | 5                     | Implement test bench        |
|                    |                       | Debug 1a, 2a, 2b            |
|                    |                       | Rewrite 1b                  |
| Gu Fenglei 顾峰雷     | 5                     | Implement 2a, 2b            |
| Ho Heiieng 何禧瑩     | 4                     | Finish Report, implement 1a |