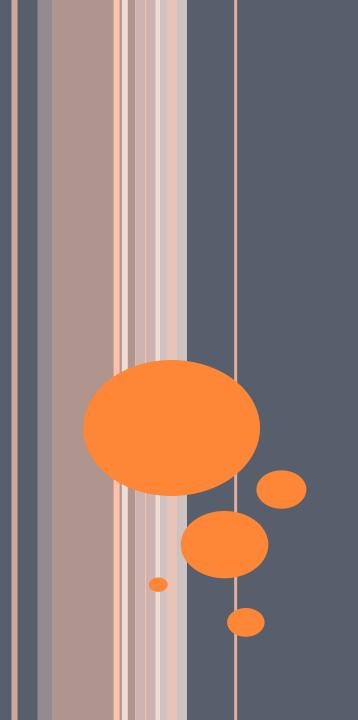
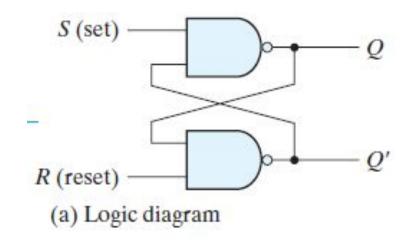
CSE 201: DIGITAL LOGIC DESIGN D,JK,T FLIP FLOP

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CSE, MIST



D FLIP-FLOP

SR LATCH WITH NAND GATE

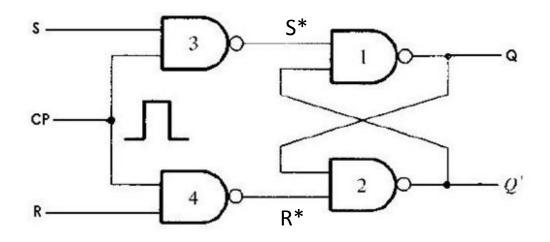


	Q'	Q	R	S
	1	0	0	1
Memory	1	0	1	1
	0	1	1	0
Memory	0	1	1	1
	alid	Inv	0	0

S	R	Q	Q'
0	0	Invalid	
0	1	1	0
1	0	0	1
1	1	Memory	

LIMITATIONS IN SR FLIP FLOP

1. It has an invalid input combination. Circuit gives unexpected outcome if such combination occurs. In D flip-flop this combination is eliminated.



S*	R*	Q	Q'
0	0	Invalid	
0	1	1	0
1	0	0	1
1	1	Memory	

СР	S	R	Q	Q'
0	X	x	Memory	
1	0	0	Memory	
1	0	1	0	1
1	1	0	1	0
1	1	1	Invalid	

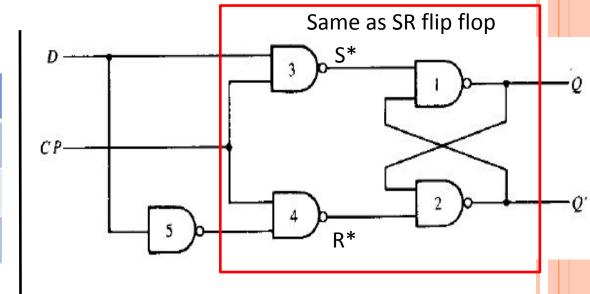
D (DATA) FLIP FLOP

Truth Table of SR flip flop

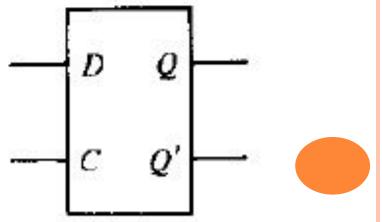
СР	S	R	Q _{t+1}
0	Х	X	Q_t
1	0	0	Q_t
1	0	1	0
1	1	0	1
1	1	1	Invalid

Truth Table of D flip flop

CLK	D	Q _{t+1}
0	X	Q _t
1	0	0
1	1	1



Graphic symbol of SR flip flop



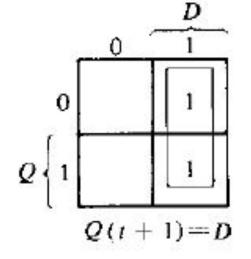
D FLIP FLOP

Truth Table of D flip flop

CLK	D	Q _{t+1}
0	X	Q _t
1	0	0
1	1	1

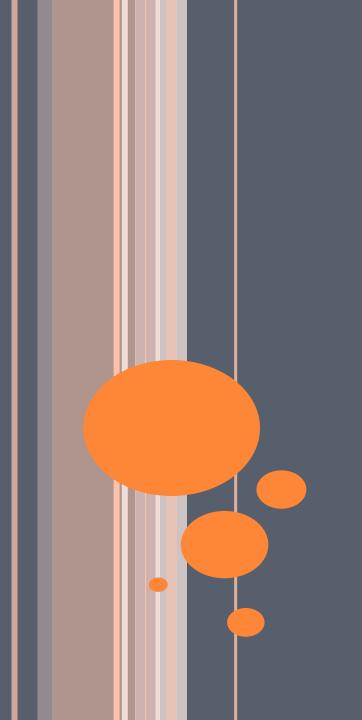
Characteristics Table of D flip flop

Q_t	D	Q_{t+1}
0	0	0
0	1	1
1	0	0
1	1	1



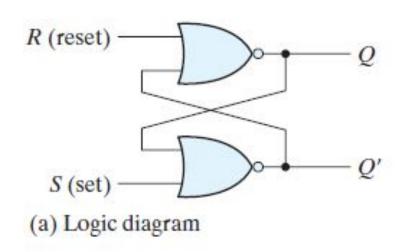
Excitation Table of D flip flop

Q _t	Q _{t+1}	D
0	0	0
0	1	1
1	0	0
1	1	1



JK FLIP-FLOP

SR LATCH WITH NOR GATE

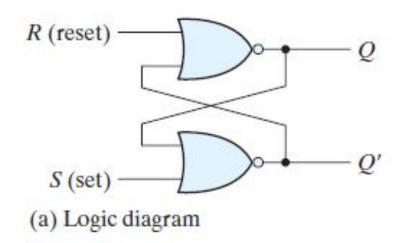


S	R	Q	Q'	
1	0	1	0	
0	0	1	0	Memory
0	1	0	1	
0	0	0	1	Memory
1	1	Forbidden		

Truth Table of NOR gate

Α	В	F
0	0	1
0	1	0
1	0	0
1	1	0

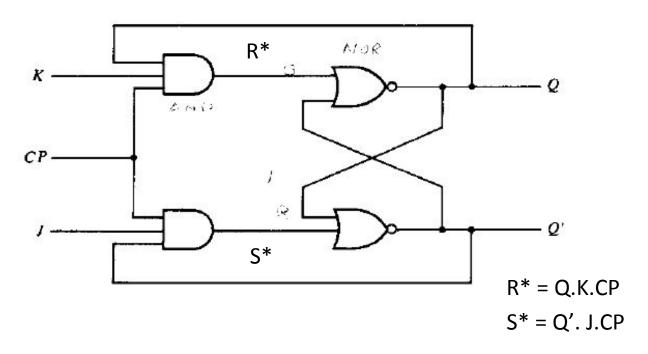
SR LATCH



S	R	Q	Q'	
1	0	1	0	
0	0	1	0	Memory
0	1	0	1	
0	0	0	1	Memory
1	1	Forbidden		

S	R	Q	Q'
0	0	Men	nory
0	1	0	1
1	0	1	0
1	1	Forbidden	

JK FLIP FLOP



S*	R*	Q	Q'
0	0	Men	nory
0	1	0	1
1	0	1	0
1	1	Forbidden	

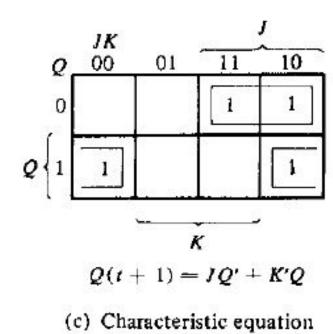
Characteristics Table of JK flip flop

Q	J	K	Q(t+1)
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	0

JK FLIP FLOP

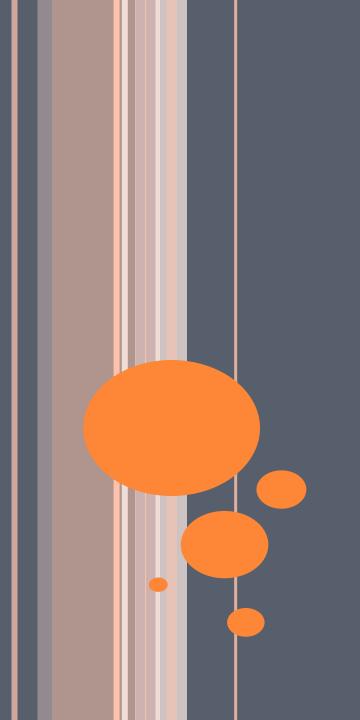
Characteristics Table of JK flip flop

Q	J	K	Q(t+1)
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	0



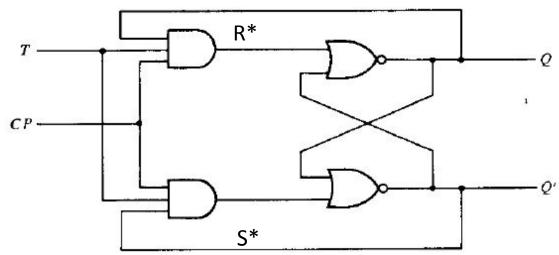
Excitation Table of JK flip flop

Q _t	Q _{t+1}	J	K
0	0	0	X
0	1	1	Χ
1	0	X	1
1	1	Х	0



T FLIP-FLOP

T FLIP FLOP



$S^* = Q'.T.CP$)
$R^* = Q. T.CP$)

S*	R*	Q	Q'
0	0	Men	nory
0	1	0	1
1	0	1	0
1	1	Forbi	dden

$$Q = \begin{cases} 0 & 1 \\ 0 & 1 \end{cases}$$

$$Q(t+1) = TQ' + T'Q$$
(c) Characteristic equation

Characteristics Table of JK flip flop

Q	Т	Q(t+1)
0	0	0
0	1	1
1	0	1
1	1	0

Excitation Table of D flip flop

\mathbf{Q}_{t}	Q_{t+1}	D
0	0	0
0	1	1
1	0	1
1	1	0