

Compiler Design

Today's Class Topics

Bottom Up Parsing

- Handle and Viable Prefixes ✓
- Operator precedence parsing



Compiler Design

Handle Pruning: Reducing the string to start symbol using the handles

Handle: A Handle is a substring of a string that matched with any of the right side of the productions then That handle will be reduced with left side of the production.

Bottom-up parsing is a process of finding the handles And using them in the reductions to get the start symbol, This entire process of reducing the string to the start symbol is called as handle pruning

Viable prefix: viable prefixes are the set of prefix of the right & leftmost form that can appear on the stack of a Shift Reduce Parse. i.e

viable prefix are the prefixes of Right & leftmost form that do not extend beyond the end of the handle

let β \in Γ^* is right & leftmost form, where β is handle then viable prefixes are any prefix of $\phi\beta$ except ϵ

NOTE:- The set of all viable prefixes of a CFL is a Regular language i.e viable prefix can be recognise by Finite Automate

Compiler Design

Question:- Consider the following grammar and Parse the input string aab using Shift Reduce Parser

G₁ = { S → AB, A → aA | a, B → bB | b }

$\begin{array}{l} S \xrightarrow{\quad} \overline{AB} \quad B \xrightarrow{\quad} \underline{b}, \quad A \xrightarrow{\quad} \overline{AA} \quad A \xrightarrow{\quad} \underline{a} \\ S \xrightarrow{\quad} \underline{A} \underline{B} \Rightarrow A b \xrightarrow{\Phi\beta} \underline{a} \underline{A} \underline{b} \xrightarrow{\Phi\beta} \underline{aab} \end{array}$

$\Phi\beta$ +

Vicile Prefix	
aa	a, aa
aA	a, aA
Ab	A, Ab
AB	A, AB

⇒

Vicile prefix = { a, aA, aA, A, Ab, AB }

Compiler Design

Question:- Consider the following grammar and Parse the input string aab using Shift Reduce Parser

$$G_1 = \{ S \rightarrow AB, A \rightarrow aA \mid a, B \rightarrow bB \mid b \}$$

Bottomup Parsing

Stack	Input Buffer	Actions
\$	aab \$	Shift
\$ a	ab \$	Shift
\$ aa	b \$	Reduce by $A \rightarrow a$
\$ aA	b \$	Reduce by $A \rightarrow a$
\$ A	b \$	Shift
\$ Ab	\$	Reduce $B \rightarrow b$
\$ AB	\$	Reduce $S \rightarrow AB$
\$ S	\$	Accepted.

Compiler Design

Ques: Consider the following grammar, which of the following are viable prefixes?

$$G = \{ S \xrightarrow{\cdot} AB, A \xrightarrow{\cdot} aA(a), B \xrightarrow{\cdot} bB(b) \}$$

- 1) aa 2) Aa 3) bb 4) bB 5) aB 6) AbB 7) ABb 8) AbbB

1) aa
viable prefix

	Stack	I/p Buffer	Action
	\$	\$	
aa	\$ aa	\$	
AA	\$ AA	\$	
A	\$ A	\$	
Ab	\$ Ab	\$	
AB	\$ AB	\$	
S	\$ S	\$	

A \rightarrow a ✓

A \rightarrow aA

Shift

Reduce B \rightarrow b

Reduce S \rightarrow AB

	Stack	I/p Buffer	Action
	\$	\$	
AA	\$ AA	\$	
AA	\$ AA	\$	

Reduce A \rightarrow a

Cannot reduce to S
 \therefore it is not viable prefix

Compiler Design

$$G = \{ S \rightarrow AB, A \rightarrow aA|a, B \rightarrow bB|b \}$$

3) Checking for bb

Stack	I/P Buffer	Action
\$		\$
\$ -		
\$ b b	\$	$B \rightarrow b$
\$ b B	\$	$B \rightarrow bB$
\$ B		

Cannot Reduce to \$
 \therefore it is not viable prefix

4) ab

Stack	I/P Buffer	Action
\$		\$
\$ -		
\$ a B		$A \rightarrow a$
\$ A B		

~~\$ A B~~
not possible

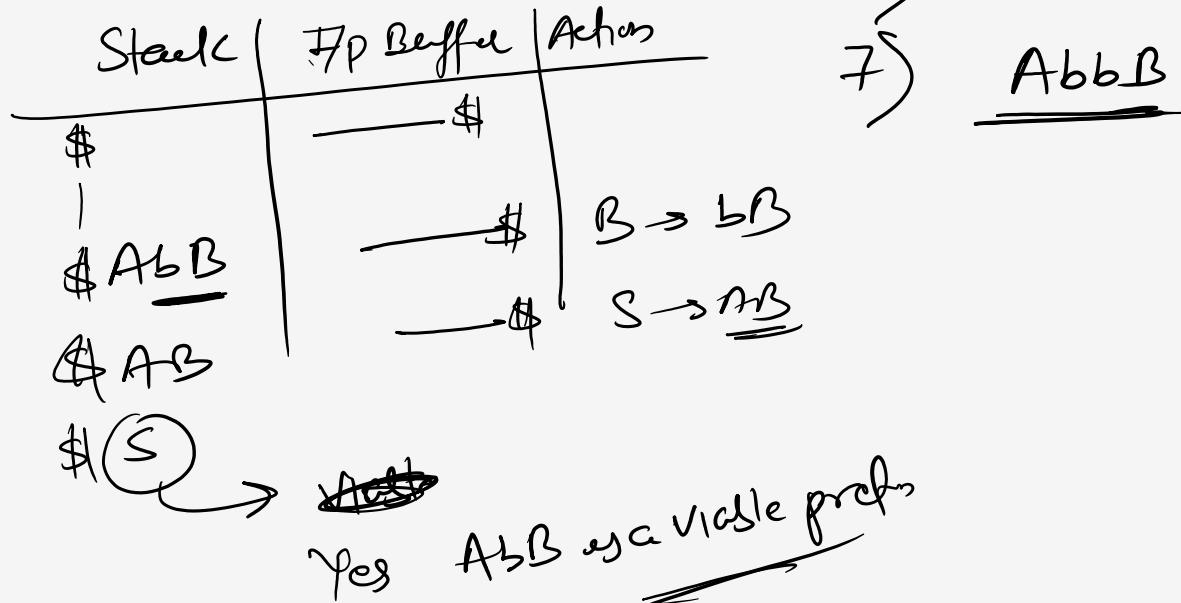
ab not viable prefix



Compiler Design

$$G = \{ S \rightarrow AB, A \rightarrow aA | a, B \rightarrow bB | b \}$$

5) AbB ✓



Compiler Design

$S \rightarrow AB, A \rightarrow aA|a, B \rightarrow bB|L$

8)

aaaaA

Stack	IP Buffer	Action
\$	\$	
\$ aaaaA	\$	$A \rightarrow aA$
\$ aaaA	\$	$A \rightarrow cA$
\$ aaA	\$	$A \rightarrow gA$
\$ CA	\$	$A \rightarrow gA$
\$ A	b \$	Shift
\$ Ab	\$	$B \rightarrow L$
\$ Ab	\$	$S \rightarrow AB$
\$		

Stack	IP Buffer	Action
\$	aaaaab	$B \rightarrow L$
\$ aaaaAb	\$	

Cannot Reduce to S

∴ it is not viable prefix

∴ aaaaA
is a viable
prefix

Compiler Design



Compiler Design



Compiler Design



Compiler Design



Compiler Design



Compiler Design

Answers

Question:- $G = \{ S \rightarrow CC, C \rightarrow aC/b \}$ viable prefixes?

- a) ~~Cb~~
- b) ~~aC~~
- c) ~~aaC~~
- d) ~~Ca~~ ✓
- e) ~~abb~~

Stack	I/P Buffer	Action
\$	\$	
\$ aC	\$	Shift
\$ C	b \$	Shift
\$ Cb	\$	Shift
\$ CC	S	Reduce $C \rightarrow b$
\$ (S)		

QC ✓

Stack	I/P Buffer	Action
\$ Ca	\$	
\$ Ca	b \$	Shift
\$ Cab	\$	Reduce $C \rightarrow b$
\$ CaC	\$	Reduce $C \rightarrow aC$
\$ CC	\$	Reduce $S \rightarrow CC$

\$ (S) ↗ Ca is also viable prefix.

$\$ a\$$ | — \$ | $C \rightarrow b$
 $\$ a\$$ | — \$ | $C \rightarrow b$
 Never get startsymbol \$
 ∵ it is not viable prefix

Compiler Design



Compiler Design



Compiler Design



Compiler Design



Compiler Design

Operator Precedence Parsing:

- Applicable only for operator grammar
- It parses Ambiguous Grammar also by using Precedence & Associativity Rules

Operator Grammars:

- i) NO ϵ -production X
- ii) NO consecutive non Terminals X

Ex: $E \rightarrow EAE \mid id$ } not operator Grammar
 $A \rightarrow + \mid *$

↓ Convert to Operator Grammar

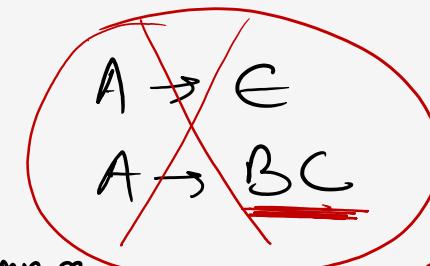
~~$E \rightarrow E+E \mid E+E \mid id$~~ } Operator Grammar
 $A \rightarrow + \mid *$

$$a > b$$

$$a < b$$

$$a = b$$

Precedence & Associativity Rules



$$a > b$$

$$a < b$$

$$a = b$$

~~$A \rightarrow AB$~~

Compiler Design

Operator Precedence Parsing Table :-

$$G: \{ E \rightarrow E+E \mid E \cdot E \mid id \}$$

$\downarrow id_1$ $\downarrow id_2$
ab

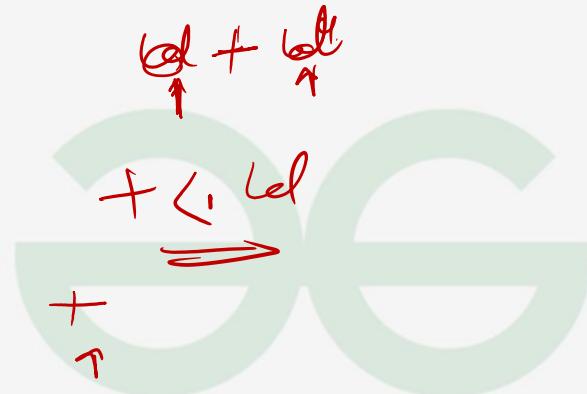
Apply Correct precedence
and Associativity Rule

	id	+	*	\$
id	-	>	>	>
+	<	>	<	>
*	<	>	>	>
\$	<	<	<	-

end marker

end marker →

led · led



Ambiguous Grammar

$$G: \{ E \rightarrow E+E / E*E / \text{id} \}$$

How to Parse String $\text{id} + \text{id} * \text{id}$:

Let the topmost symbol in the Stack is x and the present symbol in the Input Buffer is y , then proceed as follows

1) if $x < y$ Then Shift y into the Stack

2) if $x > y$ Then there will be a handle $E+E * id$ in the Stack Then perform a reduce operation with an appropriate production

Compiler Design

Operator Precedence Table

	id	+	*	\$
id	-	>	>	>
+	<	>	<	>
*	<	>	>	>
\$	<	<	<	-

Size = $O(n^2)$

Where n is the No of operators

Stack	Input Buffer	Action
\$	<	Shift
\$ id	>	Reduce $E \rightarrow id$
\$ E	<	Shift
\$ E +	<	Shift
\$ E + id	>	Reduce $E \rightarrow id$
\$ E + E	<	Shift
\$ E + E *	<	Shift
\$ E + E * id	>	Reduce $E \rightarrow id$
\$ E + E * E	>	Reduce $E \rightarrow E * E$
\$ E + E	>	Reduce $E \rightarrow E + E$
\$ E	>	Accept

NOTE: Non Terminal have least precedence but greater than \$

100×100

$10^8 \times 10^8$

Compiler Design

Size = $O(n^2)$

Operator function Table:-

	id	+	*	\$
f	-	>	(\triangleright)	>
g	<	(\triangleright)	(\triangleleft)	>
*	<	>	>	>
\$	<	<	<	-

$f(id) \rightarrow g(+)$

$f(+) \leftarrow g(id)$

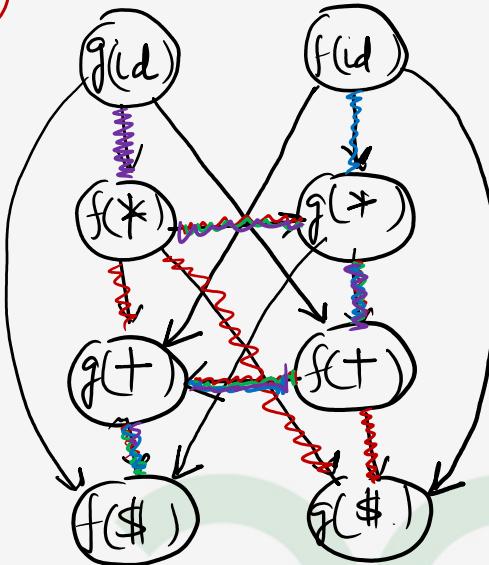
$f(id) \rightarrow g(id)$
 $f(id) \rightarrow g(*)$

$f(id) \rightarrow g(*)$
 $f(+) \leftarrow g(id)$

operator functionTable

	+	*	id	\$
f	(g)	(4)	(4)	(0)
g	(1)	(3)	(5)	(0)

Size of Table = $O(2 \times n) = O(n)$



No operators

Compiler Design



Compiler Design



Thank You !

