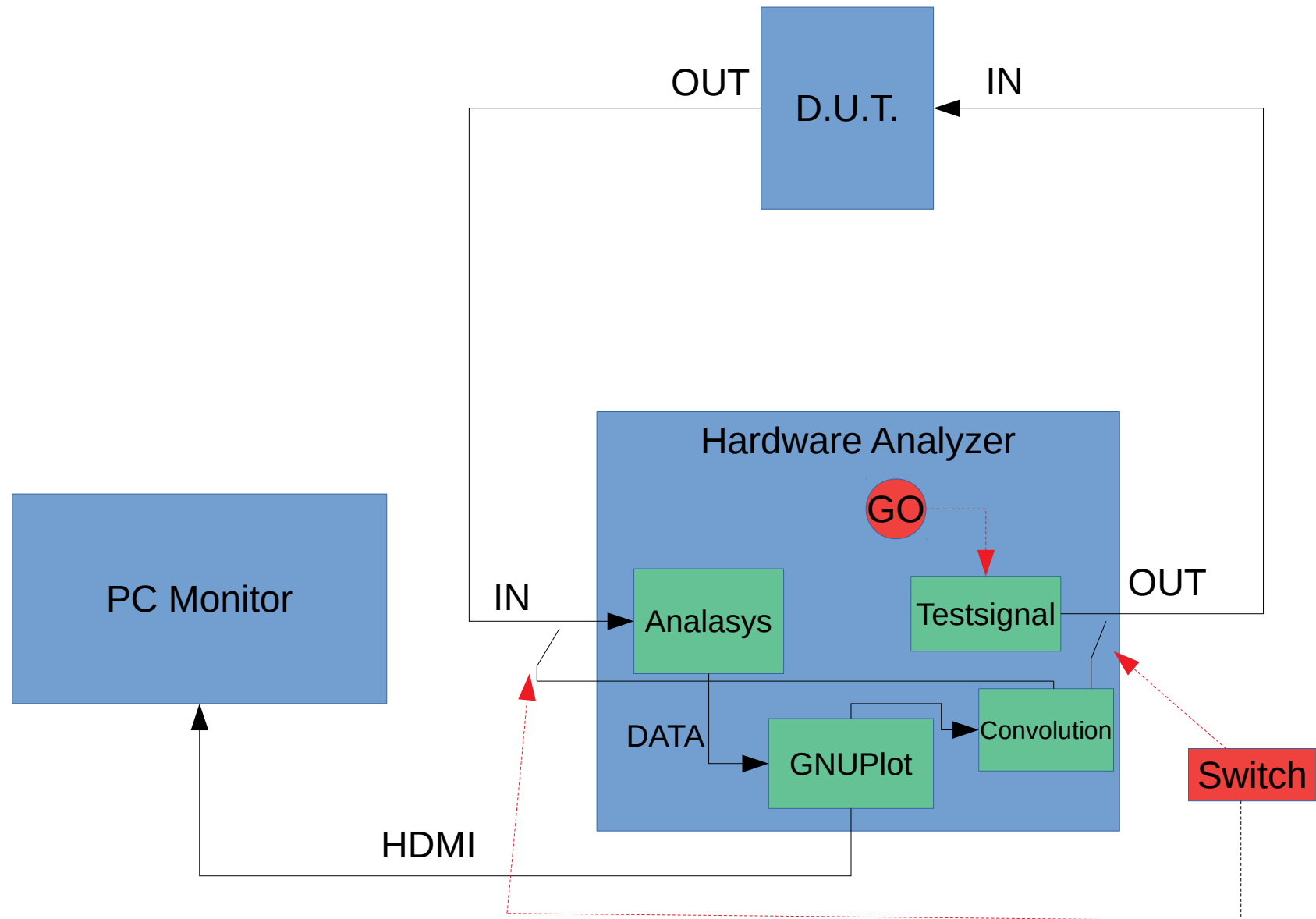
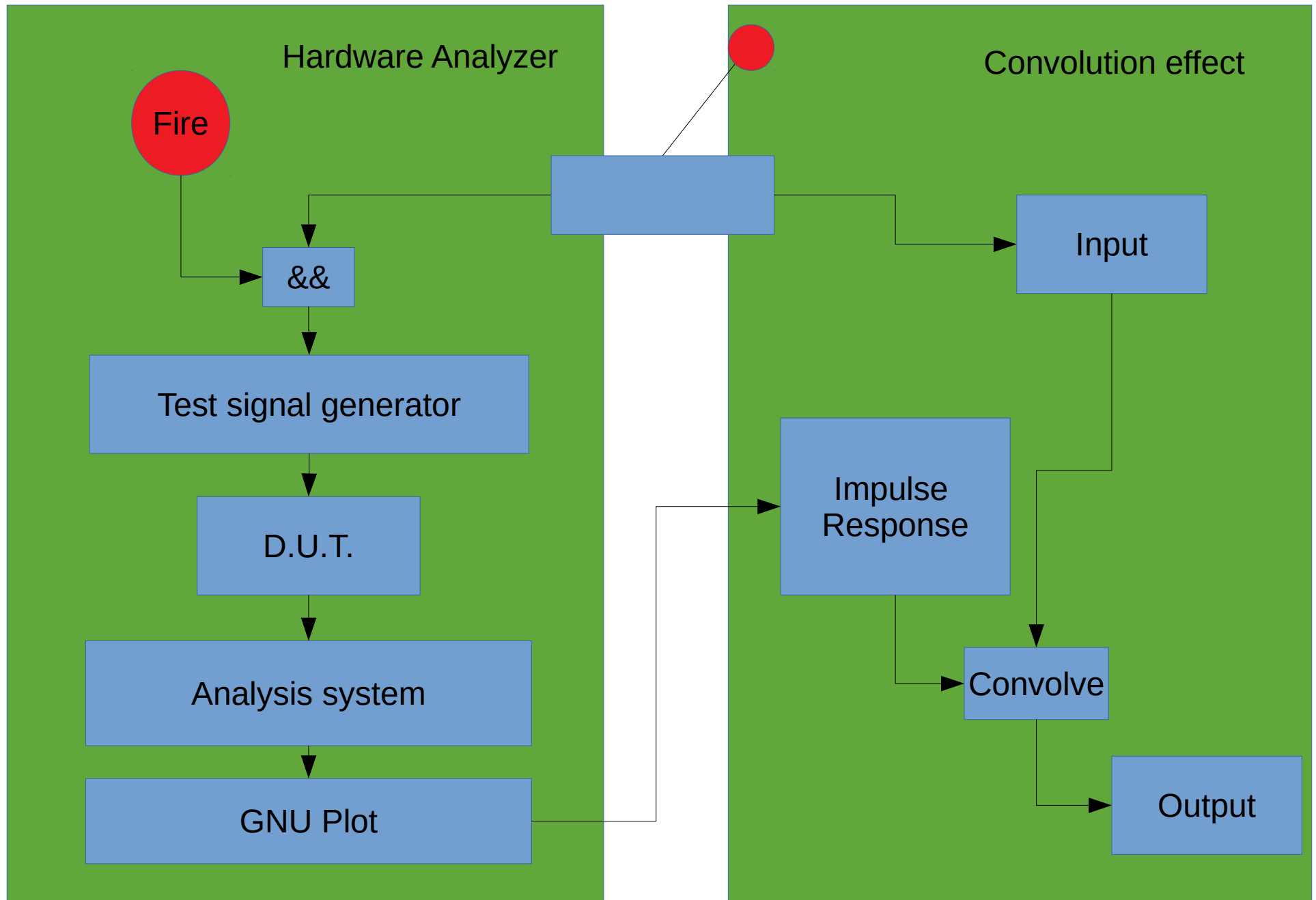


Hardware Analyzer



Data Flow



Class Diagram

