Name: Swaraj Rawat

ID: 21711283

Roll No: 54

University
Roll No: 2101271

Ques 1: Discuss the working of NAND gate with the help of circuit diagram and truth table.

Ans → A NAND gate ("not AND gate") is a logic gate that produces a low output (0) only if all its inputs are true, and high output (1) otherwise. Hence the NAND gate is the inverse of an AND gate, and its circuit is produced by connecting an AND gate to a NOT gate. Just like an AND gate, a NAND gate may have any number of input probes but only one output probe.

The NAND gate performs the logical NAND operation NAND gates are known as universal gates (along with NOR gates), which means they are a type of logic gate which can implement any Boolean function without the need to use any other gate type.

The basic logical construction of the NAND gate is shown below (we can see it is an AND gate followed by a NOT gate):

A ——
B ——
AND GATE
A·B →▷ NOT GATE → $\overline{A \cdot B}$
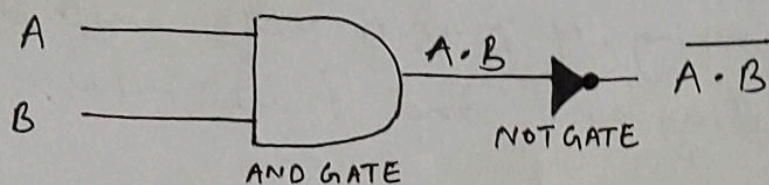
The symbol of a NAND gate is similar to the AND gate, but a bubble is drawn at the output point of the AND gate. The symbol of the NAND gate is shown below.

A ——
B ——
→ $\overline{A \cdot B}$
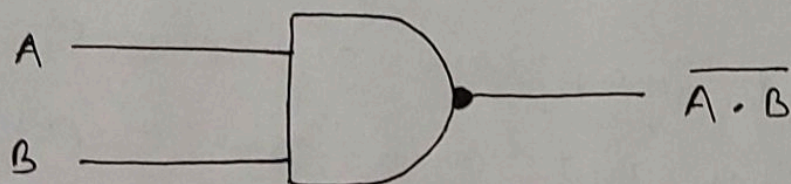
NAND GATE Truth Table –

The truth table of a 2 input NAND gate can be represented as:-

Inputs		Outputs
A	B	$X = \overline{A \cdot B}$
0	0	1
0	1	1
1	0	1
1	1	0