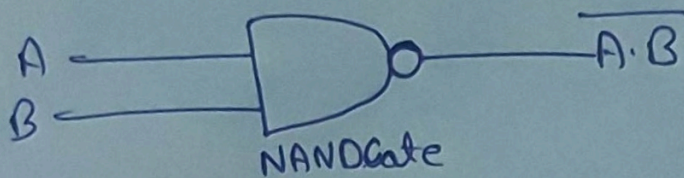
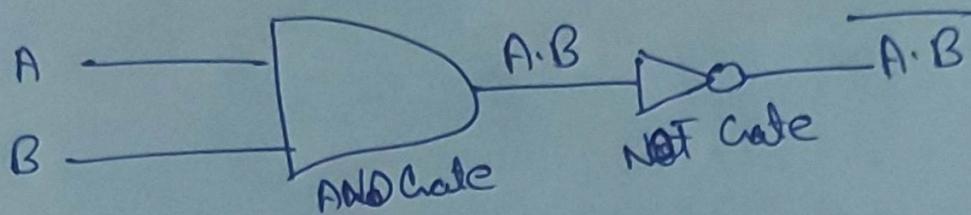


Ans-1: A NAND Gate ("not AND gate") is a logic gate that produces a low output (0) only if all its inputs are True, and high output (1) otherwise. Hence the NAND gate is the inverse of an AND gate, and its circuit is produced by connecting an AND gate to a NOT gate. Just like an AND gate, NAND gate may have any number of input probes but only one output probe.



Truth Table of 2 input NAND gate can be represented as!

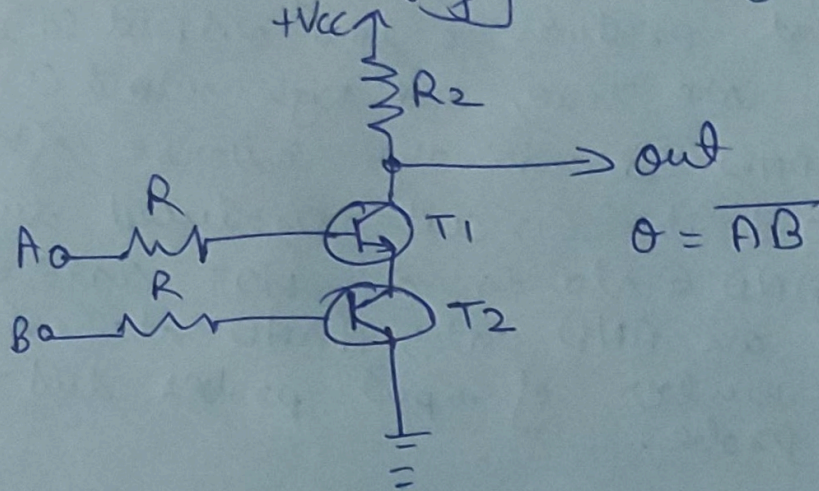
Input

A	B	output ( $X = \overline{A \cdot B}$ )
0	0	1
0	1	1
1	0	1
1	1	0

*Phit*



# Circuit diagram of NAND gate :



Prith