Norme - Pankaj Ramet
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Section - D Student Td - 21711269 s-1 Nand Grate: The NAND gate is a combination of each on AND gate and NOT get. They are connected in conscade form The NAND gote provides the folse or low output only when their outputs is high of true. The NAND gate is essential because it different I types of boolean function are implemented by using it. The NAND gote has the property of functional completeness is using the function completeness means any types of gote can be implemented by using the NAND gote of performs the function of OR, NOR and AND gote. The baje circuit of the NAND gate A - D - Z B - D - Z Z= 4.8

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	The coughin is good as "I amad
_	The equation is swood as "Z equal equal NOT A AND B". Since the
_	logic circuit involves and AND
_	gate followed by an inverter. The
	the second section of the second
_	when both the injourts are high.
_	
	The Truth table of NAND Grate
	ARZ
	0 0 1
_	0 1 1
_	1 0 1
	0
_	Con Ac 1 Le 111
	from the truth table of the MANND  gate it is clear that all the in puts  must be high to get a low output and  if any of the input is lace the
	most be high to
	if any of the inset is I low output and
-	obtained will be high 91
Name of Street, or other Persons	if any of the input is low the output and obtained will be high. If any of the input is high the output will be high the output will be
Appropriate Springer State Sta	he high that is I. be
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