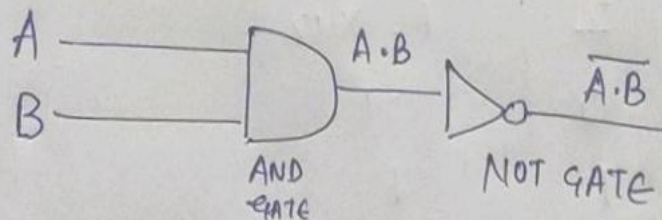


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Q1 NAND Gate

A NAND gate ("not AND gate") is a logic gate that produces a low output (0) only if all its inputs are true, and high output (1) otherwise.

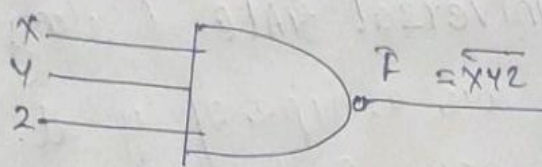
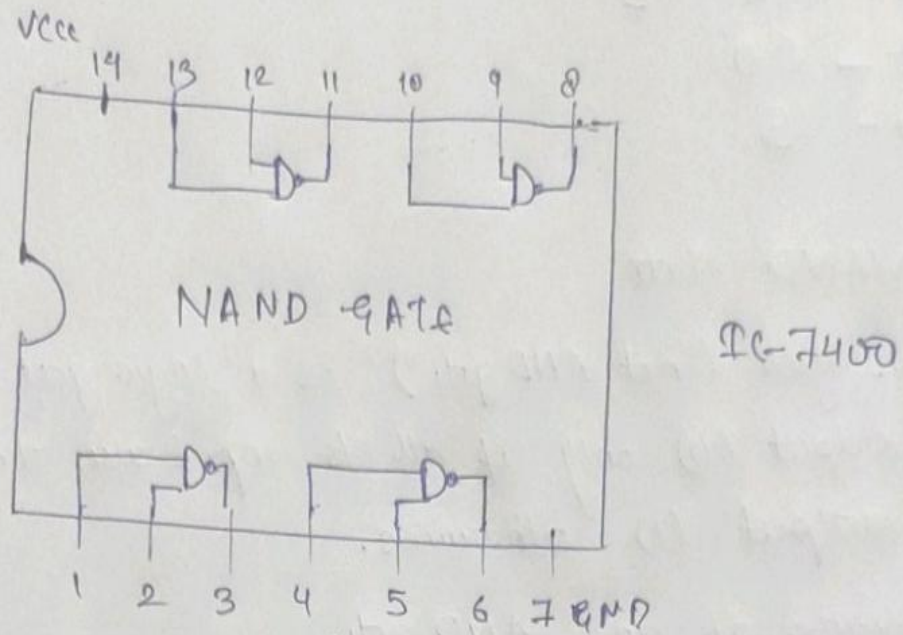
- Inverse of an AND gate.
- Circuit is produced by connecting an AND gate to a NOT gate.
- Also known as universal gate (along with NOR gate) which means they are a type of logical gate which can be implement any Boolean function without the need to use any other gate type.



TRUTH TABLE:-

A	B	F
0	0	1
0	1	1
1	0	1
1	1	0

Bdanu



TRUTH TABLE

X	Y	Z	$F = \overline{XYZ}$
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0

$$F = \overline{XYZ}$$

Bzu