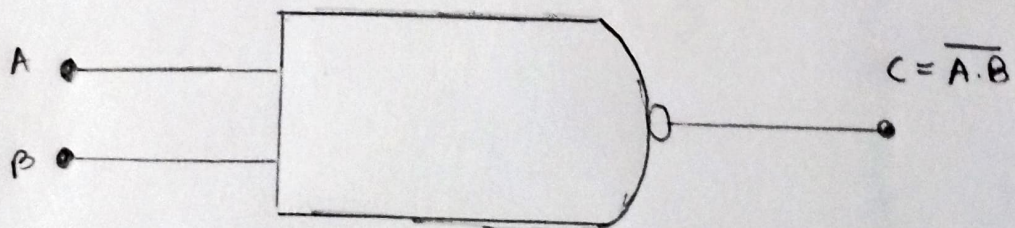
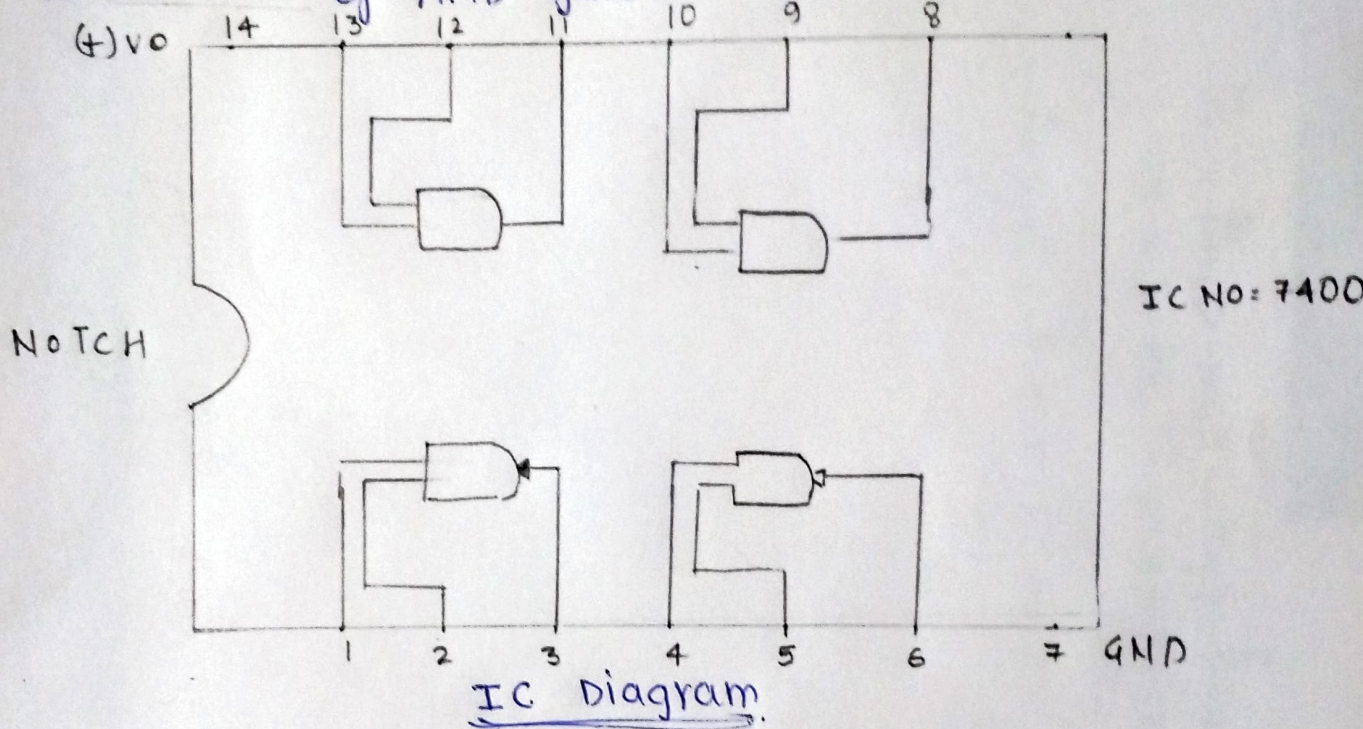


# NAND gate

The logic NAND gate is generally called as a "UNIVERSAL GATE" because it is one of the most used logic gate types all gates can be implemented by NAND gate. Boolean expression  $Q = \overline{A \cdot B}$ . The NAND gate has an output low (0) only when all inputs are high (1). It is

of AND gate



Logic diagram



Inputs		Outputs
A	B	$C = \overline{A \cdot B}$
0	0	1
1	0	1
0	1	1
1	1	0

Truth - Table