

Name: Divya Kaila

SID: 21562007

Course: MCA

Sem: 1

Sec: A

Mid-Term Practical

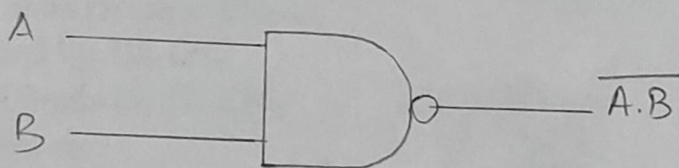
Computer Organisation & Architecture

→ Working of NAND Gate.

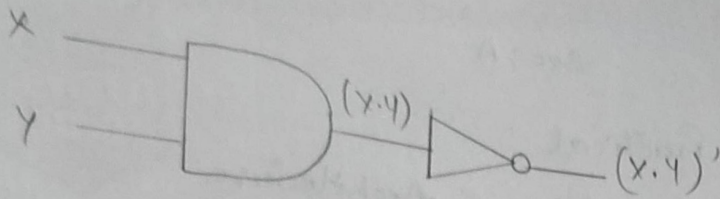
A NAND Gate ("not AND") is a logic gate that produces 0 (low) only if all its inputs are true, and high (1) otherwise.

• NAND gate is known as universal gate. It performs logical NAND operation.

• The NAND gate is an inverse of AND gate, and its circuit is produced by connecting an AND gate to a NOT gate.



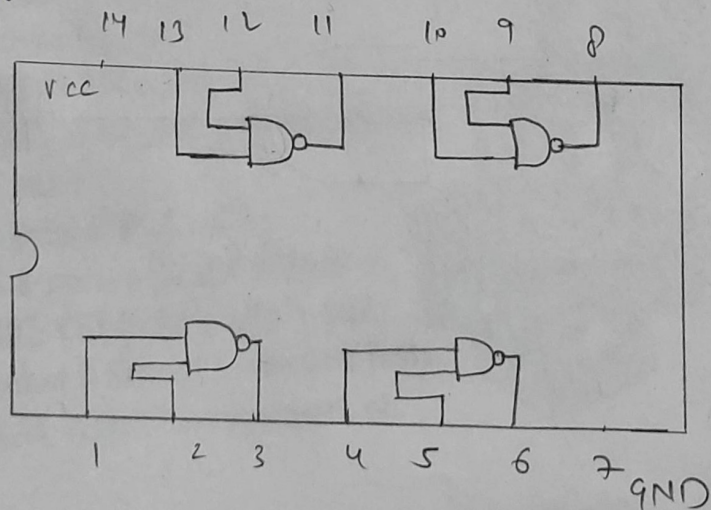
Symbol of NAND Gate



Truth Table:

Input		Output
A	B	B X
0	0	1
0	1	1
1	0	1
1	1	0

logic expression $\Rightarrow Y = (A \cdot B)'$



IC Diagram.