

Graphic Era Hill University, Dehradun
(Answer Sheet for Online Examination Feb. 2022)

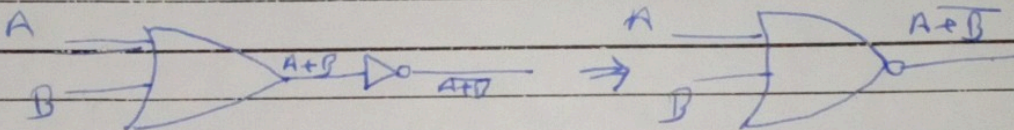
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Name: Vikrant Singh Univ. Roll No. 2101248 Student ID 2171052
Date: 22/3/22 Course: MCA Branch: Sem.: I Section: B
Subject Name: COA Subject Code: 7MC102 Page No. 1

Q2 ⇒

NOR Gate ⇒ A NOR gate ("NOT OR gate") is a logic gate that produces a high output (1) only if all its inputs are false, and low output (0) otherwise. Hence the NOR gate is a inverse of an OR gate and its circuit is produced by connecting an OR gate, a NOR gate may have any number of input probes but only one output probe.

~~A NOR gate~~ The basic logic construction of the NOR gate is shown below.



NOR gate Truth table ⇒

input		output
A	B	$X = A+B$
0	0	1
0	1	0
1	0	0
1	1	0

Vikrant Singh
Signature of Student

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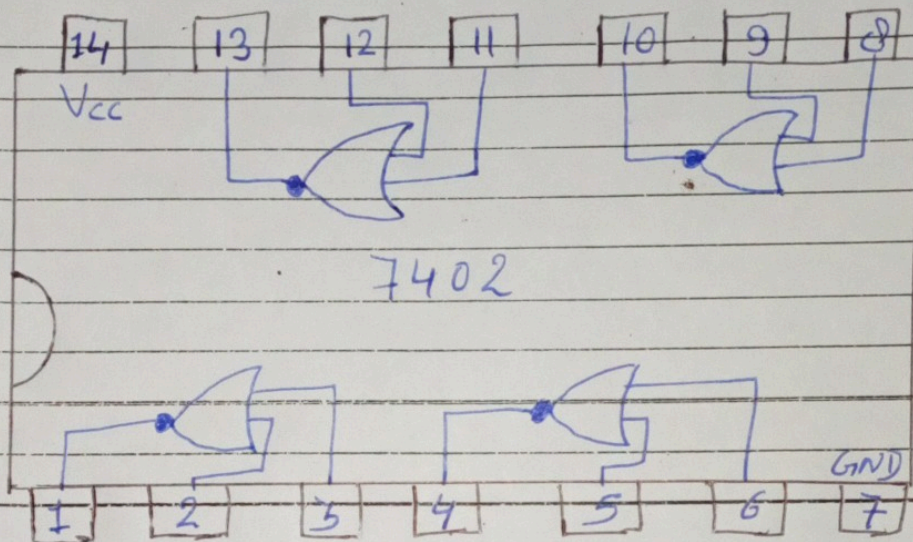
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NOR gate circuit diagram:-



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