NAME: Getarjali Bisht

FATHER 25 NAME: Dhyan Singh Bisht

SEMESTER: IST

UNIVERSITY NO.: 2101073

COURSE: MCA

SECTION: 1B'

Regular Pradical

Student id: 21712048

Subject: CO

CODE: PMC-102

Anus) NAND GATE:

To verify the working of NAND wring circuit diagram & truth table.

Apparatus Required: Digetal Trainer kit, IC Tester, IC-7400 & connecting wires.

Theory: logic gates are electronic virusits which perform logical functions on one ar more inputs to produce one output. There are several logical gates. When all inputs combinations of a logic gate are viritter in a series & their corresponding outputs writter along them, then this input / output combination is called TRVE TABLE.

NAND Gate: It is a series of AND gate with NOT gate. If we connect the output of an AND gate to the input of a NOT gate, this combination will work as NOT-AND or NAND gate. Its output is I when any or all inputs are 0, otherwise output is 1.

	A	B	Y
	0	0	1
	0	1	1
l	1	0	1
l			0



Procedure: - 1) Make the connection as per the circuit diagram on breadboard

- 2) Apply various combination of input according to truth table of NAND gat.
- 3) Note down the output reading for different combination inputs.

Result: Thus, we have verified the Truth Table.

NAND-IC

IC- 7400

NAND - GATE

