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PATENTED: THIS PRODUCT IS COVERED BY U.S. PATENT 5,736,910



DOCUMENT / PART NO.: SI-51005-F

TITLE: 10/100/1000BT, TAB UP SHIELDED

ISSUE		DE	SCRI	PTIO	N OF	CHAN	IGE			REVIE	WED	BY	A	PPRO	VED	вү	EC	N#
A0	Use new format and production release.						David Chan				Steve	Weller		062	90			
A1	Add a note 9.This compnent is a patent; US patent Number 7,123,117								Bain Liu			Steve Weller			063	39		
A2	Change circuit(C5104-F) the resistor array(26-1075) to single resistor(11-2075)							Bain Liu				Steve Weller			063	67		
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PAGE	1	2	3	4														
REV.	A2	A2	A2	A2													<u> </u>	
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RoHS 2002/95/EC

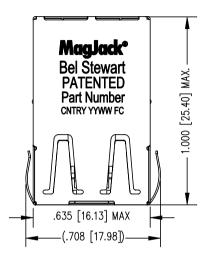
ELECTRICAL CHARACTERISTICS @ 25°C

1.0 INDUCTANCE: (P2-P3): 350uH MIN. @ 0.1V, 100KHz, 8mA DC BIAS PINS **SCHEMATIC** RJ45 (P4-P5) : 350uH MIN. @ 0.1V. 100KHz. 8mA DC BIAS (P7-P8) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC BIAS (P9-P10) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC BIAS 1CT : 1CT J1 TRP1+ TRD1+ P2 2.0 TURNS RATIO: (P2-P3): (J1-J2): 1CT : 1CT ± 2% (P4-P5): (J3-J6): 1CT : 1CT ± 2% TRD1- P3 J2 TRP1-(P7-P8): (J4-J5) : 1CT : 1CT ± 2% (P9-P10): (J7-J8) : 1CT : 1CT ± 2% 1CT : 1CT J3 TRP2+ TRD2+ P4 3.0 RETURN LOSS: 100 OHM REF. 500kHz TO 40MHz : -18dB MIN. 40MHz TO 100MHz : -12+20LOG(f/80MHz) MIN. TRD2- P5 J6 TRP2-1CT : 1CT TRD3+ P7 J4 TRP3+ 4.0 INSERTION LOSS: RS=RL=100 OHMS ou. : -1.2dB MAX. 100kHz TO 100MHz 5.0 CROSS TALK: Ch1-Ch2, Ch1-Ch3, Ch1-Ch4, Ch2-Ch3, Ch2-Ch4, Ch3-Ch4 J5 TRP3-TRD3- P8 : -35dB MIN. 1MHz TO 30MHz 1CT : 1CT TRD4+ P9 J7 TRP4+ 30MHz TO 80MHz : -24dB MIN. 80MHz TO 100MHz = -25 dB MIN.3||{ 6.0 CM TO CM REJECTION: TRD4- P10 J8 TRP4-100kHz TO 100MHz : -30dB MIN. 7.0 CM TO DM REJECTION: COMMON CT P6 100kHz TO 100MHz = -35 dB MIN.4X 75 OHMS 8.0 VOLTAGE WITHSTAND: (J1.J2) TO (P2.P3) GND P1 : 1500Vrms (J3.J6) TO (P4.P5) : 1500Vrms 1000pF 2kV (J4,J5) TO (P7,P8) : 1500Vrms (J7.J8) TO (P9.P10) : 1500Vrms SHIELD 7777

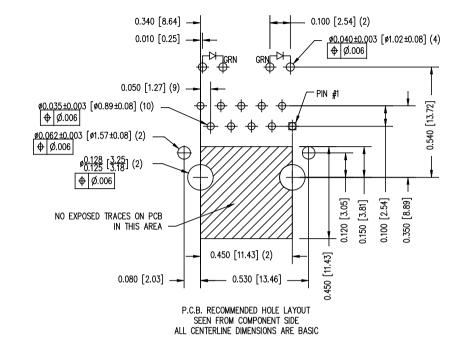
9.0 THIS COMPNENT IS A PATENT PRODUCT; US PATENT NUMBER 7,123,117.

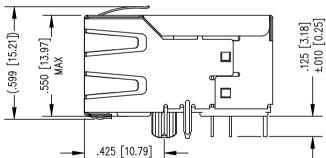
ORIGINATED BY	DATE	TITLE Mag lack®	PART NO. / DRAWING NO.	STANDA	RD DIM.	[] METRIC DIM. AS REF	ERENCE	
Zeng Xiao Chun	03-23-2011	MagJack [®]	SI-51005-F	TOL. IN	INCH	UNIT : INCH [mm]	REV.: A2	bel
DRAWN BY	DATE		FILE NAME	.X		SCALE: N/A	SIZE : A4	COMPONENTSFOR A CONNECTED PLANET
l	00 00 0044	TAB UP SHIELDED	SI-51005-F.DWG	.XX				PLANET 7
Zhang Ru Sheng	03-23-2011	PATENTED	0.0.000.1.2.1.0	XXX.	±0.005		PAGE: 2	

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LED SPECIFICATION							
STANDARD LED WAVELENGTH FORWARD V (MAX) * (TYP							
GREEN	565 nm	2.5 V	2.2 V				

*WITH A FORWARD CURRENT OF 20 mA (TYP)

NOTES:

- 1. CONNECTOR MATERIALS: HOUSING: THERMOPLASTIC UL94 V-0 CONTACT/SHIELD: COPPER ALLOY SHIELD PLATING: NICKEL OR TIN CONTACT PLATING: SELECTIVE GOLD, 50 MICRO-INCHES MIN. IN CONTACT AREA.
- 2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- 3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
- 4. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]
- 5. REFLOW AND WAVE SOLDER COMPATIBLE -260°C FOR 10 SECONDS MAX.

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Zeng Xiao Chun	03-23-2011		MagJ
DRAWN BY	DATE		100/100/
Zhang Ru Sheng	03-23-2011		TAB UP S

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PART NO. / DRAWING NO.	STANDA	RD DIM.	[] METRIC DIM. AS REFERENCE			
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SI-51005-F.DWG	.xxx	±0.005		PAGE: 3		

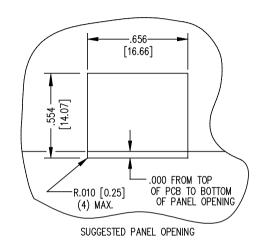


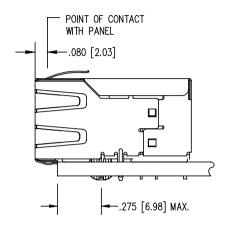
RoHS

GREEN

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- 1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
- 2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]

ORIGINATED BY	DATE	TITLE
Zeng Xiao Chun	03-23-2011	
DRAWN BY	DATE	
Zhang Ru Sheng	03-23-2011	

MagJack®
100/100/1000BT
TAB UP SHIELDED
PATENTED

PART NO. / DRAWING NO.	STANDA	ARD DIM.	[] METRIC DIM. AS REFERENCE			
SI-51005-F	TOL. IN	INCH	UNIT : INCH [mm]	REV.: A		
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SI-51005-F.DWG	.xxx.	±0.005	lacktriangle	PAGE:		

