

Date      /      /     

8. When a train is moving at 25 m/s, steam is shut off & brakes are applied. The speed  $v$  of the train in m/s after  $t$  sec is given in the following table. Find the distance moved by the train before coming to a halt using Simpson's rule & trapezoidal rule.

$t$	0	10	20	30	40	50	60
$v$	25	18.1	12.3	7.6	4	1.6	0

04.05.2019

Numerical  
\* Solution of First order DE :

$$\frac{dy}{dx} = f(x, y), \quad y(x_0) = y_0 \rightarrow \text{"initial cond."}$$

$x \rightarrow$  independent

$y \rightarrow$  dependent

$$\begin{aligned} y &= y(x) & x_1 &= x_0 + h \\ y_0 &= y(x_0) & x_2 &= x_1 + h \\ y_1 &= y(x_1) & & \\ y_2 &= y(x_2) & & \\ & & & \vdots \end{aligned}$$

Euler method

Improved Euler method

Runge - Kutta method (4<sup>th</sup> order)

Q. To solve  $\frac{dy}{dx} = f(x, y)$   $y(x_0) = y_0$  numerically, we will be finding the values of  $y$  at  $x_1, x_2, \dots$



Date \_\_\_/\_\_\_/\_\_\_

where  $y_1 = y(x_1)$ ,  $y_2 = y(x_2)$  where  $x_1 = x_0 + h$   
 $x_2 = x_1 + h$ .

I. Euler method:  $y_{n+1} = y_n + hf(x_n, y_n)$ ;  $(n=0, 1, 2, 3, \dots)$

Q. Solve:  
 $y' + 0.2y = 0$ ;  $y(0) = 5$ ;  $h = 0.2$ . Do 10 steps.  
 by Euler formula.

$$y_{n+1} = y_n + hf(x_n, y_n)$$

Here  $f(x, y) = -0.2y$ ;  $x_0 = 0$ ,  $y_0 = 5$ ,  $h = 0.2$ .

$$n=0: y_1 = y_0 + hf(x_0, y_0) = 5 + 0.2(-1) = 4.8$$

$$n=1: y_2 = y_1 + hf(x_1, y_1) = 4.8 + 0.2(-0.2 \times 4.8)$$

$$= 4.8[1 - 0.04] = 4.8 \times 0.96$$

$$= 4.608$$

$$n=2: y_3 = y_2 + hf(x_2, y_2) = 4.608 + (0.2)(-0.2 \times 4.608)$$

$$= 4.42368$$

$$n=3: y_4 = y_3 + h(-0.2y_3) = y_3[1 - 0.04] = y_3 \times 0.96$$

$$= 4.2467328$$

$$n=4: y_5 = 0.96 \times y_4 = 4.076863488$$

$$n=5: y_6 = 3.913788948$$

$$n=6: y_7 = 3.757237391$$

$$n=7: y_8 = 3.606947895$$

$$n=8: y_9 = 3.462669979$$

$$n=9: y_{10} = 3.32416318$$

Solve:

Q.  $y' = (x+y)^2$ ;  $y(0) = 0$ ;  $h = 0.1$

$f(x, y) = (x+y)^2 \Rightarrow x_0 = 0, y_0 = 0, h = 0.1$



Date \_\_\_/\_\_\_/\_\_\_

Saathi

$$x_1 = 0; y_1 = y_0 + hf(x_0, y_0) = 0; y_2 = 0 + 0.1(-)$$

$$x \quad y \quad f(x, y) \quad hf(x, y) \quad * f(x, y) + hf(x, y)$$

$$x_0 = 0 \quad y_0 = 0 \quad 0 \quad 0 \quad 0$$

$$x_1 = 0.1 \quad y_1 = 0 \quad 0.001 \quad 0.001 \quad 0$$

$$x_2 = 0.2 \quad y_2 = 0.001 \quad 0.004001 \quad 0.004001 \quad 0.001$$

$$x_3 = 0.3 \quad y_3 = 0.004001 \quad 0.49056 \quad 0.049056 \quad 0.0401 \times 10^{-3}$$

$$x_4 = 0.4 \quad y_4 = 0.04906 \quad 0.793097 \quad 0.0793097 \quad 0.014345$$

$$x_5 = 0.5 \quad y_5 = 0.0793097 \quad 0.0793097 \quad 0.0793097 \quad y_5 = 0.0315132$$

$$x_6 = 0.6 \quad y_6 = 0.059764 \quad 0.059764 \quad 0.059764 \quad 0.0315132$$

$$x_7 = 0.7 \quad y_7 = 0.03293 \quad 0.03293 \quad 0.03293 \quad 0.059764$$

$$x_8 = 0.8 \quad y_8 = 0.167821 \quad 0.167821 \quad 0.167821 \quad 0.03293$$

$$x_9 = 0.9 \quad y_9 = 0.261488 \quad 0.261488 \quad 0.261488 \quad 0.167821$$

Q. Given  $y' = x^2 + y$ ;  $y(0) = 1$ ; determine the value of  $y$  when  $x = 0.1$

Let  $h = 0.05$ ;  $x_0 = 0$ ,  $y_0 = 1$ ;  $f(x, y) = x^2 + y$

$x$	$y$	$f(x, y)$	$hf(x, y)$	$(y_{n+1})$
0	1	1	0.05	(1.05)
0.05	0.05	0.4525	<del>0.054525</del>	<del>1.054525</del>
		0.06775	<del>0.052625</del>	<del>1.102625</del>
0.1	<del>0.05775</del>	<del>0.012625</del>	<del>0.052625</del>	<del>1.15825625</del>
	<del>1.102625</del>		<del>0.05563125</del>	

$$\Rightarrow y(0.1) = 1.15825625$$



Adv of paging: (i) avoids ext. fragmentation (ii) <sup>avoid</sup> need for compaction, whereas segment does not (iii) solves considerable prob. of filling mem. chunks of varying sizes into the backing store.

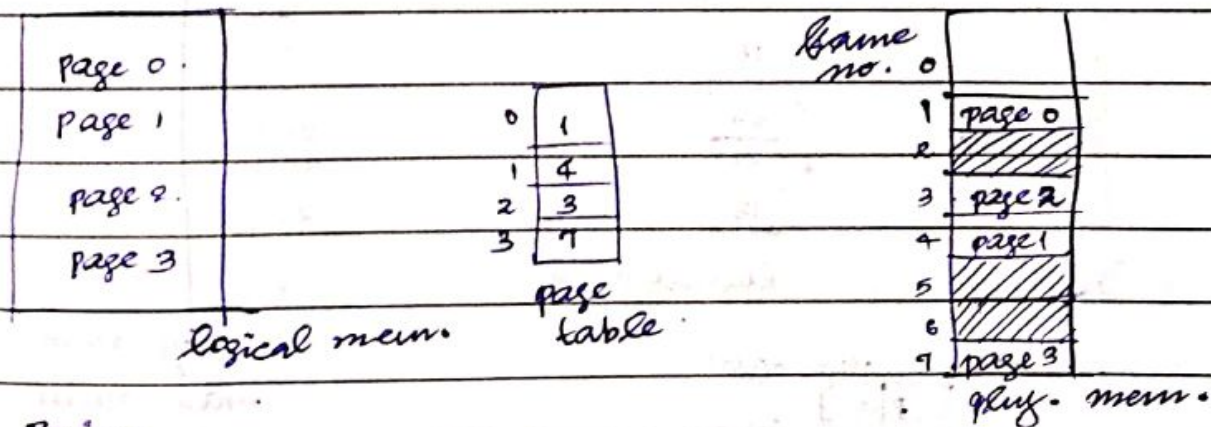
Date \_\_\_/\_\_\_/\_\_\_

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### \* How Paging:

- (i) Phys address space of a process can be non-contiguous.
- (ii) Process is divided into fixed-size blocks, each of which may reside in a diff. part of phys. mem.
- (iii) Divide phys. mem. into fixed-sized blocks called frames.
  - size of a frame is  $2^x$  where  $2^x \leq 12 \text{ bytes} < 2^x \leq 16 \text{ M bytes}$
- (iv) Divide logical mem. into blocks of same size as frames called pages.
- (v) Backing store, where the pgm. is permanently residing, is also split into blocks - storage units - called blocks.

$$(\text{Block size})_{\text{phys. mem.}} = (\text{Block size})_{\text{logical mem.}}$$



### (iii) Paging

- avoids external fragmentation. ∵ page size is same.
- still have internal " eg: page size = 50
  - ⇒ 155 bytes process requires 4 pages, though last page has just 5 bytes.
- segment size may differ

1. keep track of all free frames.



Date \_\_\_/\_\_\_/\_\_\_

2. To run a psm. of size  $N$  pages, need to find  $N$  free frames to load psm. from backing store.
3. Set up a page table to translate logical to phy. address.
4. Page table is kept in memory:
  - Page-table B.R. (PTBR) points to the page table
  - " " length reg. (PTLR) indicates size of the page table.

No. of entries in logical mem. =  $n$  (phy. mem.)

NOTE

logical mem. entries contain the page frame in the phy. mem.

eg.

0	20
1	11
2	15
3	29
4	22
5	13

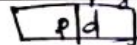
logical mem.

11
12
13
14
15
...
20
21
22
23
24

phy. mem.

main. mem.

page no.    page offset

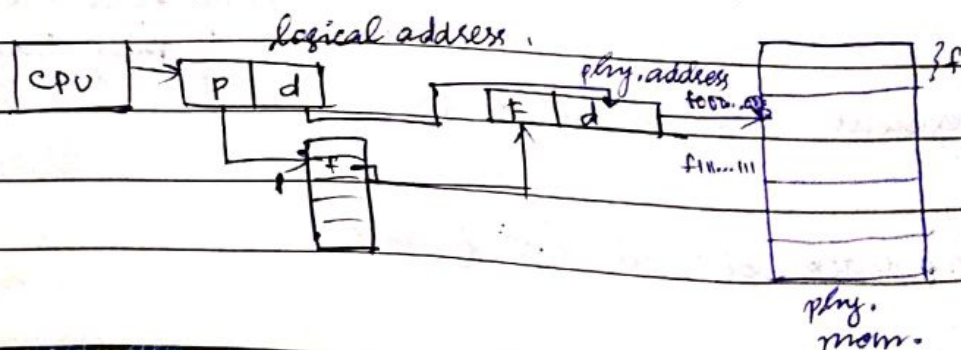


$$m-n \quad n \quad 3100 \equiv 15 \quad 100$$

where  $2^n = \text{page size}$ .

\* Paging H/w :

logical address space =  $2^m$



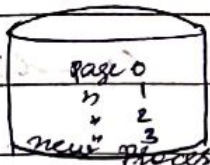


Date \_\_\_/\_\_\_/\_\_\_

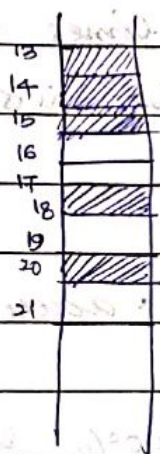
Address generated by CPU has 2 parts - p and d.

free-frame list

14  
13  
18  
20  
15



before alloc?

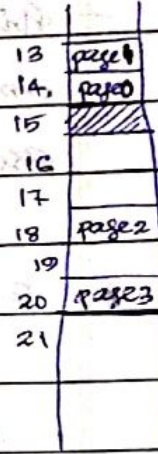


(a)

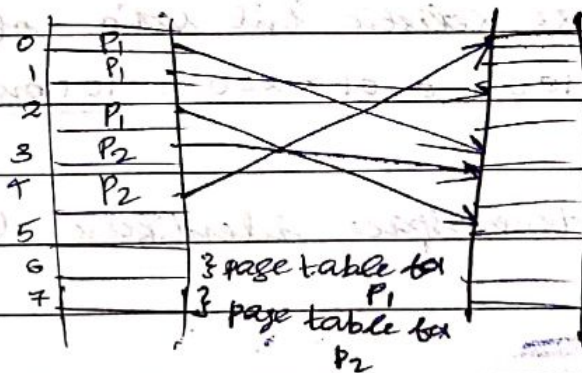


0	14
1	13
2	18
3	20

(b)



free frames (a) before alloc<sup>n</sup>. & (b) after alloc<sup>n</sup>.



$P_1 = 0210$

$P_2 = 0210$

mem. access takes more time.

\* TLB - Associative mem.

associative mem. / translation look-aside buffers

0	14
1	
2	
3	
4	

Paging now with TLB?





Date \_\_\_\_ / \_\_\_\_ / \_\_\_\_

\* Eff. access time (EAT):

- Hit ratio: % of times that a page no. is found in the associative reg; ratio related to no. of associative reg.

$$EAT = \text{hit ratio} \times \text{access time} + (1 - \text{hit ratio}) \times 9 + \text{access time}$$

if hit ratio = 80% & mem. access takes 100ns;

$$EAT = 0.80 \times 100 + 0.20 \times 200 = 120 \text{ ns}$$

Consider more realistic hit ratio of 99%?

$$EAT = 0.99 \times 100 + 0.01 \times 200 = 101 \text{ ns} \Rightarrow \text{EAT (access time)}$$

TLB issues: address-space identifiers (ASIDs)

• Memory protect? :

valid/invalid bit,

\* Page table for large address space:

Hierarchical paging

Hashed page tables.

Inverted page tables.