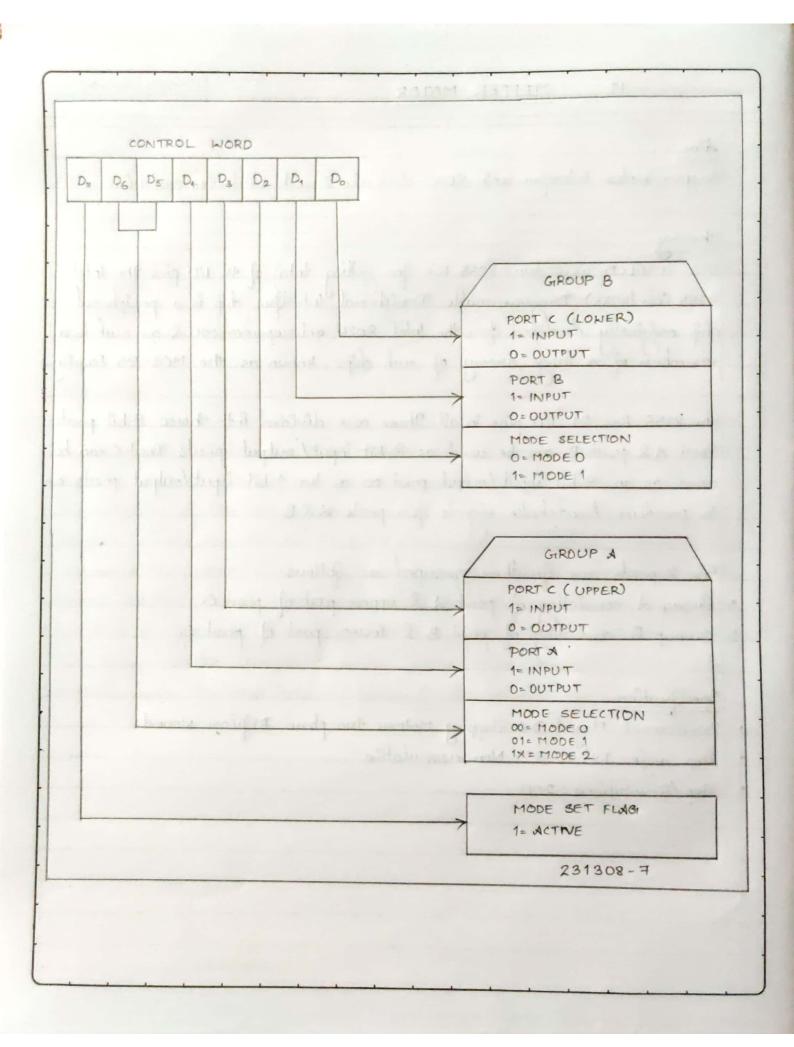
Function 8255-1	Addness	Function 8255-11	Address
posit &	40	R Jeog	60
posit B	4-2	posit B	62
posit C	44	posit c	64
control	46	control sugistes	66

## mattheople

The program initializes the 8255 (P1) in order to make post & as output post. The PAO to PAB is connected through buffer & driving circuit to the stinding of the steppes motor. The codes for clockstise movement of steppes motor are FA, F6, F5 & F9. These codes are to be output in the sequence they are written. In case of anti-clock wise movement of the steppes motor, output codes are as F9, F5, F6 & F4. The delay soutine is called to generate the delay (max. of about 1 sec.) between the steps. This delay can be changed to make faster steps. The aminimum delay clepends upon the maximum speed of the steppes motor specified.

Experi	ment Name / No.: 19 STEPPER MOTOR	Camlin   Page No. 53
	Steppen motor interface with 8086 clock wise & anti-clock wi	ise suntation.
5	Theosing STS 8086LCD uses two 8255 IC's Jose getting total of 48 8755 (on 18255) Paragrammable Penipherial Interspece chip chip originally developed Jose the Intel 8085 micorparacrast members of a large arrany of such chips, known as the	1/0 pins. The Intel is a pessiphosial now, 2 as such is a now MC3-85 Family.
10	The 8255 has 24 1/0 pins in all. These are divided into Post & 2 post B can be used as 8-bit input/output pour used as an 8-bit input/output post not as two 4-bit input to produce handshake signals Jose posts & & B.	ports. Poort C can be
1.	The 3 posts age further grouped as follows:  Group & consisting of post & & upper part of post of  Group B consisting of post B & lower part of post	· · · ·
•20	Specification  Permament Magnet D.C. Stepping Motors two phase Biffilari  Step angle: 1.8° +/- 5% Non-cumulative.  Step/Revolutions: 200	wound.
25		
	Teacher's Signatu	ure:



riment Name / N	No.: 19		Camlin Page No. 55	
			Date	
Регодист	2			
Address	Instauction		Comment	
0400		MOV AL, 80H	Instialize posit	
0402		OUT 66H, AL		
0404	LOOP2:	MOV CL, 04	Loop count	
0406		MOV BX, 500H	Table location	
0409	LOOP1:	MOV AL, [BX]	. Gret forom table	
040B		JK, 408 TOO	Place to posit	
0400		CALL DELAY	Rotation data	
0410		INC BX	Inc pointes	
04-11		LOOPNZ LOOP1		
0413		JMP LOOP2	Repeat	
0415	DELAY:	PUSH CX	Save CX	
0416		MOV CX, OFFFFH	Delay loop count	
0419	HERE!	LOOPNZ HERE		
0418		POP CX	Retolleve	
041C		RET		
Result Complete Subtation	3.	otosi intenface with 8086	dockwise & anti-dockwis	
5			Teacher's Signature:	