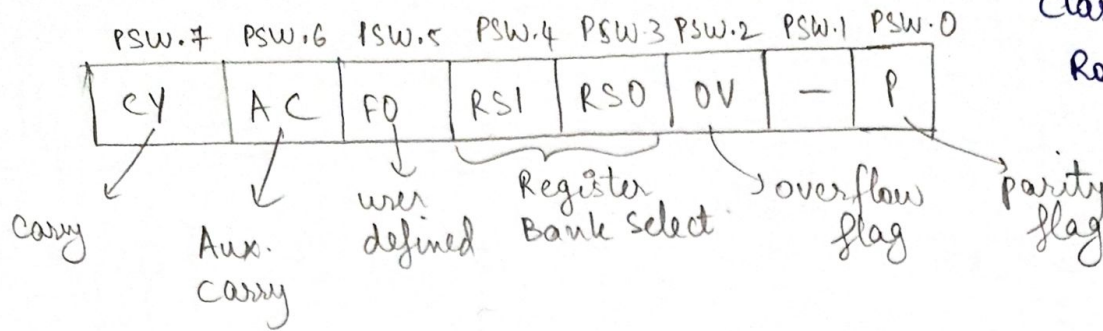


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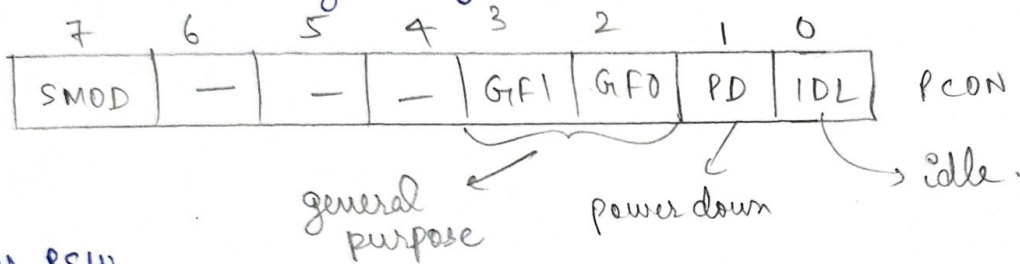
Class: CS6A

Roll no.: 55

1. PSW of 8051



2. ~~Power~~ Power Control Register of 8051



3. (a) PSW

4. (b) 00-07

5. Harvard architecture Involves both volatile & non-volatile memory.

6. (a) Source register.

7. (a) Increases with every push.

8. 2 architectures - von Neumann & Harvard.