Customer		Date	
Project title		**Code No.**	
Project Description			
IC Body	☐GPCM1F064A; SRAM: 13KB; ROM: 64KB; 31 I/O pins ☐GPCM1F064A_0001; SRAM: 13KB; ROM: 64KB; 31 I/O pins ☐GPCM1F064B; SRAM: 8KB; ROM: 64KB; 29 I/O pins ☐GPCM1F064B_0001; SRAM: 8KB; ROM: 64KB; 29 I/O pins		

Release code file (fill "00H" for unused area)			
Binary filename		**Binary file checksum**	

Hardware / Software

- If ADC Line_in is used, please check the Line_In input (shared with IOA17~24) voltage range: 0 V ~ VDDIO
- If SPI (Serial Peripheral Interface) is used, please check the following:
  a. While in standby mode, users must ensure that SPI interface is not under floating condition in order to keep standby current stabilized.
- If watchdog is enabled, watchdog port (0x400D0000) must be cleared by writing 0xAA. For more information, please refer to the corresponding programming guide (Ch.26).
- If sleep mode is applied, please check the following:
  a.   Deep Sleep Mode: CPU & Sys. CLK stopped
  b.   Halt Mode: CPU & Sys. CLK stopped, 32768Hz ON.
  c.   When GPCM1F EV chip is connected with G+Link pro, it cannot emulate the sleep mode and deep sleep mode. The G+Link pro must be disconnected with the EV chip first and power it on again to enter sleep mode and deep sleep mode.
- No floating state is allowed on I/O if it is used for key wakeup function.
- VDD_REGIN must be connected with Power_in and V33_REG must be connected with a 2.2uF to GND.
- There are two IO powers supplying IOA and IOB ports. IOA port uses the VDDIO power (Share power Input pin with VDD_REGIN) and IOB port (SPIFC I/F) uses V33_REG output power.
- Internal FLASH written-protected setting corresponded to 1K ~ 8KB for different protected range. When the range is selected as "Protected", the range is allowed to be read only. Write is not permitted.
- Please set IO mode to Input pull-low if an adjacent pad is located after the touch PAD and used for key-change wakeup source. Please refer to the CTS chapter of the GPCM1F programming guide for more detailed information.

☐**Check; by ticking off this box, customer understands that the above conditions are fully met and verified.**

General programming checklist

The general programming checklist intends to provide some general characteristics about GENERALPLUS devices.   It is the customer's responsibility to check all the information in the list.   No responsibility is assumed by GENERALPLUS for any non-checked box even this confirmation sheet has been approved by GENERALPLUS.   Make sure the following conditions are met and verified:
- All used SRAM must be initialized after power on (Strongly recommended).
- Make sure the used SRAM variables are not over stack reserved area.
- Make sure no current leakage in I/O or speaker amplifier during sleeping.
- Make sure all I/Os are not floating during sleeping.
- Non-used I/O ports must be masked off (for input process).

☐**Check; by ticking off this box, customer understands that the above conditions are fully met and verified.**

Development tools and Document version	
Software tools version	
Programming guide title and version	
User's guide title and version	
Other documents (if any)	
Customer's Signature	

Note: Please send/fax this form to GENERALPLUS.   GENERALPLUS will return it back with signature.