



Side Channel Attack on GIFT COFB

Attack on Lightweight Cryptography

CS6630 Secure Processor Microarchitecture Project

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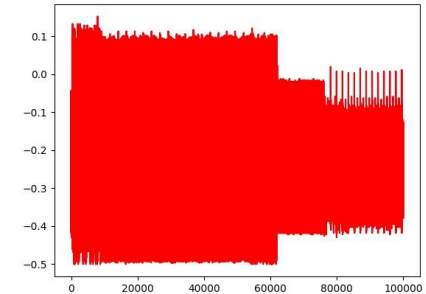
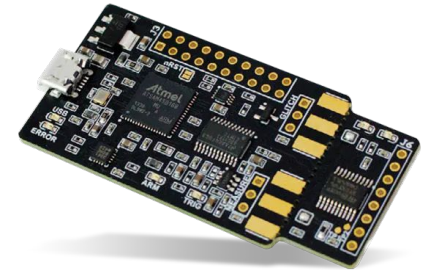
Yogasanthoshi (CS19B049)

Background

- GIFT-COFB is an Authenticated Encryption with Associated Data (AEAD) scheme, based on
 - GIFT lightweight block cipher and,
 - COFB lightweight AEAD operating mode.
- Authenticated Encryption with Associated Data (AEAD) scheme:
 - Provides both confidentiality and authenticity.
- Power Side Channel Attacks:
 - Recover secret information from hardware by processing the power consumption of the device.

Our work & Overall results

- Power traces from ChipWhisperer Nano running software implementation of GIFT-COFB collected
- Performed two different implementations of CPA and DOM attacks
- Observed both fail to obtain secret key



COFB Encryption

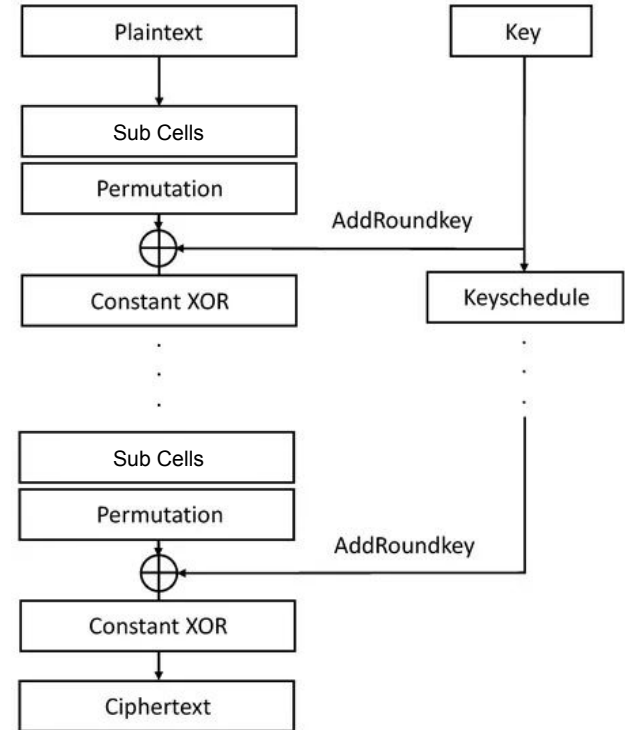
- The encryption algorithm takes as input
 - An encryption key $K \in \{0, 1\}^{128}$
 - A nonce $N \in \{0, 1\}^{128}$ (non-repeating)
 - Associated data and message $A, M \in \{0, 1\}^*$
- Generates the following output:
 - Ciphertext $C \in \{0, 1\}^{|M|}$
 - Tag $T \in \{0, 1\}^{128}$
- Underlying block cipher: GIFT 128 block cipher

COFB Decryption with Verification

- Decryption of a ciphertext-tag pair (C, T)
 - An encryption key $K \in \{0, 1\}^{128}$
 - Symmetric Key algorithm
 - A nonce $N \in \{0, 1\}^{128}$
 - Associated data and ciphertext $A, C \in \{0, 1\}^*$
 - Tag $T \in \{0, 1\}^{128}$
- It generates the following output:
 - Message $M \in \{0, 1\}^{|M|} \cup \{\perp\}$

Block Cipher GIFT-128

- GIFT-128 is an 128-bit Substitution-Permutation network (SPN) based block cipher with a key length of 128-bit.
- 40 Identical rounds. Each round consists of
 - SubCells Operation,
 - Permutation of Bits,
 - Add Round Key
 - Key-Schedule and Add-Round constant



State Representation



$$S = \begin{bmatrix} S_0 \\ S_1 \\ S_2 \\ S_3 \end{bmatrix} \leftarrow \begin{bmatrix} B_0 & || & B_1 & || & B_2 & || & B_3 \\ B_4 & || & B_5 & || & B_6 & || & B_7 \\ B_8 & || & B_9 & || & B_{10} & || & B_{11} \\ B_{12} & || & B_{13} & || & B_{14} & || & B_{15} \end{bmatrix}$$

$$KS = \begin{bmatrix} W_0 & || & W_1 \\ W_2 & || & W_3 \\ W_4 & || & W_5 \\ W_6 & || & W_7 \end{bmatrix} \leftarrow \begin{bmatrix} B_0 || B_1 & || & B_2 || B_3 \\ B_4 || B_5 & || & B_6 || B_7 \\ B_8 || B_9 & || & B_{10} || B_{11} \\ B_{12} || B_{13} & || & B_{14} || B_{15} \end{bmatrix}$$

Subcell Operation

$$S_1 \leftarrow S_1 \oplus (S_0 \& S_2)$$

$$S_0 \leftarrow S_0 \oplus (S_1 \& S_3)$$

$$S_2 \leftarrow S_2 \oplus (S_0 | S_1)$$

$$S_3 \leftarrow S_3 \oplus S_2$$

$$S_1 \leftarrow S_1 \oplus S_3$$

$$S_3 \leftarrow \sim S_3$$

$$S_2 \leftarrow S_2 \oplus (S_0 \& S_1)$$

$$\{S_0, S_1, S_2, S_3\} \leftarrow \{S_3, S_1, S_2, S_0\},$$

Bit Permutation Operation

Index	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
S_0	29	25	21	17	13	9	5	1	30	26	22	18	14	10	6	2
S_1	30	26	22	18	14	10	6	2	31	27	23	19	15	11	7	3
S_2	31	27	23	19	15	11	7	3	28	24	20	16	12	8	4	0
S_3	28	24	20	16	12	8	4	0	29	25	21	17	13	9	5	1

Index	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
S_0	31	27	23	19	15	11	7	3	28	24	20	16	12	8	4	0
S_1	28	24	20	16	12	8	4	0	29	25	21	17	13	9	5	1
S_2	29	25	21	17	13	9	5	1	30	26	22	18	14	10	6	2
S_3	30	26	22	18	14	10	6	2	31	27	23	19	15	11	7	3

$$b_{P(i)} \leftarrow b_i, i \in \{0, \dots, n-1\}$$

Add Round Key & Constant Operation

$$U \leftarrow W_2 \parallel W_3, \quad V \leftarrow W_6 \parallel W_7. \quad S = \begin{bmatrix} S_0 \\ S_1 \\ S_2 \\ S_3 \end{bmatrix} \leftarrow \begin{bmatrix} B_0 \parallel B_1 \parallel B_2 \parallel B_3 \\ B_4 \parallel B_5 \parallel B_6 \parallel B_7 \\ B_8 \parallel B_9 \parallel B_{10} \parallel B_{11} \\ B_{12} \parallel B_{13} \parallel B_{14} \parallel B_{15} \end{bmatrix}$$

$$RK = U \parallel V.$$

Add Round Key:

$$S_2 \leftarrow S_2 \oplus U,$$

$$S_1 \leftarrow S_1 \oplus V.$$

$$KS = \begin{bmatrix} W_0 \parallel W_1 \\ W_2 \parallel W_3 \\ W_4 \parallel W_5 \\ W_6 \parallel W_7 \end{bmatrix} \leftarrow \begin{bmatrix} B_0 \parallel B_1 \parallel B_2 \parallel B_3 \\ B_4 \parallel B_5 \parallel B_6 \parallel B_7 \\ B_8 \parallel B_9 \parallel B_{10} \parallel B_{11} \\ B_{12} \parallel B_{13} \parallel B_{14} \parallel B_{15} \end{bmatrix}$$

Add Round Constant:

$$S_3 \leftarrow S_3 \oplus 0x800000XY,$$

$$\text{byte } XY = 00c_5c_4c_3c_2c_1c_0$$

COFB Authenticated Encryption Mode

COFB has the following building blocks:

- Key and Block cipher: E_K
- Padding Function $\text{Pad}(x)$
- Feedback Function G

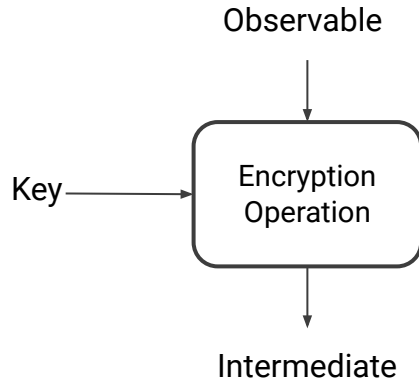
Algorithm COFB- $\mathcal{E}_K(N, A, M)$

1. $Y[0] \leftarrow E_K(N)$, $L \leftarrow \text{Trunc}_{n/2}(Y[0])$
2. $(A[1], \dots, A[a]) \xleftarrow{n} \text{Pad}(A)$
3. **if** $M \neq \epsilon$ **then**
4. $(M[1], \dots, M[m]) \xleftarrow{n} \text{Pad}(M)$
5. **for** $i = 1$ **to** $a - 1$
6. $L \leftarrow 2 \cdot L$
7. $X[i] \leftarrow A[i] \oplus G \cdot Y[i - 1] \oplus L \parallel 0^{n/2}$
8. $Y[i] \leftarrow E_K(X[i])$
9. **if** $|A| \bmod n = 0$ **and** $A \neq \epsilon$ **then** $L \leftarrow 3 \cdot L$
10. **else** $L \leftarrow 3^2 \cdot L$
11. **if** $M = \epsilon$ **then** $L \leftarrow 3^2 \cdot L$
12. $X[a] \leftarrow A[a] \oplus G \cdot Y[a - 1] \oplus L \parallel 0^{n/2}$
13. $Y[a] \leftarrow E_K(X[a])$
14. **for** $i = 1$ **to** $m - 1$
15. $L \leftarrow 2 \cdot L$
16. $C[i] \leftarrow M[i] \oplus Y[i + a - 1]$
17. $X[i + a] \leftarrow M[i] \oplus G \cdot Y[i + a - 1] \oplus L \parallel 0^{n/2}$
18. $Y[i + a] \leftarrow E_K(X[i + a])$
19. **if** $M \neq \epsilon$ **then**
20. **if** $|M| \bmod n = 0$ **then** $L \leftarrow 3 \cdot L$
21. **else** $L \leftarrow 3^2 \cdot L$
22. $C[m] \leftarrow M[m] \oplus Y[a + m - 1]$
23. $X[a + m] \leftarrow M[m] \oplus G \cdot Y[a + m - 1] \oplus L \parallel 0^{n/2}$
24. $Y[a + m] \leftarrow E_K(X[a + m])$
25. $C \leftarrow \text{Trunc}_{|M|}(C[1] \parallel \dots \parallel C[m])$
26. $T \leftarrow \text{Trunc}_\tau(Y[a + m])$
27. **else** $C \leftarrow \epsilon$, $T \leftarrow \text{Trunc}_\tau(Y[a])$
28. **return** (C, T)

Attack Design

Side Channel attacks require:

- Observable
- Intermediate

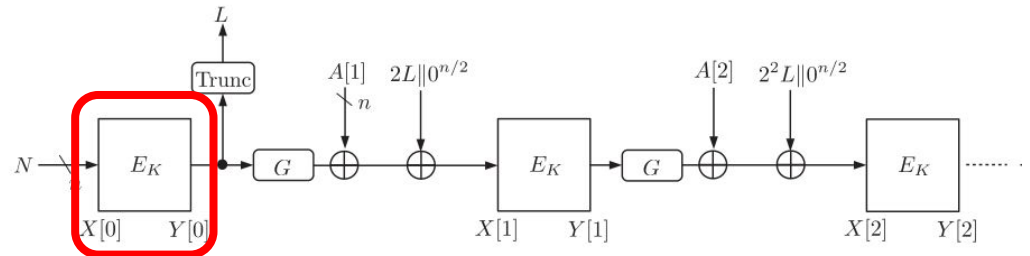


The nonce is encrypted to yield the first internal chaining value

Encryption on Nonce is the first block encryption component and is our point of attack

Algorithm $\text{COFB-}\mathcal{E}_K(N, A, M)$

1. $Y[0] \leftarrow E_K(N)$, $L \leftarrow \text{Trunc}_{n/2}(Y[0])$
2. $(A[1], \dots, A[a]) \xleftarrow{n} \text{Pad}(A)$
3. **if** $M \neq \epsilon$ **then**
4. $(M[1], \dots, M[m]) \xleftarrow{n} \text{Pad}(M)$
5. **for** $i = 1$ **to** $a - 1$
6. $L \leftarrow 2 \cdot L$
7. $Y[i] \leftarrow A[i] \oplus C \cdot Y[i-1] \oplus T \parallel 0^{n/2}$



Attack Design: 1

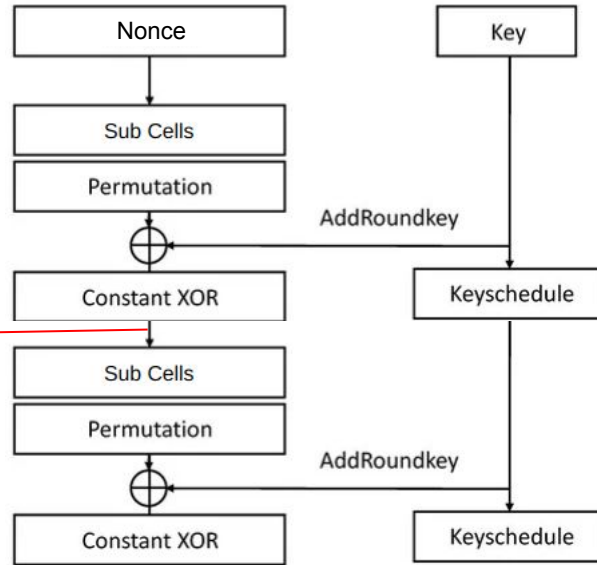
Keys retrieved:

$$KS = \begin{bmatrix} W_0 & \| & W_1 \\ W_2 & \| & W_3 \\ W_4 & \| & W_5 \\ W_6 & \| & W_7 \end{bmatrix} \leftarrow \begin{bmatrix} B_0 \| B_1 & \| & B_2 \| B_3 \\ B_4 \| B_5 & \| & B_6 \| B_7 \\ B_8 \| B_9 & \| & B_{10} \| B_{11} \\ B_{12} \| B_{13} & \| & B_{14} \| B_{15} \end{bmatrix}$$

Number of possible guesses:

- 256 per byte of key
- 2048 for all 8 bytes

Point of
Attack



```
S[2] ^= S[0] & S[1]
```

```
T = S[0]
S[0] = S[3]
S[3] = T
```

```
# ===PermBits=== #
S[0] = rowperm(S[0],0,3,2,1)
S[1] = rowperm(S[1],1,0,3,2)
S[2] = rowperm(S[2],2,1,0,3)
S[3] = rowperm(S[3],3,2,1,0)
```

```
# ===AddRoundKey=== #
```

```
#keyindex = 4: gives W2[0]
if key_index == 4:
    intermediate = ((S[2] >> 0) & 0b11111111) ^ keyguess
#keyindex = 5: gives W2[1]
elif key_index == 5:
    intermediate = ((S[2] >> 8) & 0b11111111) ^ keyguess
#keyindex = 6: gives W3[0]
elif key_index == 6:
    intermediate = ((S[2] >> 16) & 0b11111111) ^ keyguess
#keyindex = 7: gives W3[1]
elif key_index == 7:
    intermediate = ((S[2] >> 24) & 0b11111111) ^ keyguess
#keyindex = 12: gives W6[0]
elif key_index == 12:
    intermediate = ((S[1] >> 0) & 0b11111111) ^ keyguess
#keyindex = 13: gives W6[1]
elif key_index == 13:
    intermediate = ((S[1] >> 8) & 0b11111111) ^ keyguess
#keyindex = 14: gives W7[0]
elif key_index == 14:
    intermediate = ((S[1] >> 16) & 0b11111111) ^ keyguess
#keyindex = 15: gives W7[1]
elif key_index == 15:
    intermediate = ((S[1] >> 24) & 0b11111111) ^ keyguess
```

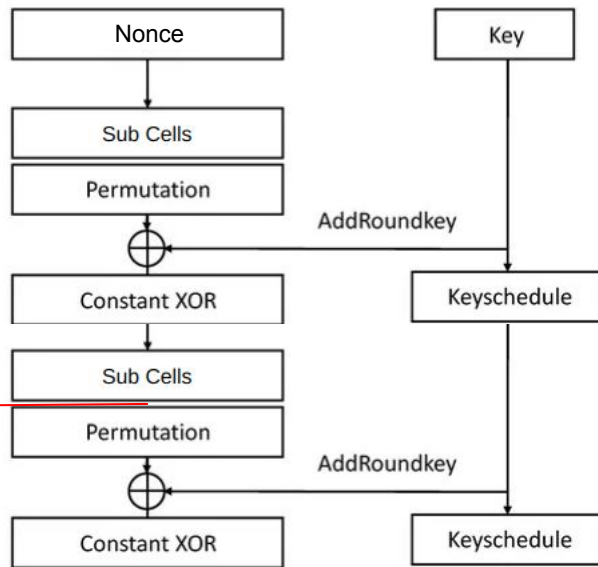
```
return intermediate
```

Attack Design: 2

Keys retrieved:

$$KS = \begin{bmatrix} W_0 & \| & W_1 \\ W_2 & \| & W_3 \\ W_4 & \| & W_5 \\ W_6 & \| & W_7 \end{bmatrix} \leftarrow \begin{bmatrix} B_0 \| B_1 & \| & B_2 \| B_3 \\ B_4 \| B_5 & \| & B_6 \| B_7 \\ B_8 \| B_9 & \| & B_{10} \| B_{11} \\ B_{12} \| B_{13} & \| & B_{14} \| B_{15} \end{bmatrix}$$

Point of
Attack



```

S[3] ^= 0xffffffff
S[2] ^= S[0] & S[1]

```

```

T = S[0]
S[0] = S[3]
S[3] = T

```

```

# ===PermBits=== #
S[0] = rowperm(S[0],0,3,2,1)
S[1] = rowperm(S[1],1,0,3,2)
S[2] = rowperm(S[2],2,1,0,3)
S[3] = rowperm(S[3],3,2,1,0)

```

```

#Add Round Constant operation
S[3] ^= 0x80000000 ^ GIFT_RC[1]

```

```

#Add Round Key Operation - For the byte from S1 and S2
S1dash = ((S[1]>>(key_index*8)) & 0xff) ^ keyguess1
S2dash = ((S[2]>>(key_index*8)) & 0xff) ^ keyguess2

```

```

#Choosing only the byte from S0 and S3 that is being a
S0dash = (S[0]>>(key_index*8)) & 0xff
S3dash = (S[3]>>(key_index*8)) & 0xff

```

```

#Part of Subcell Operation to increase nonlinearity
S1dashdash = S1dash ^ (S0dash & S2dash)
S0dashdash = S0dash ^ (S1dashdash & S3dash)

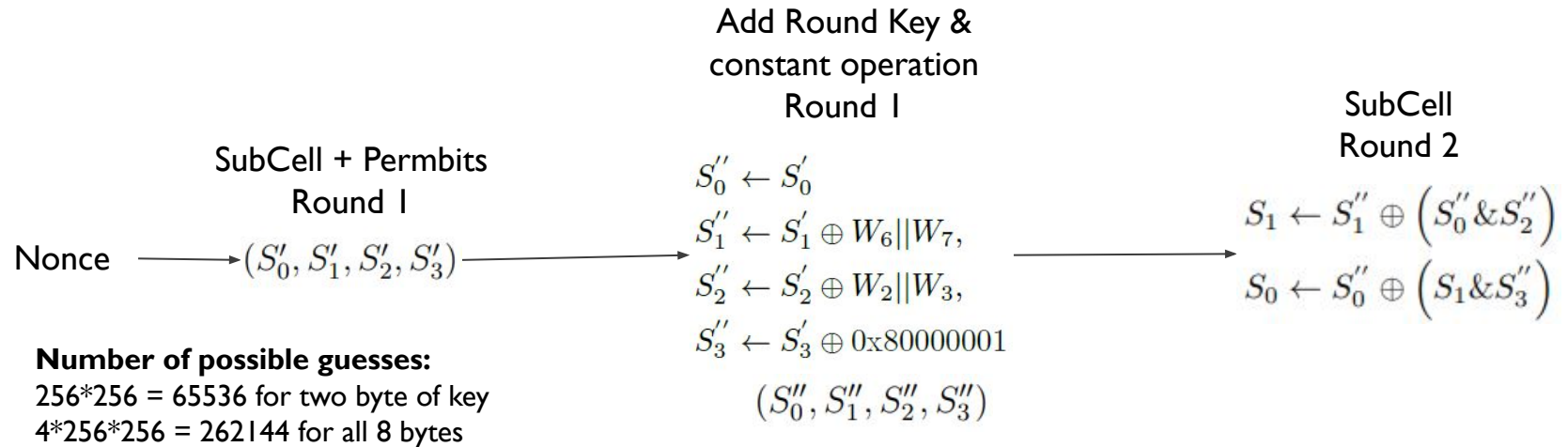
```

```

#Return Immediate Value
intermediate = S0dashdash
return intermediate

```

Attack Design: 2



Work Split up

Arun Krishna: Hardware setup, Capturing Power Trace, CPA design

Yogasanthoshi: DOM, CPA Design

Drive Link (containing codes, presentation video):

<https://drive.google.com/drive/folders/1vmNPdr0VXj3OygLR3y7RHXDDj5G2sD2X?usp=sharing>

Thank You !!