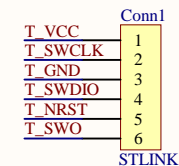
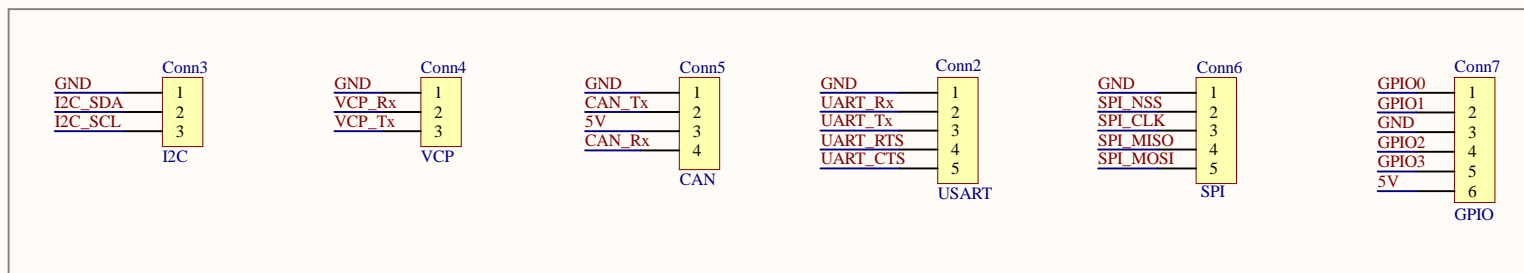


MAIN DEBUGGING CONNECTORS

ST-LINK Discovery
Interface Conn



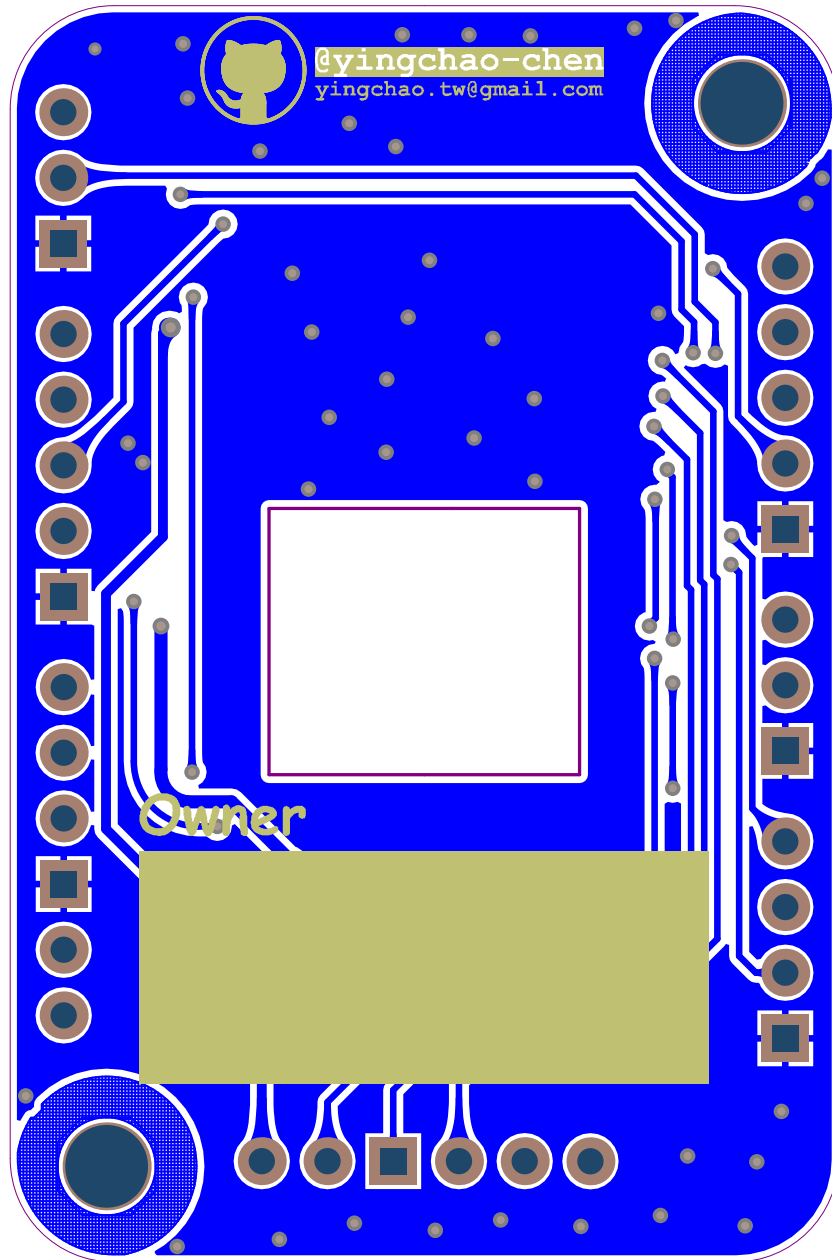
AUX PROGRAMMING CONNECTORS

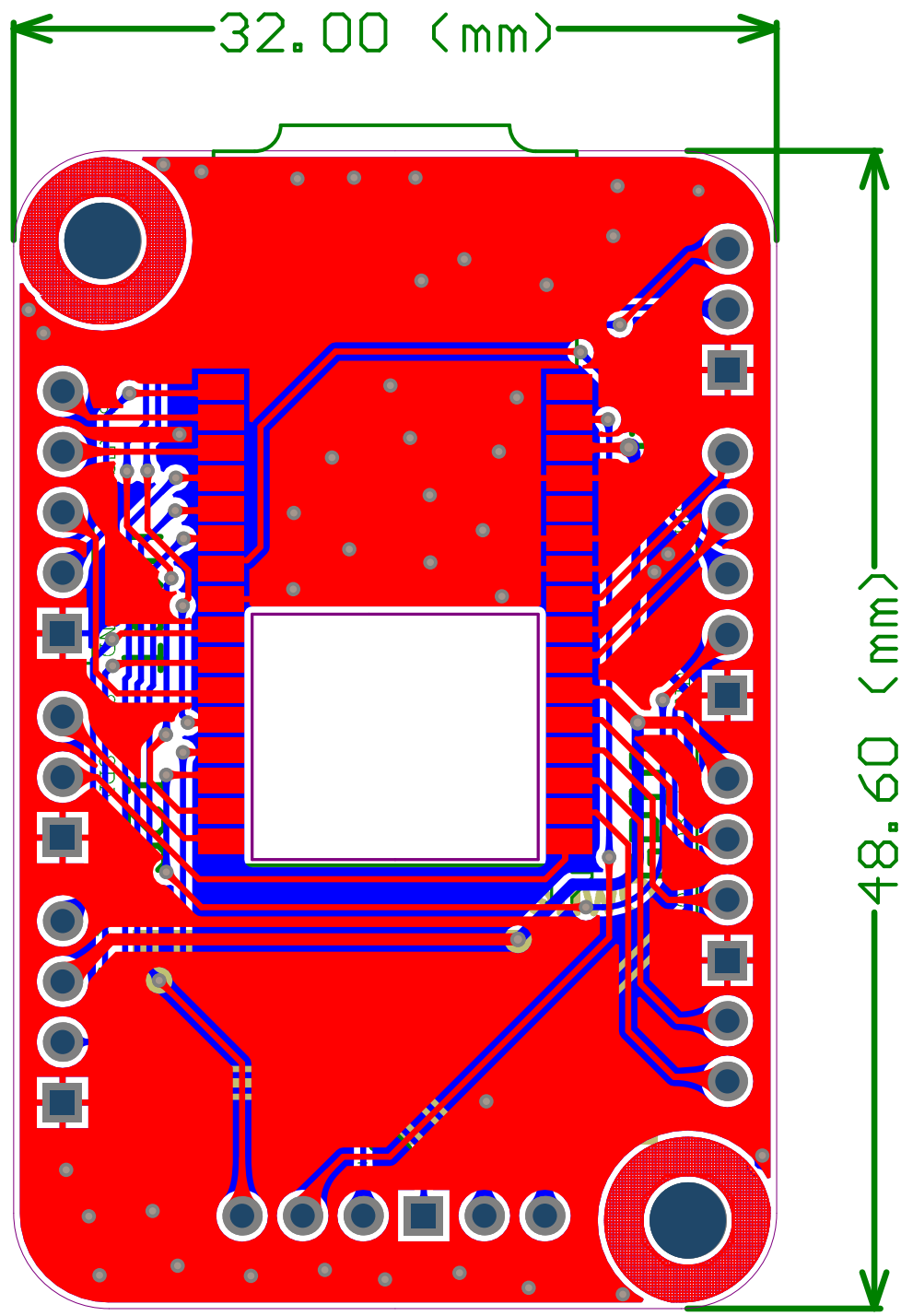


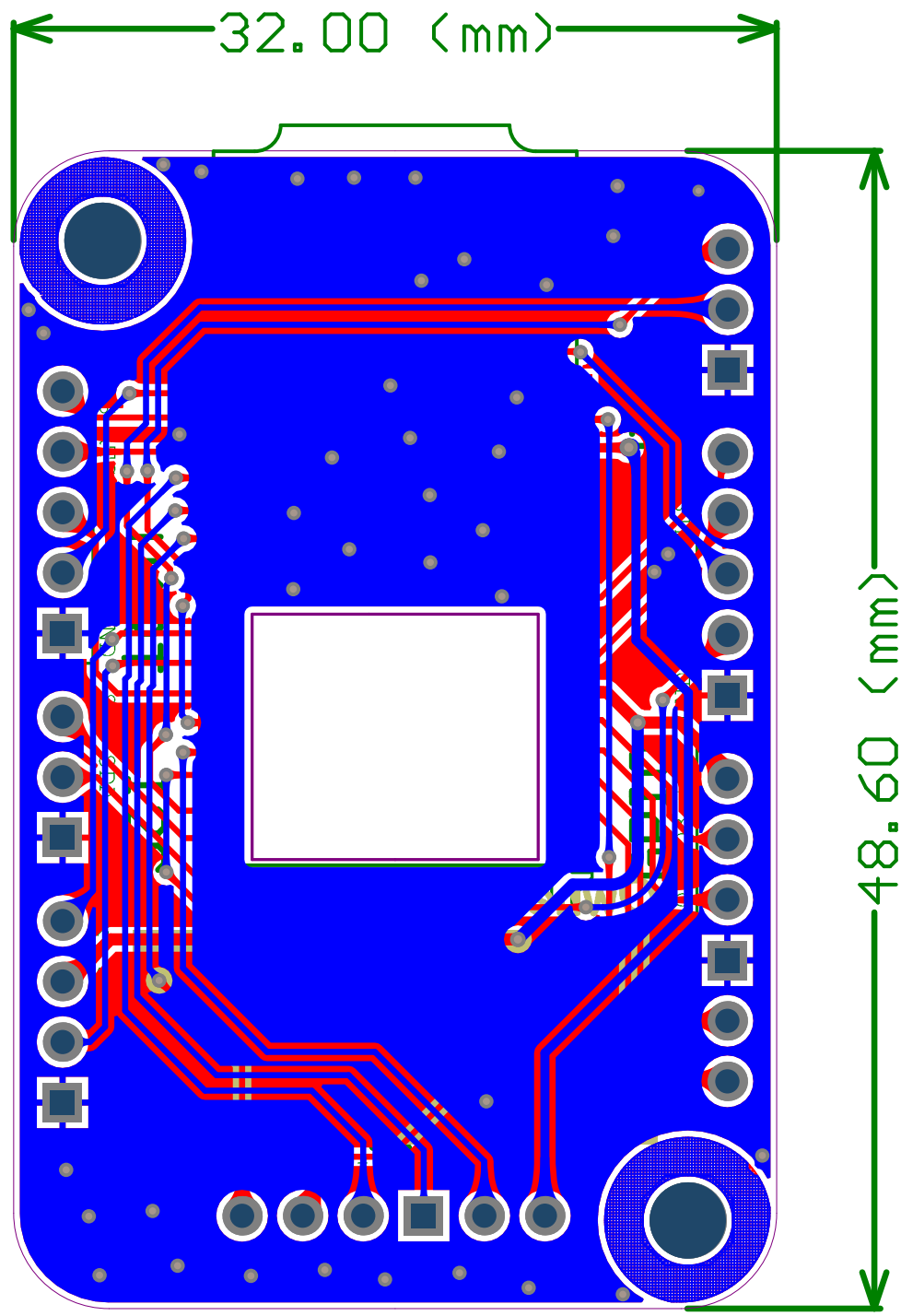
Designer:@yingchao-chen

Date:06/01/20

Email:yingchao.tw@gmail.com







Comment	Description	Designator	Footprint	LibRef	Quantity
MB1467		Board_1	MB1467	MB1467	1
STLINK	Header, 6-Pin	Conn1	CONNECT_6P_90	CONN_06	1
USART	Header, 5-Pin	Conn2	CONNECT_5P	CONN_05	1
I2C	Header, 3-Pin	Conn3	CONNECT_3P	CONN_03	1
VCP	Header, 3-Pin	Conn4	CONNECT_3P	CONN_03	1
CAN	Header, 4-Pin	Conn5	CONNECT_4P	CONN_04	1
SPI	Header, 5-Pin	Conn6	CONNECT_5P	CONN_05	1
GPIO	Header, 6-Pin	Conn7	CONNECT_6P	CONN_06	1