A

Board_1

UART_Rx	1	Bridge_UART_Rx	Bridge_I2C_SDA	17	I2C_SDA
UART_CTS	2	Bridge_UART_CTS	Bridge_GPIO0	18	GPIO0
UART_RTS	3	Bridge_UART_RTS	Bridge_GPIO1	19	GPIO1
T_SWDIO	4	T_JTMS/T_SWDIO	Bridge_GPIO2	20	GPIO2
T_GND	5	GNDDetect	Bridge_GPIO3	21	GPIO3
T_SWO	6	T_JTDO/T_SWO	Reserved	22	5V
SPI_CLK	7	Bridge_SPI_CLK	Bridge_SPI_MISO	23	SPI_MISO
	8	GND	GND	24
CAN_Rx	9	Bridge_CAN_Rx	Bridge_SPI_MOSI	25	SPI_MOSI
CAN_Tx	10	Bridge_CAN_Tx	GND	26
UART_Tx	11	Bridge_UART_Tx	GND	27
VCP_Tx	12	T_VCP_TX	T_JTDI/NC	28
T_SWCLK	13	T_JCLK/T_SWCLK	GND	29
SPI_NSS	14	Bridge_SPI_NSS	T_VCC	30	T_VCC
VCP_Rx	15	T_VCP_Rx	T_NRST	31	T_NRST
I2C_SCL	16	Bridge_I2C_SCL	T_SW_DIR	32

MB1467

GND

GND

B

MAIN DEBUGGING CONNECTORS

ST-LINK Discovery
Interface Conn

Conn1

T_VCC	1
T_SWCLK	2
T_GND	3
T_SWDIO	4
T_NRST	5
T_SWO	6
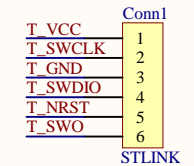
STLINK

AUX PROGRAMMING CONNECTORS

Conn3
GND	1
I2C_SDA	2
I2C_SCL	3
I2C

Conn4
GND	1
VCP_Rx	2
VCP_Tx	3
VCP

Conn5
GND	1
CAN_Tx	2
5V	3
CAN_Rx	4
CAN

Conn2
GND	1
UART_Rx	2
UART_Tx	3
UART_RTS	4
UART_CTS	5
USART

Conn6
GND	1
SPI_NSS	2
SPI_CLK	3
SPI_MISO	4
SPI_MOSI	5
SPI

Conn7
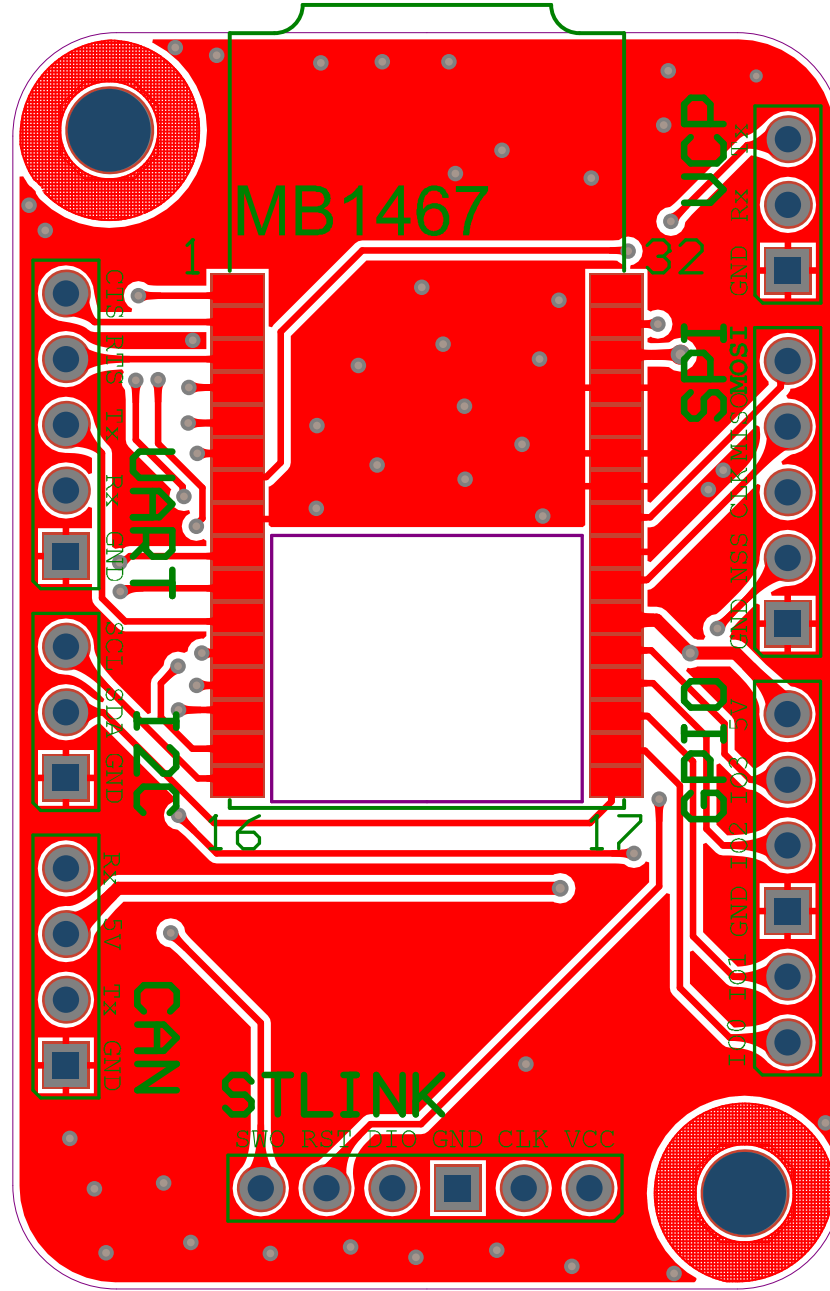| GPIO0 | 1 |
GPIO1	2
GND	3
GPIO2	4
GPIO3	5
5V	6
GPIO
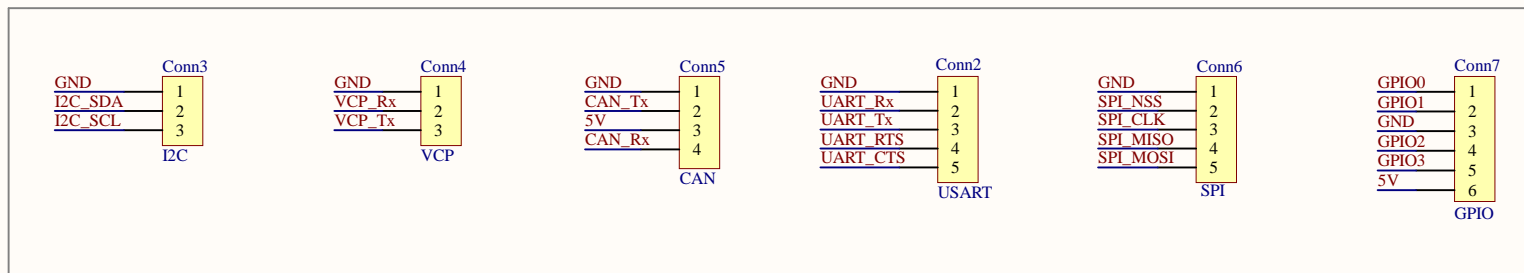
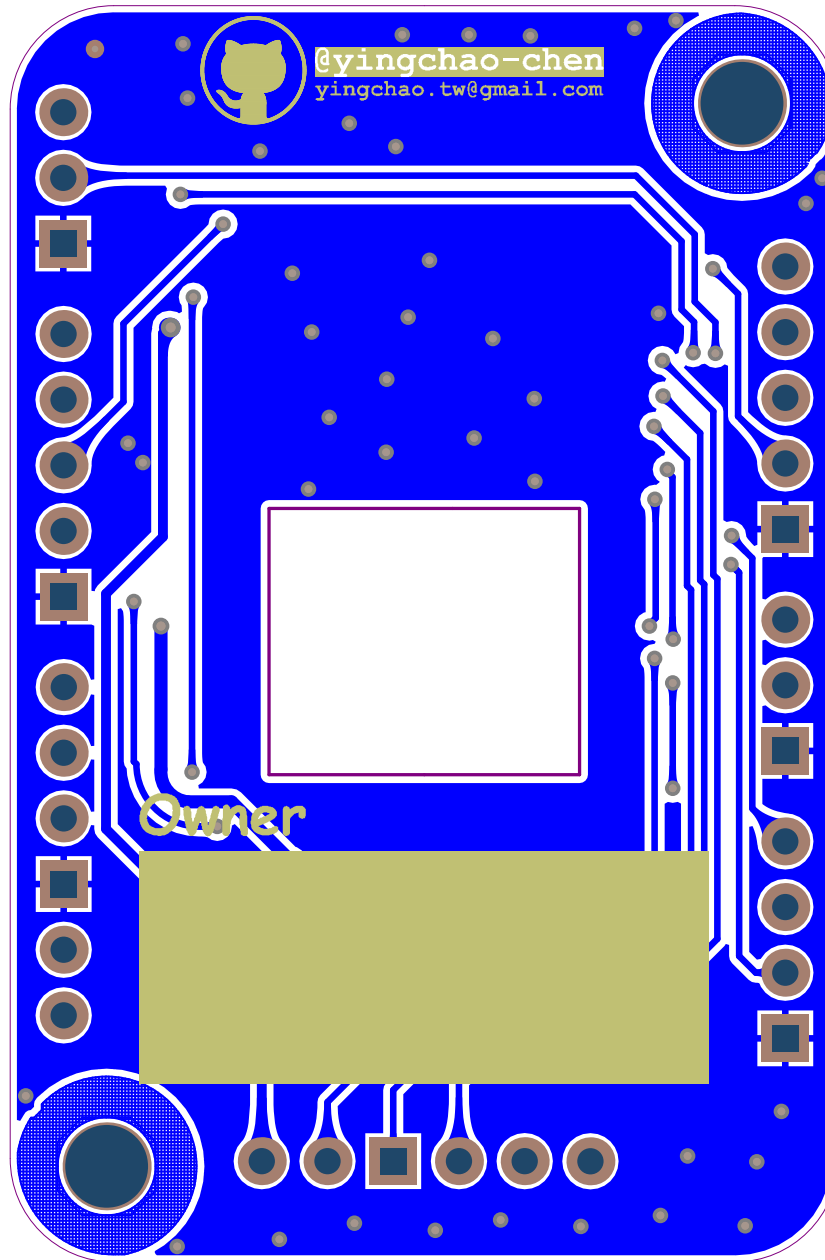Cannot open file E:\[電路]
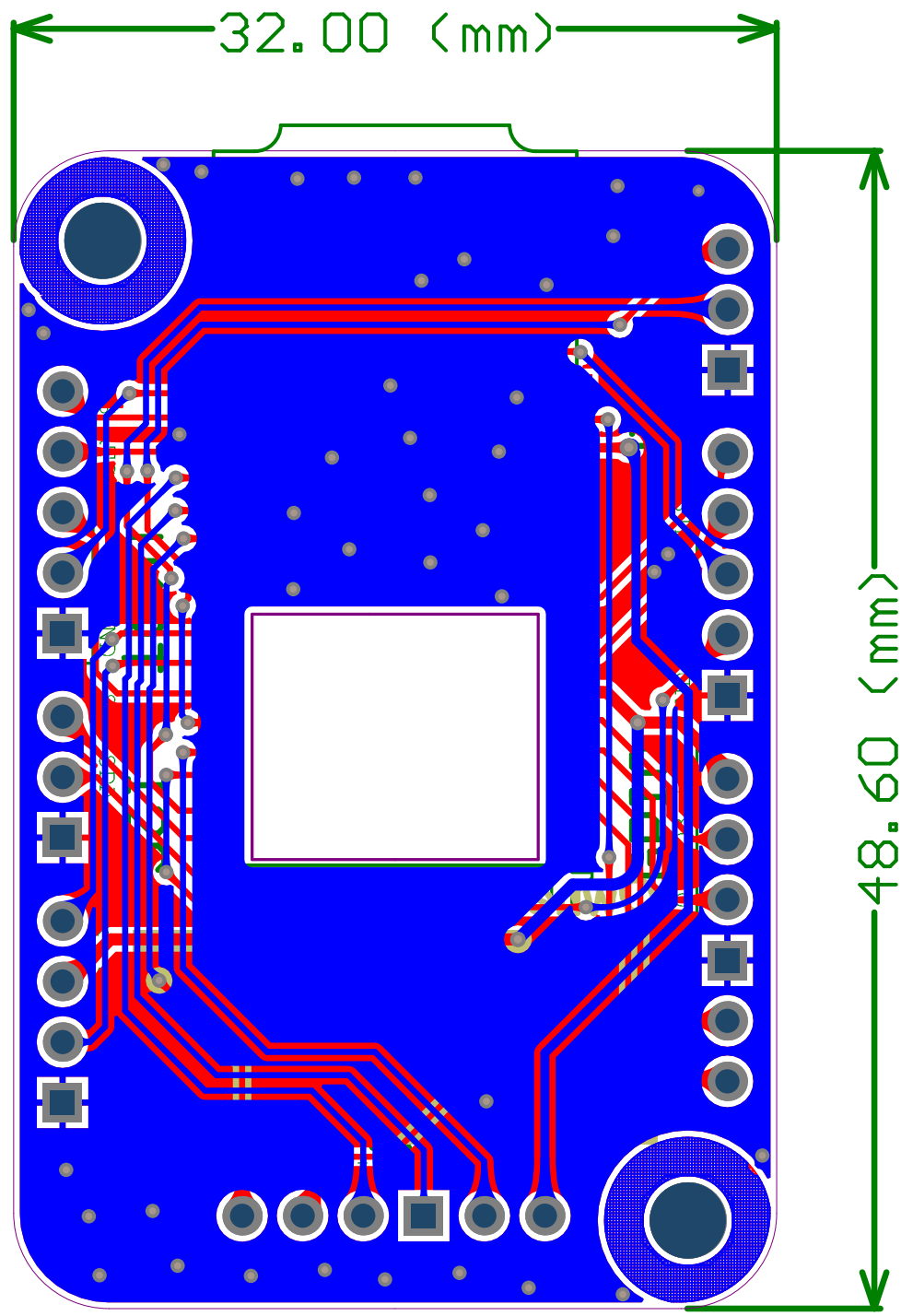STLINK-V3\Image\QR-Code.
bmp

C

Designer:@yingchao-chen

Date:06/01/20

Email:yingchao.tw@gmail.com

D

MB1467

I2C

SPI

UART

I2C

GPIO

CAN

STLINK

SWO RST DIO GND CLK VCC

1

32

16

17

@yingchao-chen
yingchao.tw@gmail.com

Owner

32.00 （mm）

48.60 （mm）

32.00 (mm)

48.60 (mm)
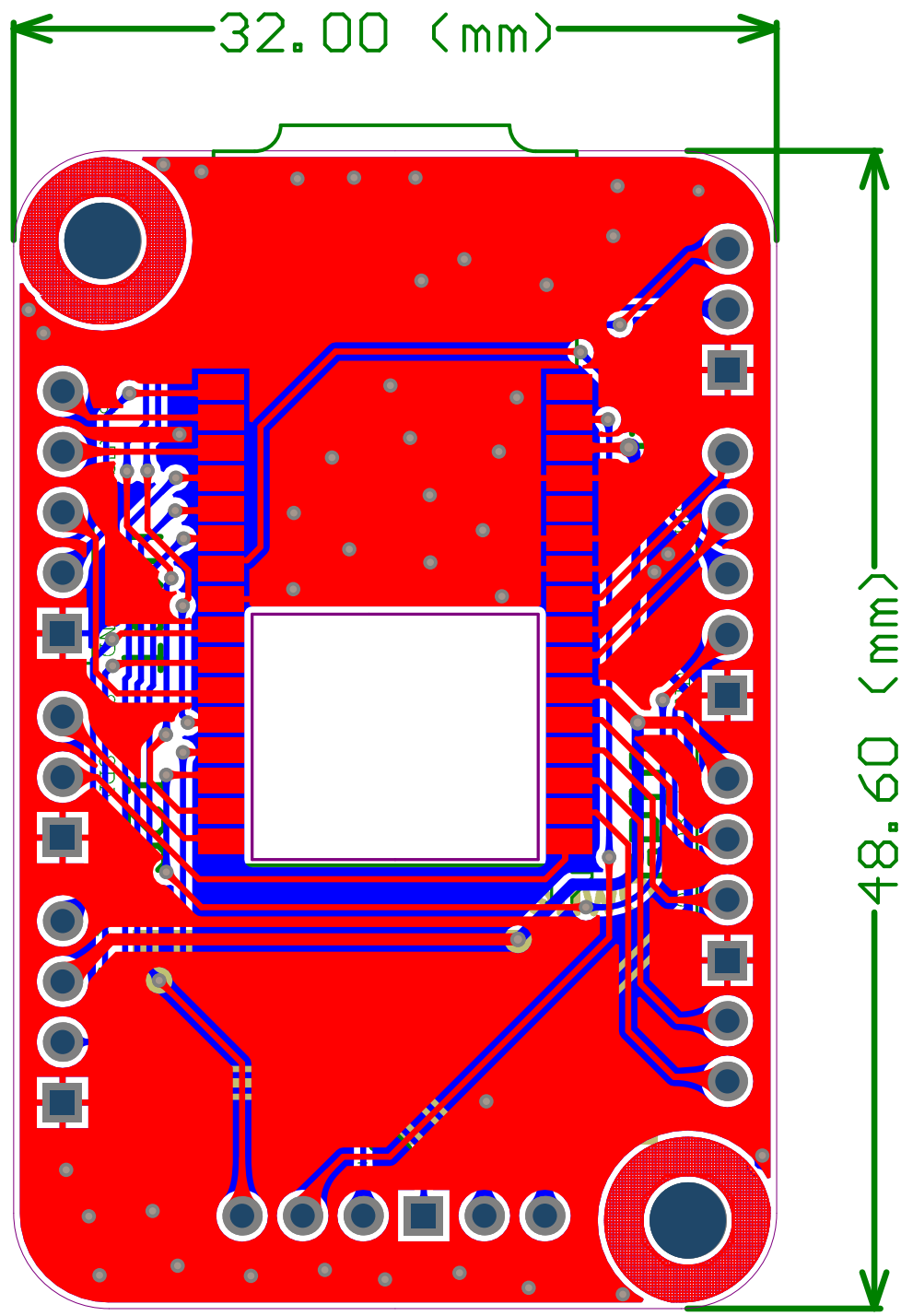
Comment	Description	Designator	Footprint	LibRef	Quantity
MB1467		Board_1	MB1467	MB1467	1
STLINK	Header, 6-Pin	Conn1	CONNECT_6P_90	CONN_06	1
USART	Header, 5-Pin	Conn2	CONNECT_5P	CONN_05	1
I2C	Header, 3-Pin	Conn3	CONNECT_3P	CONN_03	1
VCP	Header, 3-Pin	Conn4	CONNECT_3P	CONN_03	1
CAN	Header, 4-Pin	Conn5	CONNECT_4P	CONN_04	1
SPI	Header, 5-Pin	Conn6	CONNECT_5P	CONN_05	1
GPIO	Header, 6-Pin	Conn7	CONNECT_6P	CONN_06	1