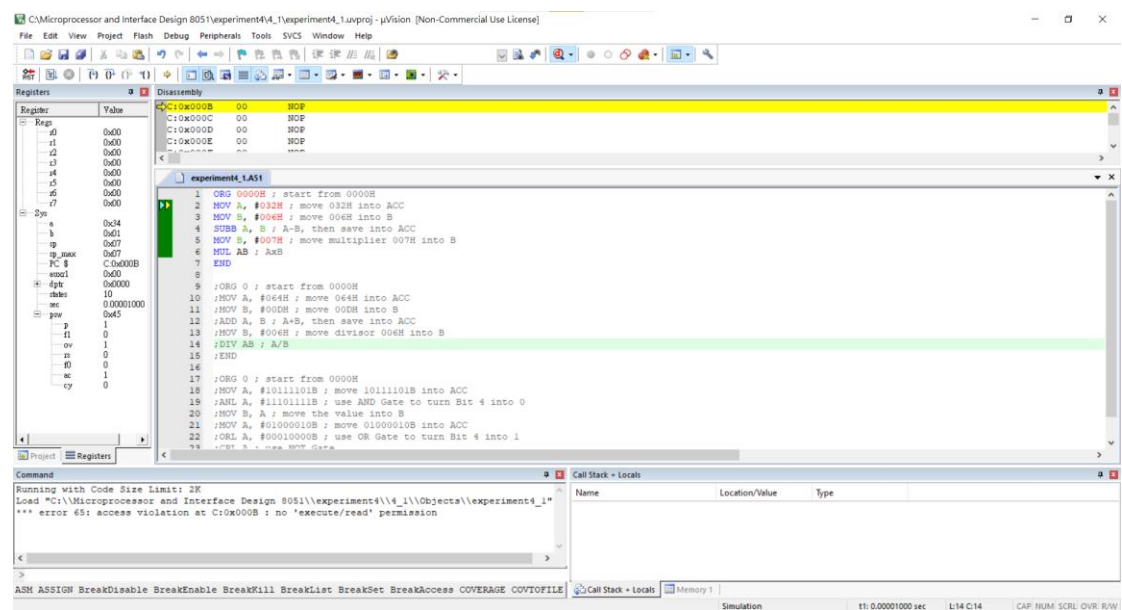
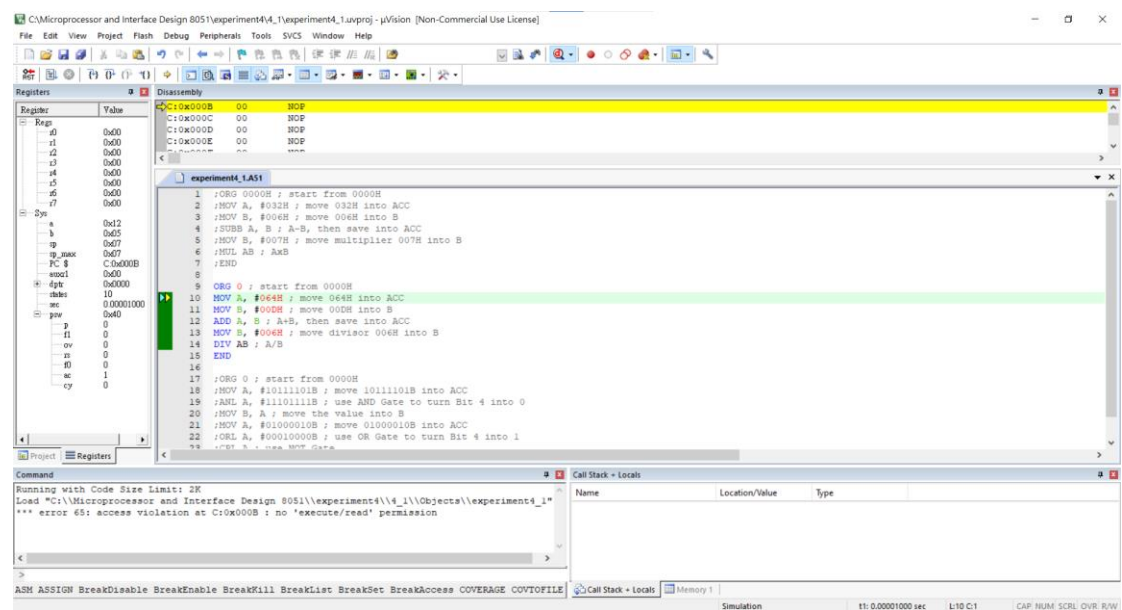


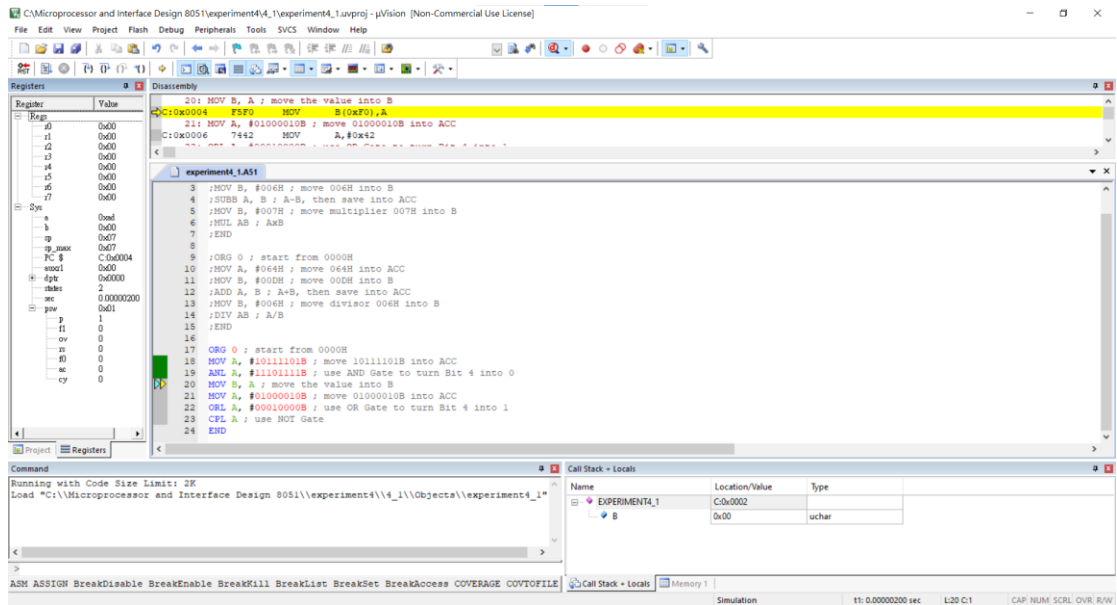
(32H-6H) *7H



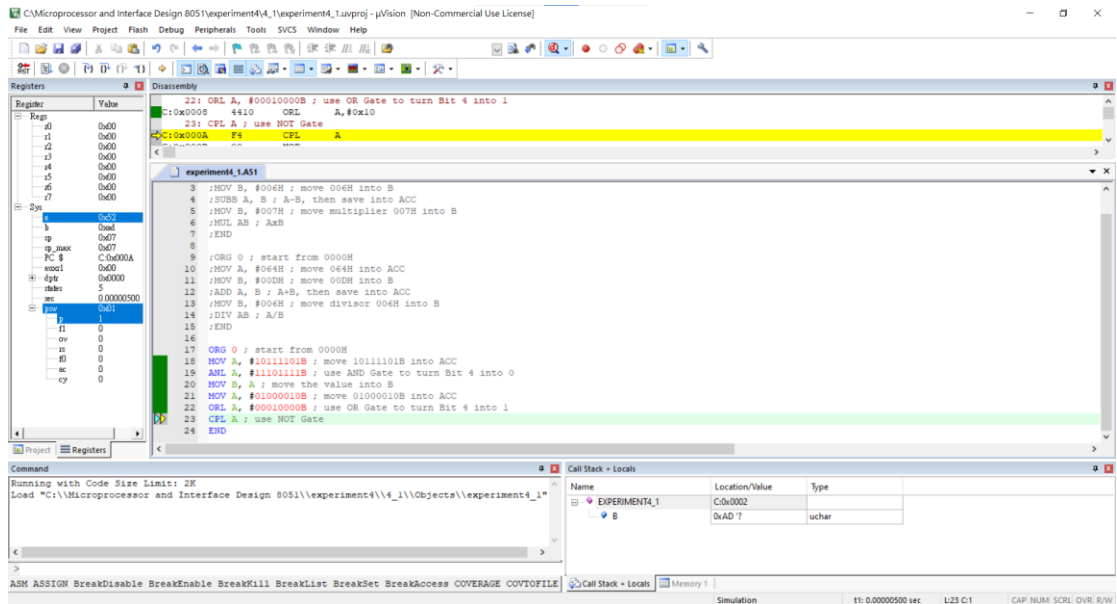
(64H+DH) /6H



ANL A, #11101111B



ORL A, #0001000B



CPL A

C:\Microprocessor and Interface Design 8051\experiment4\4_1\experiment4_1.uvproj - uVision [Non-Commercial Use License]

File Edit View Project Flash Debug Peripherals Tools SVCS Window Help

Registers

| Register | Value |
|----------|-------|
| R0 | 0x00 |
| R1 | 0x00 |
| R2 | 0x00 |
| R3 | 0x00 |
| R4 | 0x00 |
| R5 | 0x00 |
| R6 | 0x00 |
| R7 | 0x00 |

Disassembly

```
22: ORL A, #00010000B ; use OR Gate to turn Bit 4 into 1
C10x0008 4410 ORL A, #0x10
23: CPL A ; use NOT Gate
C10x000A F5 CPL A
C10x000C 00 NOP
C10x000E 00 NOP
C10x0010 00 NOP
C10x0012 00 NOP
C10x0014 00 NOP
C10x0016 00 NOP
C10x0018 00 NOP
C10x001A 00 NOP
C10x001C 00 NOP
C10x001E 00 NOP
C10x0020 00 NOP
C10x0022 00 NOP
C10x0024 00 NOP
C10x0026 00 NOP
C10x0028 00 NOP
C10x002A 00 NOP
C10x002C 00 NOP
C10x002E 00 NOP
C10x0030 00 NOP
C10x0032 00 NOP
C10x0034 00 NOP
C10x0036 00 NOP
C10x0038 00 NOP
C10x003A 00 NOP
C10x003C 00 NOP
C10x003E 00 NOP
C10x0040 00 NOP
C10x0042 00 NOP
C10x0044 00 NOP
C10x0046 00 NOP
C10x0048 00 NOP
C10x004A 00 NOP
C10x004C 00 NOP
C10x004E 00 NOP
C10x0050 00 NOP
C10x0052 00 NOP
C10x0054 00 NOP
C10x0056 00 NOP
C10x0058 00 NOP
C10x005A 00 NOP
C10x005C 00 NOP
C10x005E 00 NOP
C10x0060 00 NOP
C10x0062 00 NOP
C10x0064 00 NOP
C10x0066 00 NOP
C10x0068 00 NOP
C10x006A 00 NOP
C10x006C 00 NOP
C10x006E 00 NOP
C10x0070 00 NOP
C10x0072 00 NOP
C10x0074 00 NOP
C10x0076 00 NOP
C10x0078 00 NOP
C10x007A 00 NOP
C10x007C 00 NOP
C10x007E 00 NOP
C10x0080 00 NOP
C10x0082 00 NOP
C10x0084 00 NOP
C10x0086 00 NOP
C10x0088 00 NOP
C10x008A 00 NOP
C10x008C 00 NOP
C10x008E 00 NOP
C10x0090 00 NOP
C10x0092 00 NOP
C10x0094 00 NOP
C10x0096 00 NOP
C10x0098 00 NOP
C10x009A 00 NOP
C10x009C 00 NOP
C10x009E 00 NOP
C10x00A0 00 NOP
C10x00A2 00 NOP
C10x00A4 00 NOP
C10x00A6 00 NOP
C10x00A8 00 NOP
C10x00AA 00 NOP
C10x00AC 00 NOP
C10x00AE 00 NOP
C10x00B0 00 NOP
C10x00B2 00 NOP
C10x00B4 00 NOP
C10x00B6 00 NOP
C10x00B8 00 NOP
C10x00BA 00 NOP
C10x00BC 00 NOP
C10x00BE 00 NOP
C10x00C0 00 NOP
C10x00C2 00 NOP
C10x00C4 00 NOP
C10x00C6 00 NOP
C10x00C8 00 NOP
C10x00CA 00 NOP
C10x00CC 00 NOP
C10x00CE 00 NOP
C10x00D0 00 NOP
C10x00D2 00 NOP
C10x00D4 00 NOP
C10x00D6 00 NOP
C10x00D8 00 NOP
C10x00DA 00 NOP
C10x00DC 00 NOP
C10x00DE 00 NOP
C10x00E0 00 NOP
C10x00E2 00 NOP
C10x00E4 00 NOP
C10x00E6 00 NOP
C10x00E8 00 NOP
C10x00EA 00 NOP
C10x00EC 00 NOP
C10x00EE 00 NOP
C10x00F0 00 NOP
C10x00F2 00 NOP
C10x00F4 00 NOP
C10x00F6 00 NOP
C10x00F8 00 NOP
C10x00FA 00 NOP
C10x00FC 00 NOP
C10x00FE 00 NOP
C10x0000 00 NOP
C10x0001 00 NOP
C10x0002 00 NOP
C10x0003 00 NOP
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C10x0005 00 NOP
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C10x0007 00 NOP
C10x0008 00 NOP
C10x0009 00 NOP
C10x000A 00 NOP
C10x000B 00 NOP
C10x000C 00 NOP
C10x000D 00 NOP
C10x000E 00 NOP
C10x000F 00 NOP
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C10x0011 00 NOP
C10x0012 00 NOP
C10x0013 00 NOP
C10x0014 00 NOP
C10x0015 00 NOP
C10x0016 00 NOP
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C10x0018 00 NOP
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C10x001C 00 NOP
C10x001D 00 NOP
C10x001E 00 NOP
C10x001F 00 NOP
C10x0020 00 NOP
C10x0021 00 NOP
C10x0022 00 NOP
C10x0023 00 NOP
C10x0024 00 NOP
C10x0025 00 NOP
C10x0026 00 NOP
C10x0027 00 NOP
C10x0028 00 NOP
C10x0029 00 NOP
C10x002A 00 NOP
C10x002B 00 NOP
C10x002C 00 NOP
C10x002D 00 NOP
C10x002E 00 NOP
C10x002F 00 NOP
C10x0030 00 NOP
C10x0031 00 NOP
C10x0032 00 NOP
C10x0033 00 NOP
C10x0034 00 NOP
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C10x0047 00 NOP
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C10x0057 00 NOP
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C10x005E 00 NOP
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C10x0070 00 NOP
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C10x007A 00 NOP
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C10x007C 00 NOP
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C10x00B1 00 NOP
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C10x00B5 00 NOP
C10x00B6 00 NOP
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C10x00C3 00 NOP
C10x00C4 00 NOP
C10x00C5 00 NOP
C10x00C6 00 NOP
C10x00C7 00 NOP
C10x00C8 00 NOP
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C10x00D4 00 NOP
C10x00D5 00 NOP
C10x00D6 00 NOP
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C10x00F5 00 NOP
C10x00F6 00 NOP
C10x00F7 00 NOP
C10x00F8 00 NOP
C10x00F9 00 NOP
C10x00FA 00 NOP
C10x00FB 00 NOP
C10x00FC 00 NOP
C10x00FD 00 NOP
C10x00FE 00 NOP
C10x00FF 00 NOP
```

experiment4_1.A51

```
3 ;MOV B, #006H ; move 006H into B
4 ;SUBB A, B ; A-B, then save into ACC
5 ;MOV B, #007H ; move multiplier 007H into B
6 ;MUL AB ; A*B
7 ;END
8
9 ;ORG 0 ; start from 0000H
10 ;MOV A, #064H ; move 064H into ACC
11 ;MOV B, #008H ; move 008H into B
12 ;ADD A, B ; A+B, then save into ACC
13 ;MOV B, #006H ; move divisor 006H into B
14 ;DIV AB ; A/B
15 ;END
16
17 ORG 0 ; start from 0000H
18 MOV A, #10111010B ; move 10111010B into ACC
19 ANL A, #11101111B ; use AND Gate to turn Bit 4 into 0
20 MOV B, A ; move the value into B
21 MOV A, #10000010B ; move 10000010B into ACC
22 ORL A, #00010000B ; use OR Gate to turn Bit 4 into 1
23 CPL A ; use NOT Gate
24 END
```

Command

Running with Code Size Limit: 2K
Load "C:\Microprocessor and Interface Design 8051\experiment4\4_1\Objects\experiment4_1"
*** error 65: access violation at C10x000B : no 'execute/read' permission

Call Stack - locals

| Name | Location/Value | Type |
|---------------|----------------|-------|
| EXPERIMENT4_1 | C0x0002 | |
| B | 0xAD ? | uchar |

Simulation

tt: 0.00000600 sec L17 C:25 CAP: NUM: SCRL: OVR: R/W