



Table 22. Boot mode when OTP BOOT_SRC bits are zero				
Boot mode	P2_9	P2_8	P1_2	P1_1
USART0	LOW	LOW	LOW	LOW
SPIFI	LOW	LOW	LOW	HIGH
EMC 8-bit	LOW	LOW	HIGH	LOW
EMC 16-bit	LOW	LOW	HIGH	HIGH
EMC 32-bit	LOW	HIGH	LOW	LOW
USB0	LOW	HIGH	LOW	HIGH
USB1	LOW	HIGH	HIGH	LOW
SPI (SSP)	LOW	HIGH	HIGH	HIGH
USART3	HIGH	LOW	LOW	LOW

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