

ECE 383 - Embedded Computer Systems II Lecture 24 - Direct Digital Synthesis and Linear Interpolation



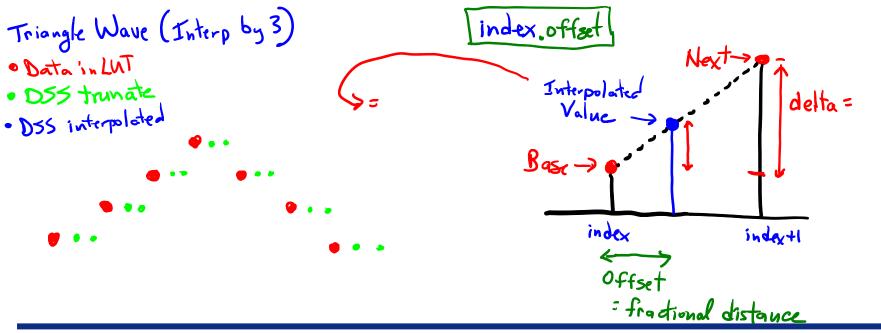
$$f = \frac{F_s \cdot x}{a^n}$$

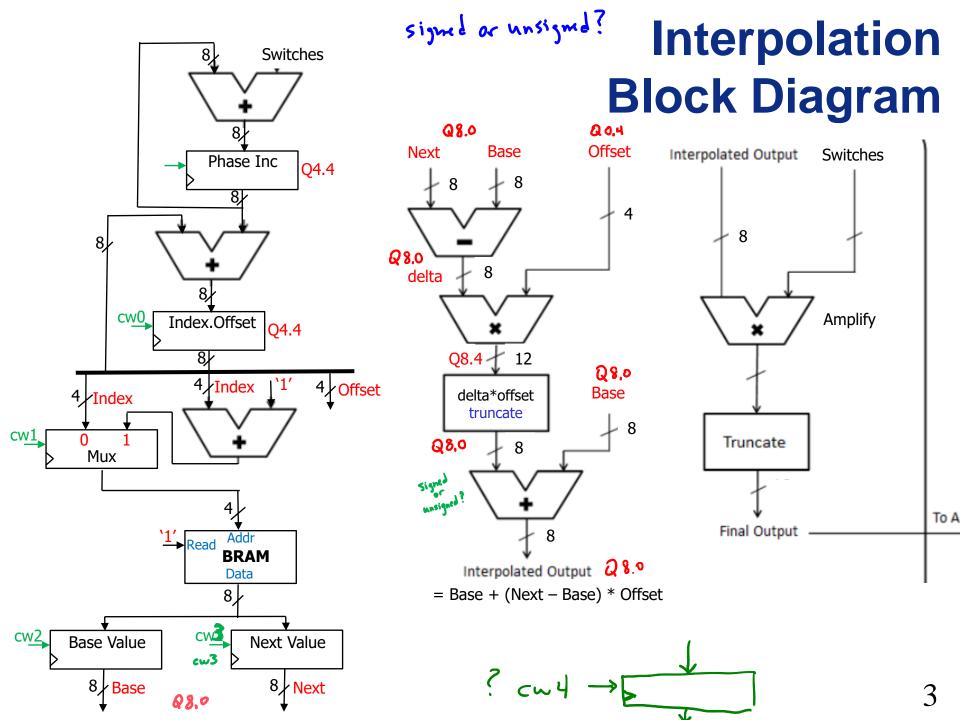
Lesson Outline

Project Proposals Due BOC TODAY

Jump 15 X = 0.3333

Direct Digital Synthesis with Interpolation









- Let's say that you wanted to compute the square root of an value using the microBlaze.
- If you were lucky enough to have a compiler which provided this function you could just use the math library functions.
- If on the other hand, you did not have the use of such a library, you would have to figure out a way to compute the square root.



- There are many ways to compute the SQRT Function:
- Crack open a book to find a mathematical expression
 - Unfortunately this typically leads to timely computations
- You could enumerate an every possible value of x and its square root.
 - We could then <u>look-up</u> a SQRT in the table, by going to the row corresponding to the x and retrieving its value.
 - This approach seems silly since it would use a lot of space
 - We could reduce this space by eliminating entries
 - We save space at the expense of introducing errors.
- If you wanted the SQRT for x and its entry wasn't in the table you would have to use the closest x in the table



- A good compromise among all three of these design constraints:
 - Space
 - Time
 - Error
- Is to use Interpolation in a partial Look-Up-Table (LUT)

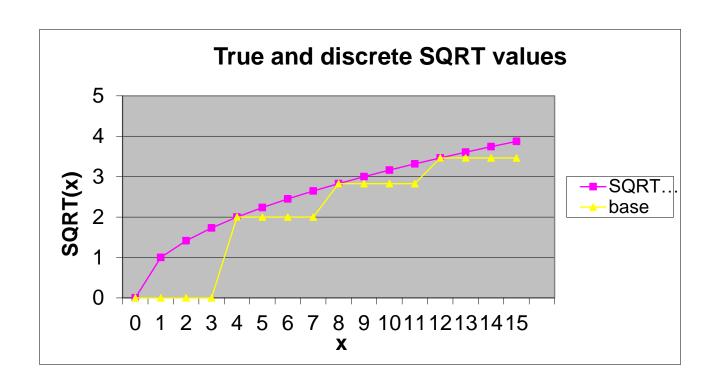


Interpolation

- "Interpolation is a mathematical method of creating missing data. ... There are many methods of interpolation, but one simple method would be to generate a new value by using the average of the value of the two values on either side of the one to be created."
- This average is also referred to as linear interpolation.
- For example if you have a value of x which is 1/2 way between 0 and 4 then you assume that the SQRT is 1/2 between 0 and 2.



Example Interpolation

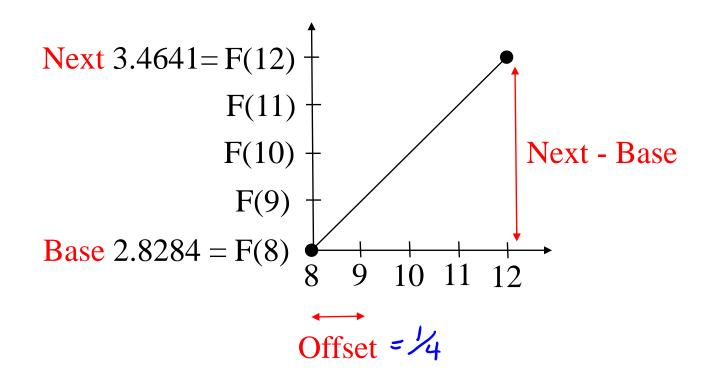




Linear Interpolation

- For example, let: F(8)=2.8284 and F(12)=3.4641 then
 - F(9) = 2.8284 + 1/4(3.4641-2.8284) = 2.987
 - F(10) = 2.8284 + 2/4(3.4641-2.8284) = 3.146
 - F(11) = 2.8284 + 3/4(3.4641-2.8284) = 3.305
- We understand that this is an approximation and consequently we will have error, but sometimes close is better than exact in embedded computing especially when time is of the essence.







Linear Interpolation

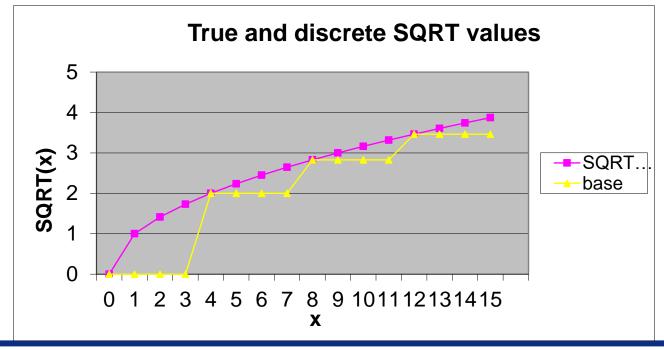
```
A code chunk to perform linear
 interpolation of some unknown fnc at
x+i where i is between 0 and 4
inclusive. You are given that
f(x)=y
                f(x+4)=z
You are given i, please return f(x+i)
 delta = (z-y) >> 2;
                                 delta = (i^*(z-y))>>2;
 f = y + delta*i;
                                 f = y + delta;
```

It would be better to do the division by 4 (shift right by 2-bits) after the multiplication of delta*i because the difference (z-y) might be small and the division may result in a 0 value.



Square Root Interpolation 4 Point LUT

index	X	sqrt(x)
0	0	0
1	4	2
2	8	2.828427
3	12	3.464102
4	16	4







Square Root Interpolation Equation

- Let's consider how to use interpolation to find a better value for the SQRT(9). Clearly, the SQRT will be between SQRT(8) and SQRT(12).
- How much between?
- Well 1/4 of the way because 9 is a 1/4 of the way between 9 and 12.
- Have the class write an equation describing SQRT(9) in terms of SQRT(12) and SQRT(8).

SQRT(9) = SQRT(8) + 1/4*(SQRT(12)-SQRT(8))



Square Root Interpolation Equation

■ Now write the equation replacing the 1/4 by a statement using 9,8, and 12.



Square Root Interpolation Spreadsheet

- Now divide the class into 16 sections to compute all the values of SQRT for 0-15.
- Compare these to the excel spreadsheet (4pt SQRT tab).
 - In this spreadsheet, I've broken the computation down so that you can see what the individual parts are doing...see next slide



Square Root Interpolation Spreadsheet

- SQRT(9) = SQRT(8) + (9-8)/(12-8)*(SQRT(12)-SQRT(8))
- base this is the SQRT(8) term in the equation above. It represents the base value from which we will interpolate.
- offset this is the (9-8)/(12-8) term in the equation above. It represents how much offset you are into the interval.
- **delta** this is the SQRT(12)-SQRT(8) term in the equation above. Its how much range the function covers between the two values in the LUT.
- base + offset*delta this is the value of the SQRT function for the input given in the "x" column.
- error(x) this is the absolute values of the difference between the linear interpolation value and the true value of the SQRT function.

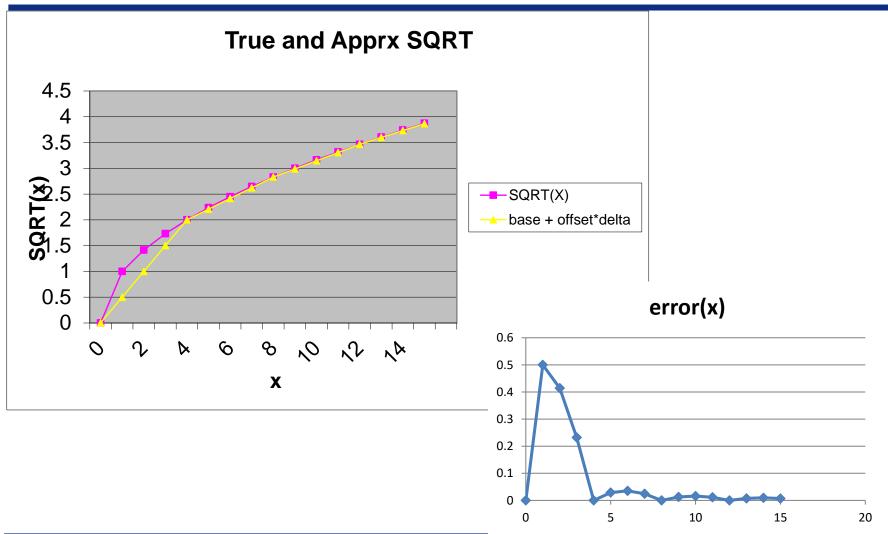


Square Root Interpolation Spreadsheet - 4 Point LUT

X	SQRT(X)	base	offset	delta	base + offset*delta	error(x)
0		0	0	2	0	0
1	1	0	0.25	2	0.5	0.5
2	1.414214	0	0.5	2	1	0.414214
3	1.732051	0	0.75	2	1.5	0.232051
4	2	2	0	0.828427	2	0
5	2.236068	2	0.25	0.828427	2.207106781	0.028961
6	2.44949	2	0.5	0.828427	2.414213562	0.035276
7	2.645751	2	0.75	0.828427	2.621320344	0.024431
8	2.828427	2.828427	0	0.635674	2.828427125	0
9	3	2.828427	0.25	0.635674	2.987345747	0.012654
10	3.162278	2.828427	0.5	0.635674	3.14626437	0.016013
11	3.316625	2.828427	0.75	0.635674	3.305182993	0.011442
12	3.464102	3.464102	0	0.535898	3.464101615	0
13	3.605551	3.464102	0.25	0.535898	3.598076211	0.007475
14	3.741657	3.464102	0.5	0.535898	3.732050808	0.009607
15	3.872983	3.464102	0.75	0.535898	3.866025404	0.006958



Square Root Interpolation 4 Point LUT



Red = LU	Г						
Blue = Int	terpolated						
	Input		Output				
	index.offset						
index	Q2.2	X	sqrt(X)	Q2.6	Hex		
0	00.00	0	0	00.00000	00		
	00.01	1					
	00.10	2					
	00.11	3					
1	01.00	4	2	10.000000	80		
	01.01	5					
	01.10	6					
	01.11	7					
2	10.00	8	2.8284	10.110101	B5		
	10.01	9					
→	10.10	10					
	10.11	11					
3	11.00	12	3.4641	11.011101	DD		
	11.01	13					
	11.10	14					
	11.11	15					
		16	4				

:BASE + offset. detta

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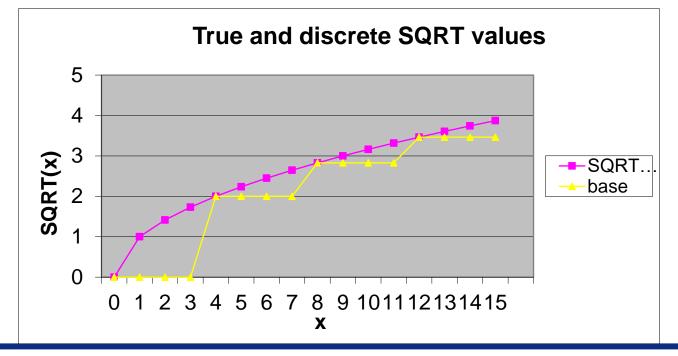
Square Root Interpolation Data Type

```
// Fnc
           SQRT
// In
           A 4-bit integer
// Out
           An approximate SQRT 8-bit fixed point with decimal at 6th bit.
// Pur
           This function computes a linearly Interpolated value for the SQRT. The
//
           5th entry in the lut is an approximation to 4.
int8 SQRT(int8 x) {
  int8 Iut[5] = \{0x00, 0x80, 0xB5, 0xDE, 0xFF\};
  int8 index;
  fixed base;
  index = x >> 2;
  base = lut[index];
  offset = (x \& 0x03)
  delta = lut[index+1] - lut[index];
  return(base + offset*delta)>>2;
} // end SQRT
```



Square Root Interpolation LUT

index	X	sqrt(x)	sqrt * 2^6	Trunc Down	2.6 Binary	2.6 Hex
0	0	0	0	0	00.00000	00
1	4	2	128	128	10.000000	80
2	8	2.828427	181.0193	181	10.110101	B5
3	12	3.464102	221.7025	221	11.011101	DD
4	16	4	256	255	11.111111	FF





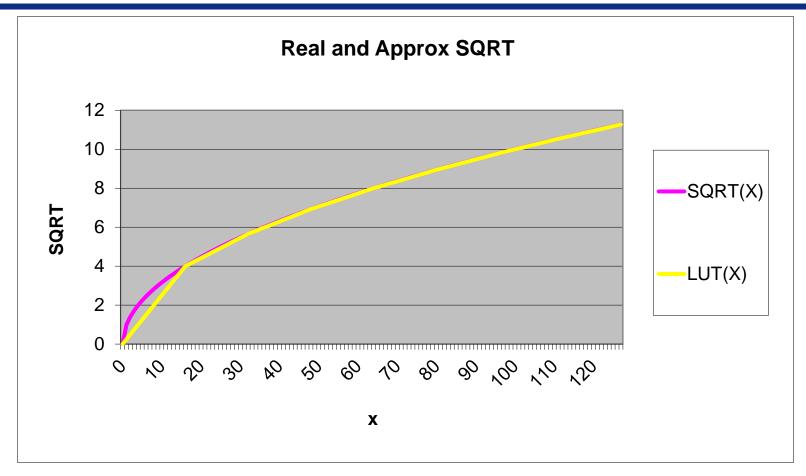
Square Root Interpolation 8 Point LUT

$\mathbf{\sim}$	

					10.6 B	inary
index	X	sqrt(x)	sqrt * 2^6	Trunc down	Upper 10-Bits	Lower 6-Bits
0	0	0	0	0	000000000	000000
1	16	4	256	256	000000100	000000
2	32	5.656854	362.0387	362	000000101	101010
3	48	6.928203	443.405	443	000000110	111011
4	64	8	512	512	000001000	000000
5	80	8.944272	572.4334	572	0000001000	111100
6	96	9.797959	627.0694	627	0000001001	110011
7	112	10.58301	677.3123	677	0000001010	100101
8	128	11.31371	724.0773	724	0000001011	010100

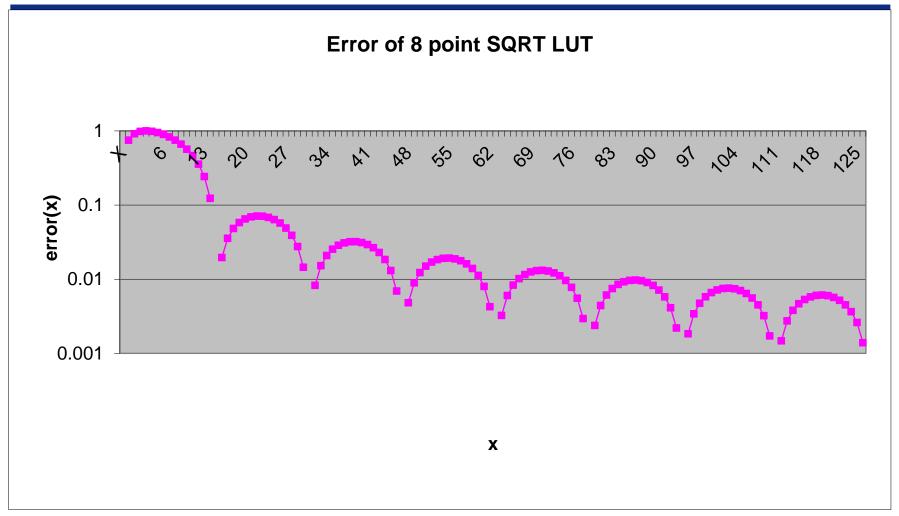


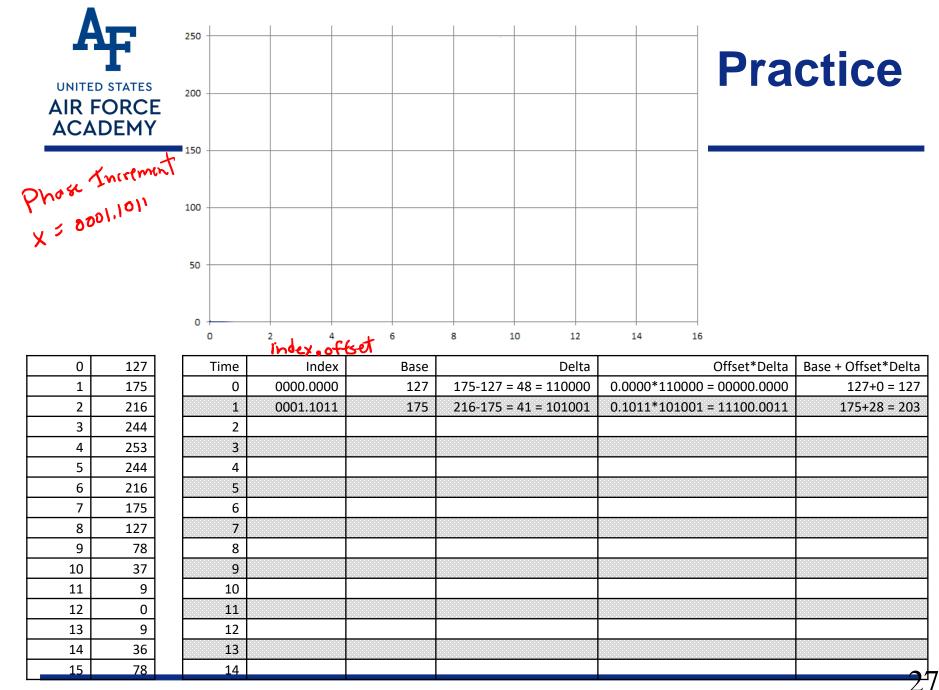
Square Root Interpolation 8 Point LUT



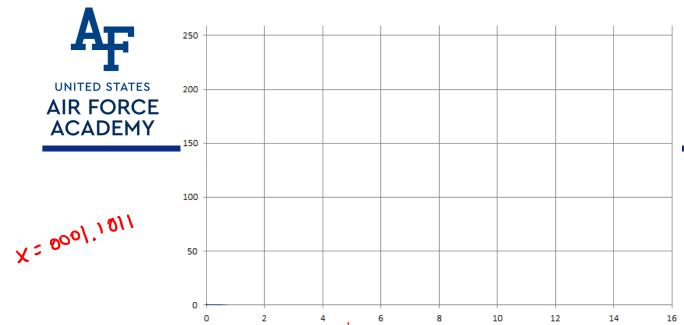


Square Root Interpolation LUT





Integrity - Service - Excellence



Solution

index. offset

Time	Index	Base	Delta		Offset*Delta		Base + Offset*Delta
0	0000.0000	127	175-12	7 = 48 = 110000	0.0000*10	1010 = 00000.0000	127+0 = 127
1	0001.1011	175	216-17	'5 = 41 = 101001	0.1011*10	1001 = 11100.0011	175+28 = 203
2	0011.0110	244	9	00001001	3.375	0011.0110	247
3	0101.0001	244	-28	1111100100	-1.75	111110.0100	242
4	0110.1100	216	-41	1111010111	-30.75	100001.0100	185
5	1000.0111	127	-49	1111001111	-21.4375	101010.1001	106
6	1010.0010	37	-28	1111100100	-3.5	111100.1000	34
7	1011.1101	9	-9	1111110111	-7.3125	111000.1011	2
8	1101.1000	9	27	00011011	13.5	1101.1000	23
9	1111.0011	78	49	00110001	9.1875	1001.0011	87
10	0000.1110	127	48	00110000	42	0000101010.0000	169
11	0010.1001	216	28	00011100	15.75	1111.1100	232
12	0100.0100	253	-9	1111110111	-2.25	111101.1100	251
13	0101.1111	244	-28	1111100100	-26.25	100101.1100	218
14	0111.1010	175	-48	1111010000	-30	1000100000	145

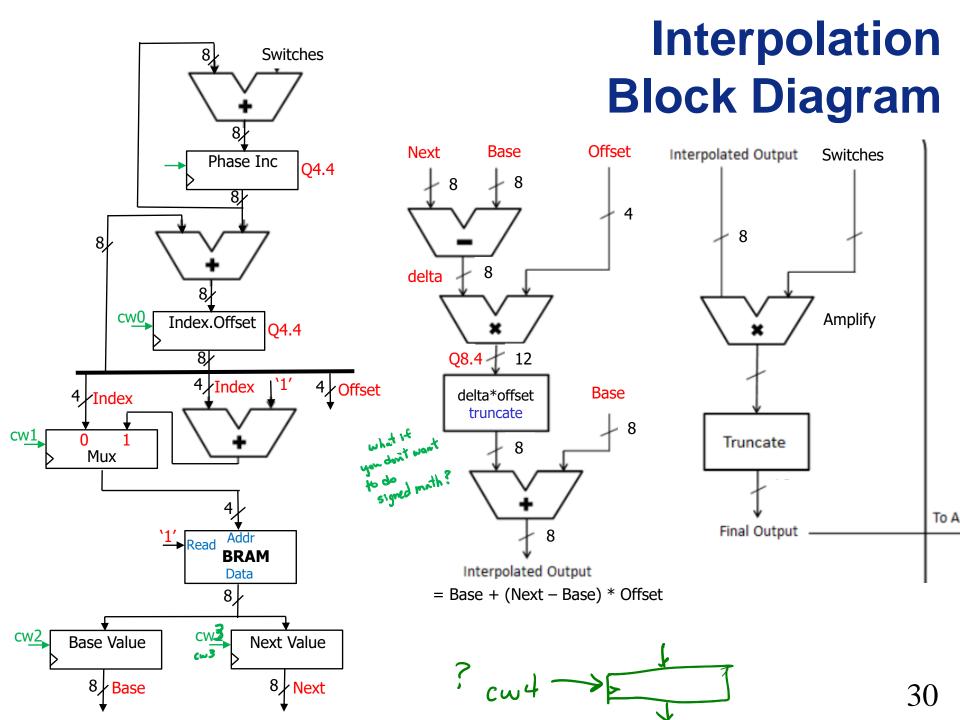
L	Lut
Index	Value
0	127

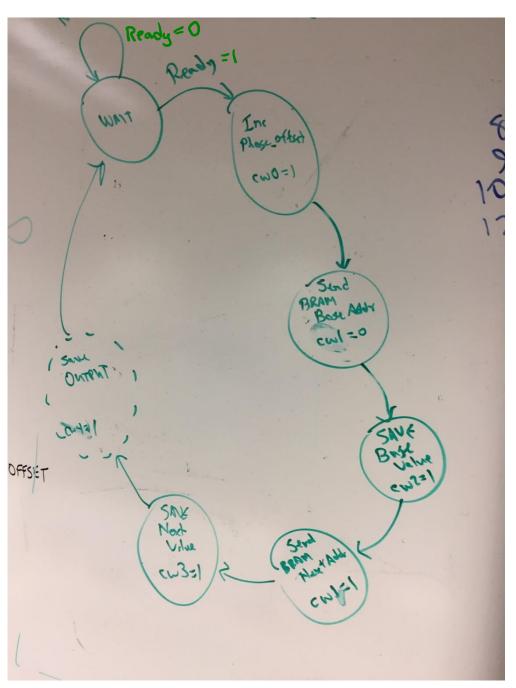
IIIUGX	value				
0	127				
1	175				
2	216				
3	244				
4	253				
5	244				
6	216				
7	175				
8	127				
9	78				
10)	37				
11	9				
12	. 0				
13	9				
14	36				
15	78				

Time	Bin Index	Dec Index	Trunc Dec Index	Base			Delta	Binary Delta
0	00000000	0	0	127	175	127	48	175-127 = 42 = 101010
1	00011011	1.6875	1 1	175	216	175	41	216-175 = 41 = 101001
2	00110110	3.375	3	244	253	244	9	00001001
3	01010001	5.0625	5	244	216	244	-28	1111100100
4	01101100	6.75	6	216	175	216	-41	1111010111
5	10000111	8.4375	8	127	78	127	-49	1111001111
6	10100010	10.125	10	37	9	37	-28	1111100100
7	10111101	11.8125	11	9	0 -	9	9	1111110111
8	11011000	13.5	13	9	36	9	27	00011011
9	11110011	15.1875	2-15	78	127	78.	49	00110001
10	00001110	0.875	0	127	175	127	48	00110000
11	00101001	2.5625	2	216	244	216	28	00011100
12	01000100	4.25	4	253	244	253	-9	1111110111
13	01011111	5.9375	5	244	216	244	-28	1111400100
14	01111010	7.625	7	175	127	175	-48	1111010000

more columns

Offse	t Bin Offset	Offset*Delta	Offset*Delta	Base + Offset*Delta	Base + Offset*Delta	Final Value
0	0000	0	0.0000*101010 = 00000.0000	127	127+0 = 127	127
0.687	5 1011	28.1875	0.1011*101001 = 11100.0011	203.1875	175+28 = 203	203
0.37	5 0110	3.375	00110110	247.375	247	247
0.062	5 0001 4	-1.75	1111100100	242.25	242	242
0.75	1100	-30.75	1000010100	185.25	185	185
0.43	75 0111	-21.4375	1010101001	105.5625	105 ;- **	106
0.12	5 0010	-3.5	1111001000	33.5	33	34
0.81	25 1101	-7.3125	1110001011	1.6875	1.	2
0.5	1000	13.5	11011000	22.5	22	23
0.18	75 0011	9.1875	10010011	87.1875	87	√.87 <u>-</u> - <u>-</u> 2
0.87	5 1110	42	00001010100000	169	169	169
0.56	25 1001	15.75	11111100	231.75	231	232
0.25	0100	-2.25	1111011100	250.75	250	251
0.93	75 1111	-26.25	1001011100	217.75	217	218
0.62	5 0000	-30	1000100000	145	145	145





State Machine?

29 March 2021

```
BRAM
     BRAM SDP MACRO: Simple Dual Port RAM 7 Series
                     Xilinx HDL Libraries Guide, version 2012.4
     Source:
                     https://www.xilinx.com/support/documentation/sw manuals/xilinx2012 2/ug953-vivado-
     Link:
                     7series-libraries.pdf
     Page:
                                  ) who makes this?
sampleMemory: BRAM SDP MACRO
     generic map (
          BRAM SIZE => "18Kb",
                                          -- Target BRAM, "18Kb" or "36Kb"
                                          -- Target device: "VIRTEX5", "VIRTEX6", "SPARTAN6", "7SERIES"
          DEVICE => "7SERIES",
                                          -- Optional output register disabled
          DO REG \Rightarrow 0,
          -- Initial values on output port
          INIT FILE => "NONE",
          WRITE WIDTH => 16,
                                          -- Valid values are 1-72 (37-72 only valid when BRAM SIZE="36Kb")
                                          -- Valid values are 1-72 (37-72 only valid when BRAM SIZE="36Kb")
          READ WIDTH => 16,
          SIM COLLISION CHECK => "NONE",
                                          -- Simulation collision check
          -- Set/Reset value for port output
          INIT 00 => X"8BC28AF98A31896988A<u>0</u>87D8870F8647857E84B583EC8323825A819180C8B000
          INIT 01 => X"9830976A96A595DF95199452938C92C591FE913790708FA98EE18E198D528C8A",
    Skip INIT 02 => X"A462A3A2A2E0A21FAL5DA 9B9FD89F169E529D8F9CCB9C079B439A7F99BA98F5",
          INIT 3E = X''7247717F70B76FF06F236E616D9A6CD46C0D6B476A8069BA68F5682F676A66A5'',
          INIT 3F => X"7ECF7E067D3D7C747BAC7AE37A1A7951788977C076F7762F7567749F73D6730E")
     port map (
                                          -- Output read data port, width defined by READ WIDTH parameter
          DO => DO,
          RDADDR => vecAddrRead
                                          -- Input address, width defined by port depth
                                          -- 1-bit input clock
          RDCLK => clk,
                                          -- active high reset
          RST => reset,
                                           -- read enable
          RDEN => '1',
          REGCE => '1',
                                          -- 1-bit input read output register enable - ignored
                                          -- Dummy write data - never used in this application
          DI => DI,
                                          -- write to neither byte
          WRADDR -> "000000000"
                                          -- Dummy place holder address
                                          -- 1-bit input write clock
          WRCLK => clk.
```

29 March 2021

-- we are not writing to this RAM

WREN \Rightarrow '0');



- See example spreadsheet for creating BRAM LUT (or use python, java, matlab...)
- Block Diagram?
- Intro to lab#4



Lab4 Intro