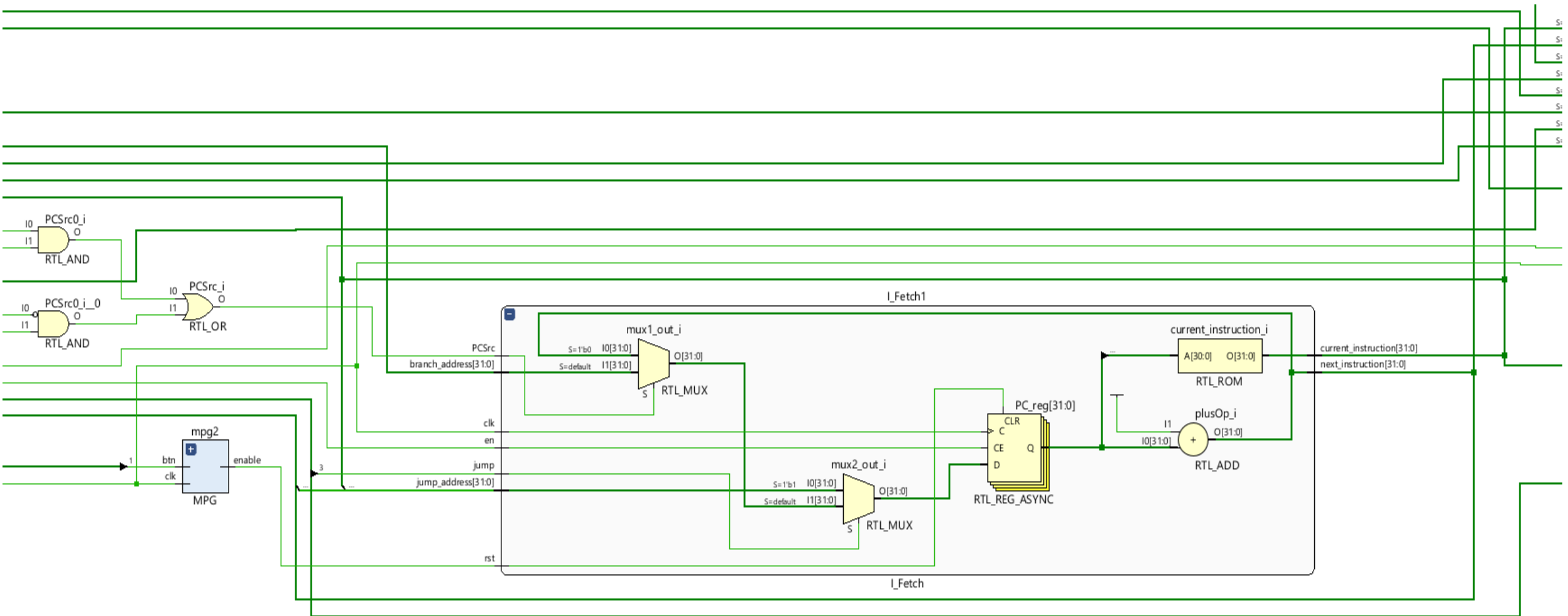
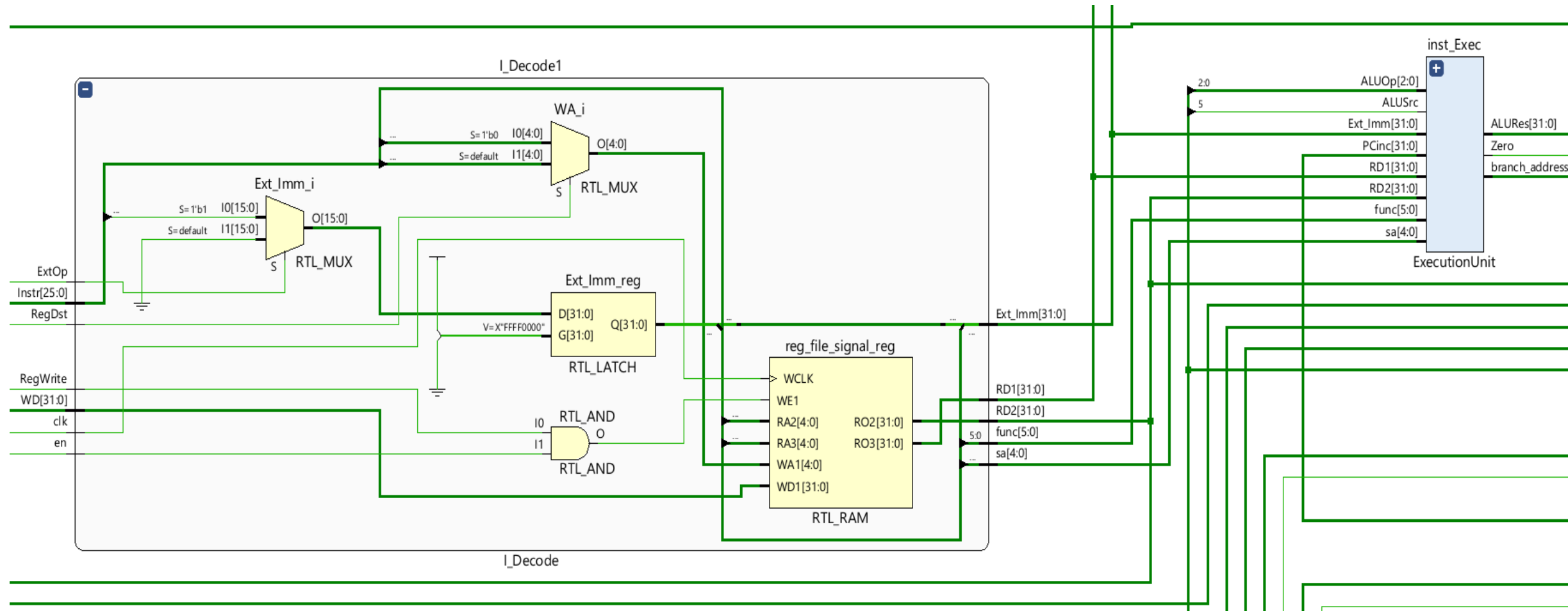


RTL schematic

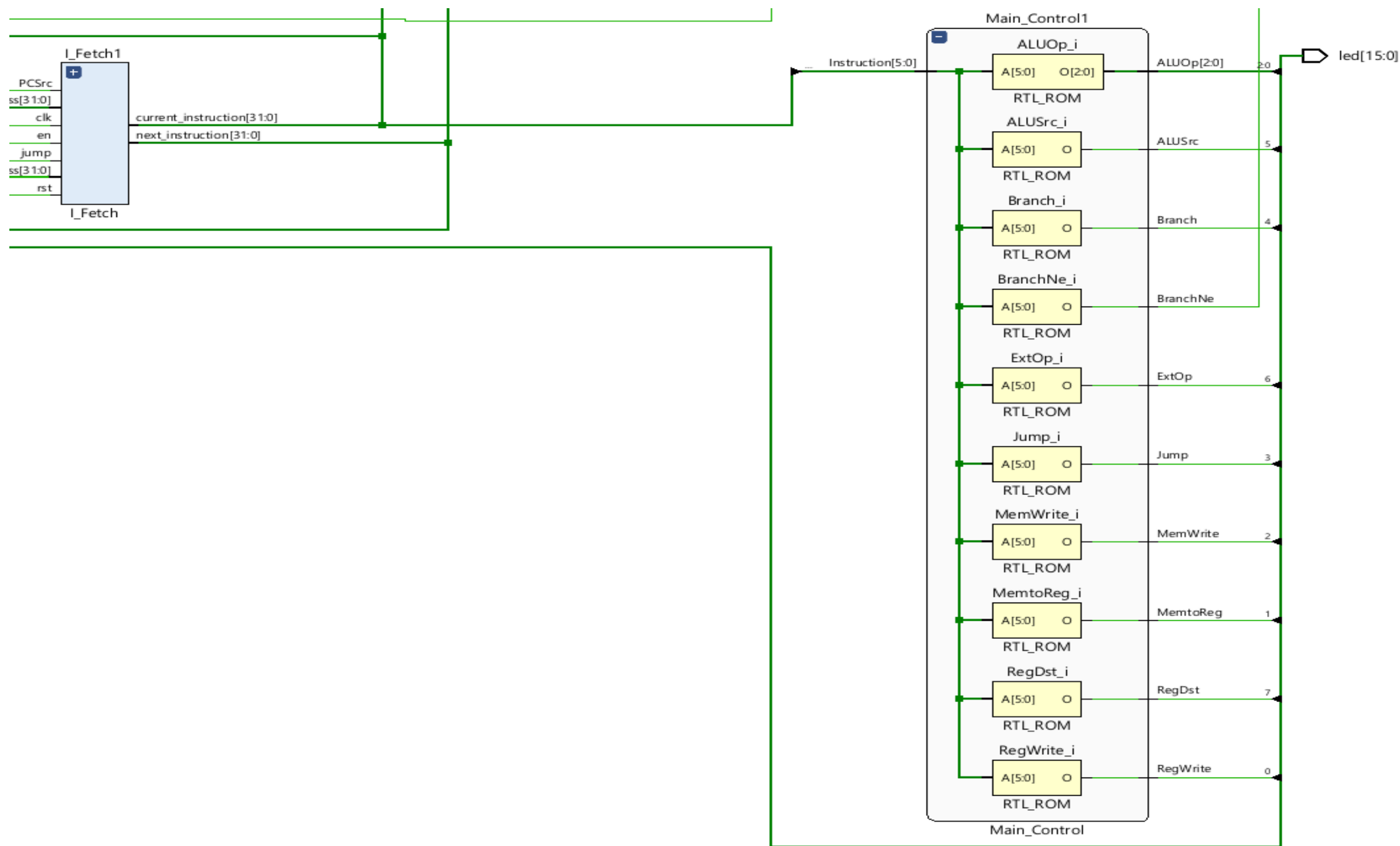
1.I_Fetch



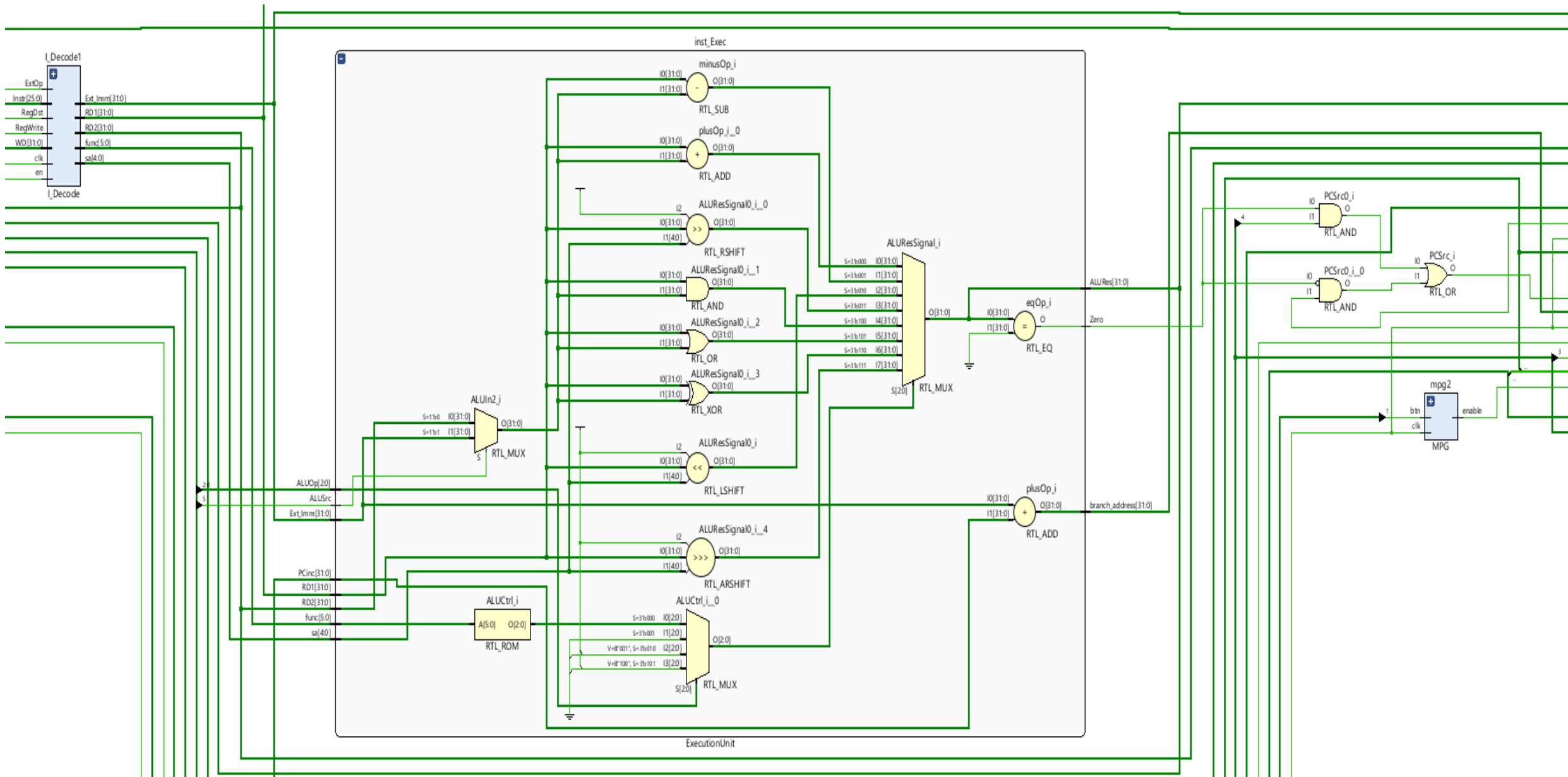
2.I_Decode



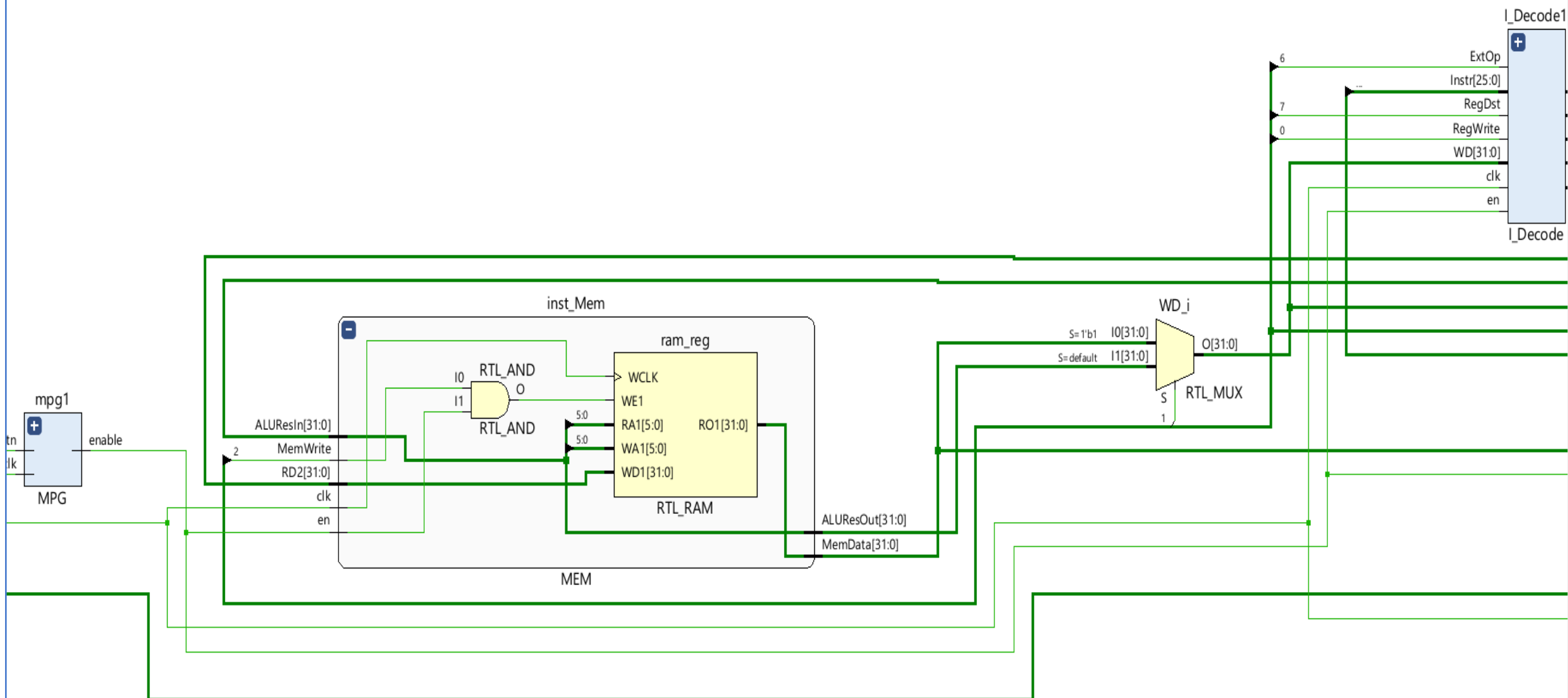
3.MainControl



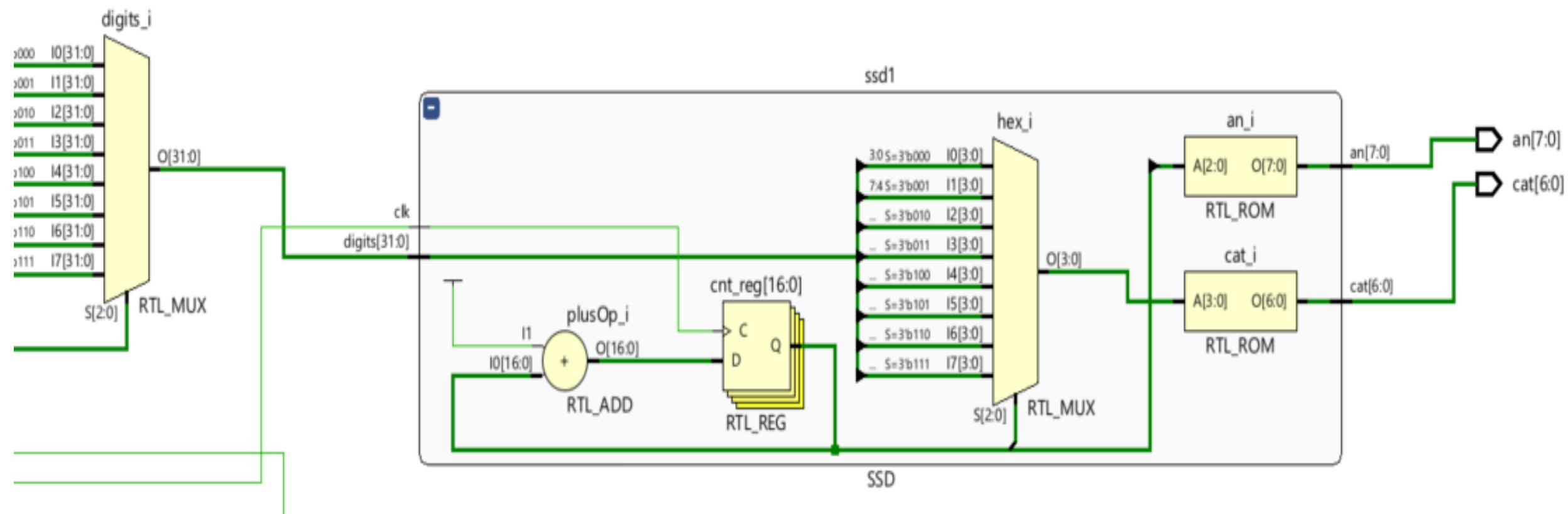
4.ExecutionUnit



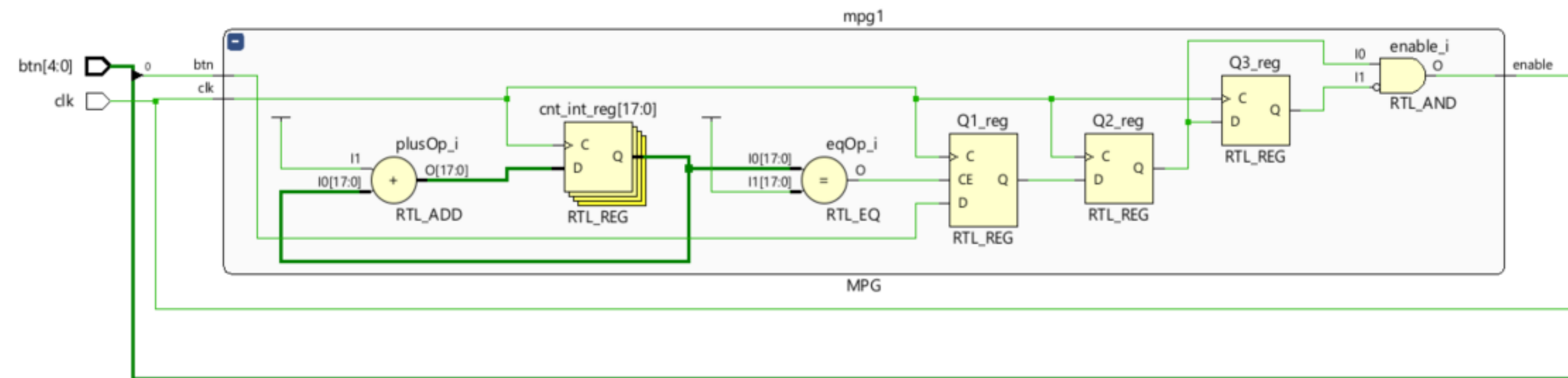
5.MEM



6.SSD



7. MPG



8.test_env

