**Trasarea execuției programului de test pentru MIPS32**

|  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Pas** | **SW(7:5)** | "000" | "001" | "010" | "011" | "100" | "101" | "110" | "111" | **De completat numai pentru instrucțiuni de salt** | |
| **Instr** (*în asamblare*) | **Instr** (*hexa*) | **PC+1** | **RD1** | **RD2** | **Ext\_Imm** | **ALURes** | **MemData** | **WD** | **BranchAddr** | **JumpAddr** |
| 0 | add $1, $0, $0 | X”0000 0820” | X”0000 0001” | X”0000 0000” | X”0000 0000” | X”0000 0820” | X”0000 0000” | X”0000 000A” | X”0000 0000” |  |  |
| 1 | addi $4, $0, 11 | X”2004 000b” | X”0000 0002” | X”0000 0000” | X”0000 000b” | X”0000 000b” | X”0000 000b” | X”FFFF FFFF” | X”0000 000b” |  |  |
| 2 | add $2, $0, $0 | X”0000 1020” | X”0000 0003” | X”0000 0000” | X”0000 0000” | X”0000 1020” | X”0000 0000” | X”0000 000A” | X”0000 0000” |  |  |
| 3 | add $5, $0, $0 | X”0000 2820” | X”0000 0004” | X”0000 0000” | X”0000 0000” | X”0000 2820” | X”0000 0000” | X”0000 000A” | X”0000 0000” |  |  |
| 4 | lw $3, 0($2) | X”8C43 0000” | X”0000 0005” | X”0000 0000” | X”0000 000A” | X” 0000 0000” | X”0000 0000” | X”0000 000A” | X”0000 000A” |  |  |
| 5 | beq $1, $4, 7 | X”1024 0007” | X”0000 0006” | X”0000 0000” | X”0000 000b” | X”0000 0007” | X”FFFF FFF5” | X”FFFF FFFF” | X” FFFF FFF5” | X”0000 000d” |  |
| 6 | lw $3, 0($2) | X”8C43 0000” | X”0000 0007” | X”0000 0000” | X”0000 000A” | X”0000 0000” | X”0000 0000” | X”0000 000A” | X”0000 000A” |  |  |
| 7 | andi $6, $3, 1 | X”3066 0001” | X”0000 0008” | X”0000 000A” | X”0000 0000” | X”0000 0001” | X”0000 0000” | X”0000 000A” | X”0000 0000” |  |  |
| 8 | beq $6, $0, 1 | X”10C0 0001” | X”0000 0009” | X”0000 0000” | X”0000 0000” | X”0000 0001” | X”0000 0000” | X”0000 000A” | X”0000 0000” | X”0000 000A” |  |
| 9 | add $5, $5, $3 | X”00A3 2820” |  |  |  |  |  |  |  |  |  |
| 10 | addi $2, $2, 1 | X”2042 0001” | X”0000 000b” | X”0000 0000” | X”0000 0000” | X”0000 0001” | X”0000 0001” | X”0000 000b” | X”0000 0001” |  |  |
| 11 | addi $1, $1, 1 | X”2021 0001” | X”0000 000C” | X”0000 0000” | X”0000 0000” | X”0000 0001” | X”0000 0001” | X”0000 000b” | X”0000 0001” |  |  |
| 12 | j 4 | X”0800 0004” | X”0000 000d” | X”0000 0000” | X”0000 0000” | X”0000 0004” | X”0000 0000” | X”0000 000A” | X”0000 0000” |  | X”0000 0004” |
| 13 | sw $5, 56($0) | X”AC05 0038” | X”0000 000E” |  |  |  |  |  |  |  |  |
| 14 |  |  |  |  |  |  |  |  |  |  |  |
| 15 |  |  |  |  |  |  |  |  |  |  |  |
| 16 |  |  |  |  |  |  |  |  |  |  |  |
| 17 |  |  |  |  |  |  |  |  |  |  |  |
| 18 |  |  |  |  |  |  |  |  |  |  |  |
| 19 |  |  |  |  |  |  |  |  |  |  |  |
| 20 |  |  |  |  |  |  |  |  |  |  |  |
| 21 |  |  |  |  |  |  |  |  |  |  |  |
| 22 |  |  |  |  |  |  |  |  |  |  |  |
| 24 |  |  |  |  |  |  |  |  |  |  |  |