HLT   I   O   CZELG   *
CE → PCT

INR

I   O
RGA

D A B   DAB   D A B

I   O
MEM   ALUI   AOPR   RGA   RGB

SPI
S P
SPD   STKI   STKO   EXC → ALU   RGC

I   O
MAR   RGA

O
INP   OUT

*: flags
SPCC Carry
SPCZ Zero
SPCE Equal
SPCL Lower
SPCG Greater

Legend of terms:

----- *manually settable components* -----
PCT - Program CounTer
INR - INstruction Register
STC - STep CounTer (microsteps per instruction)
MAR - Memory Address Register
INP - INPut Register
OUT - OUTput Register
RGA - ReGister A
RGB - ReGister B
RGB - ReGister B
AOPR- ALU Operand 2 (Operand 1 = RGA)

----- *non settable components* ----------
ALU - Arithmetic Logical Unit
CLK - A CLocK pulse generator
CTU - ConTrol Unit
DAB - Data and Address Bus
FLR - FLags Register