



Выбор
октавы

Выбор режимов
сигнала



ФАКТ

Клавиатура

UART TX + I2C0 SDA	SPI0 RX	GPIO 1
UART RX + I2C0 SCL	SPI0 CS0	GPIO 2
I2C1 SDA	SPI0 SCK	GPIO 3
I2C1 SCL	SPI0 TX	GPIO 4
UART TX + I2C0 SDA	SPI0 RX	GPIO 5
UART RX + I2C0 SCL	SPI0 CS0	GPIO 6
I2C1 SDA	SPI0 SCK	GPIO 7
I2C1 SCL	SPI0 TX	GPIO 8
UART TX + I2C0 SDA	SPI0 RX	GPIO 9
UART RX + I2C0 SCL	SPI0 CS0	GPIO 10
I2C1 SDA	SPI1 SCK	GPIO 11
I2C1 SCL	SPI1 TX	GPIO 12
UART TX + I2C0 SDA	SPI1 RX	GPIO 13
UART RX + I2C0 SCL	SPI1 CS0	GPIO 14
I2C1 SDA	SPI1 SCK	GPIO 15
I2C1 SCL	SPI1 TX	GPIO 16
UART TX + I2C0 SDA	SPI1 RX	GPIO 17
UART RX + I2C0 SCL	SPI1 CS0	GPIO 18
I2C1 SDA	SPI1 SCK	GPIO 19
I2C1 SCL	SPI1 TX	GPIO 20

VBUS	ADC0
VSSYS	ADC1
GND	ADC2
3V3LEN	ADC3
3V3(OUT)	ADC4
ADC_VREF	ADC5
GP28	ADC6
GP27	ADC7
GP26	ADC8
BUN	ADC9
GP22	ADC10
GND	ADC11
GP21	ADC12
GP20	ADC13
GP19	ADC14
GP18	ADC15
GND	ADC16
GP17	ADC17
GP16	ADC18