UG Mid Sem Exam Semester : II "Sem, Date: 25/10/2021

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Branch: CSE - B

Subject: Computer Network

Subject Code: C5301

Q.1.

a) Ano) OSI Model us TCPIIP model:

PROS:

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- (i) In OSI model, the transport layer guarantees the delivery Of packets, but in TCPIIP model, the transport layer does not guarantee delivery of packets.
- (ii) Protocols in OSI model are easily replaced as the technology changes, whereas the protocols in TCPIIP are not easy to replace.

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- (i) The OSI model does not have any mechanism for providing a reliable and secure connection for data transmission, while TCPIIP model has a 3-way handshake mechanism for providing a reliable and secure connection link over the network.
- (ii) The OSI model is purely a theoritical model that does not consider the availability of appropriate technology, while TCPIIP is a practical model and can be practically implemented.

b)and) There are four components of delay:

i) Processing delay: It is the time accociated with the system analysing a packet header and determining where the packet must be sent.

Example: The taken by router to process packet header.

"i) Queueing delag: It is the time between a packet being queued and it being sent.

Example: Time a packet is put in queue when it arrives faster than the router can process.

(11) Transmission delay: It is the time needed to push a packet's data bits into the wire.

Example: Time it takes for the router to push all packets into the wire.

iv) Propagation delay: It is the time associated with the first bit of the packet travelling from the sending endpoint to the receiving endpoint.

Example: The packet sent by the sender is received by the receiver.

There takes some for the packet to travel. This is an enample of propagation delay over distance by speed.

<u>Q.8.</u> Ans:

OTTITIO TITIOOF OTI OTTO TOOTTOTO TOOTTIT FOTOTOTT

- 1) Ans) for above frame sequence, 2 bytes are used for address.
- 11) Aw) The receiver, in the above frame sequence, receives only 8 bits of the actual data.
 - iii) Aws) If the receiver was earlier ack the receipt of 5 frames,

 Then frames received by sender = 1+2+1+2+1

 = 7 bytes.

 Total frame received = 05 frames
- (v) Avo) The bit Stuffing is done 3 times in this frame.
- V) Aus) It is information field.

Q.20

(a) And) There are three persistent CSMA schemes:

- (1) O-persistent C&MA: In this type, the Station that has frames to send, only the station senses for the Channel.
- (11) I persistent CSMA: In this type, the station continuously

 Senses the channel to check its state,

 if it can transfer the data or not.
- (iii) p-percistent CSMA: It is the hybrid of O- 2nd 1-persistent

 In this type, when the station is ready
 to send the frames, It will sense
 the channel, and if the channel is
 found to be busy, the channel will

 Send wait for next slot.

a) Aus) Criven, n+ 1 rows.

So, I entra row is there.

Since I parity bit can detect I error, So, a parity bits can detect a bit error.

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Ans: Given, A -> 10101110

-> transmite I bit

B -> TOOTOOTO

> transmits I bit

C> 70070707

-> transmits 0 bit

A encodes same > 10101110

B encodes a same → 10010010

& encodes on same

C encoded in reverse > 01101010

Signal Codes,

B -> +1, -1, -1, +1, -1, -1, +1, -1

C -> -1, +1, +1, -1, +1, -1, +1, -1

Therefore,

Signal received is:

+ L 1 - L 1 + L 1 - L 1 + L 1 - L 1 + 3 1 - 3 1 + L 1 - L 1 + L 1 - L 1

+ 1 , - 1 , +3 , -3-