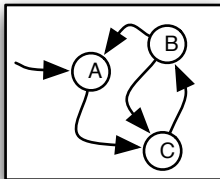
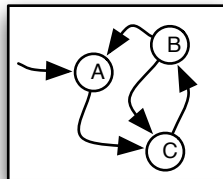


```
timing mem
signal in : memStart
signal out: memEnd,
memVal
{
  u3 nb
  wait for signal memStart
  wait 10 cycle
  ...
}
```

description textuelle  
Harmless



modèle interne sous  
forme d'automate  
Harmless



automate temporisé  
au format Uppaal



```
while(i < nb && !m_stopSimu)
{
    if(m_nextCyclicWakeUpDate
    <= m_nbCycles) manageCyclicPart();

    currentInstruction =
    m_decoder->decode(this, m_PC );
}
```

modèle de simulation  
(code C)