# Báo cáo thực hành KTMT tuần 10

Họ và tên: Đỗ Gia Huy

MSSV: 20215060

### Assignment 1

#### 1. Code

```
.eqv SEVENSEG_LEFT 0xFFFF0010 # Dia chi cua den led 7 doan trai.
                                # Bit 0 = doan a;
                                # Bit 1 = doan b; ...
                                # Bit 7 = dau.
.eqv SEVENSEG_RIGHT 0xFFFF0011 # Dia chi cua den led 7 doan phai
.text
main:
        li $a0, 0x3F
                           # set value for segments
         jal SHOW_7SEG_LEFT # show
         li $a0, 0x7D # set value for segments
         jal SHOW_7SEG_RIGHT # show
         li $v0, 10
exit:
         syscall
endmain:
#-----
# Function SHOW_7SEG_LEFT: turn on/off the 7seg
# param[in] $a0 value to shown
# remark $t0 changed
#-----
SHOW_7SEG_LEFT:
    li $t0, SEVENSEG_LEFT # assign port's address
    sb $a0, 0($t0)
                           # assign new value
    jr $ra
```

```
#------
# Function SHOW_7SEG_RIGHT: turn on/off the 7seg

# param[in] $a0 value to shown

# remark $t0 changed

#------
SHOW_7SEG_RIGHT:

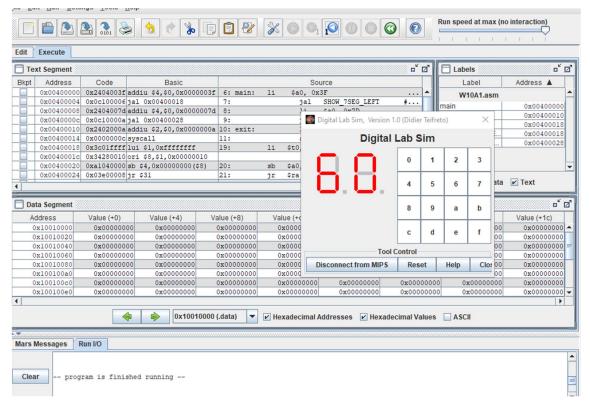
li $t0, SEVENSEG_RIGHT # assign port's address

sb $a0, 0($t0) # assign new value

jr $ra

#Do Gia Huy 20215060
```

Mở Digital Lab Sim trong Tool, kết nối nó với MIPS, chạy và hiển thị kết quả Đỗ Gia Huy có MSSV 20215060, số 60 là số cần hiện ra



⇒ Kết quả đúng với lý thuyết

# **Assignment 2**

#### 1. Code

```
.eqv SEVENSEG_LEFT 0xFFFF0011
                                       # Dia chi cua den led 7 doan trai.
                                        # Bit 0 = doan a;
                                        # Bit 1 = doan b; ...
                                        # Bit 7 = dau.
.eqv SEVENSEG_RIGHT 0xFFFF0010
                                       # Dia chi cua den led 7 doan phai
.data
     message: .asciiz "Nhap vao mot so nguyen: "
.text
main:
           $v0, 4
     li
           $a0, message
     la
     syscall
     li
           $v0, 5
     syscall
                $s0, $v0
     move
     li
           $t2, 10
          $s0, $t2
     div
     mfhi $t1
case0r:
     bne $t1, 0, case1r
     li
           $a0, 0x3F
           SHOW_7SEG_RIGHT
     jal
     i
           defaultr
case1r:
     bne $t1, 1, case2r
     li
           $a0, 0x6
```

```
jal
           SHOW_7SEG_RIGHT
           defaultr
     j
case2r:
     bne $t1, 2, case3r
     li
           $a0, 0x5B
     jal
           SHOW_7SEG_RIGHT
           defaultr
     j
case3r:
          $t1, 3, case4r
     bne
     li
           $a0, 0x4F
           SHOW_7SEG_RIGHT
     jal
     j
           defaultr
case4r:
     bne $t1, 4, case5r
     li
           $a0, 0x66
     jal
           SHOW_7SEG_RIGHT
           defaultr
     j
case5r:
     bne $t1, 5, case6r
           $a0, 0x6D
     li
     jal
           SHOW_7SEG_RIGHT
     j
           defaultr
case6r:
     bne $t1, 6, case7r
     li
           $a0, 0x7D
           SHOW_7SEG_RIGHT
     jal
           defaultr
case7r:
```

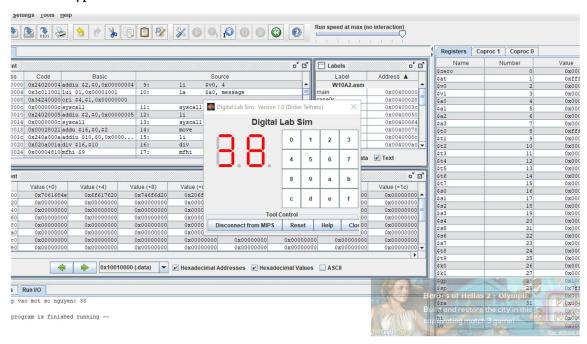
```
bne $t1, 7, case8r
     li
           $a0, 0x7
     jal
           SHOW_7SEG_RIGHT
     j
           defaultr
case8r:
     bne $t1, 8, case9r
     li
           $a0, 0x7F
           SHOW_7SEG_RIGHT
     jal
           defaultr
     j
case9r:
     bne $t1, 9, defaultr
     li
           $a0, 0x6F
           SHOW_7SEG_RIGHT
     jal
     j
           defaultr
defaultr:
          $s0, $s0, $t1
     sub
     div
           $s0, $t2
     mflo $t3
     div
           $t3, $t2
     mfhi $t1
case0l:
     bne $t1, 0, case11
           $a0, 0x3F
     li
     jal
           SHOW_7SEG_LEFT
     j
           defaultl
case1l:
     bne $t1, 1, case2l
     li
           $a0, 0x6
```

```
jal
           SHOW_7SEG_LEFT
           defaultl
     j
case2l:
     bne $t1, 2, case31
     li
           $a0, 0x5B
     jal
           SHOW_7SEG_LEFT
           defaultl
     j
case3l:
           $t1, 3, case4l
     bne
     li
           $a0, 0x4F
           SHOW_7SEG_LEFT
     jal
     j
           defaultl
case4l:
     bne $t1, 4, case51
     li
           $a0, 0x66
     jal
           SHOW_7SEG_LEFT
           defaultl
     j
case5l:
     bne $t1, 5, case61
     li
           $a0, 0x6D
     jal
           SHOW_7SEG_LEFT
     j
           defaultl
case6l:
     bne $t1, 6, case71
     li
           $a0, 0x7D
     jal
           SHOW_7SEG_LEFT
           defaultl
     j
case7l:
```

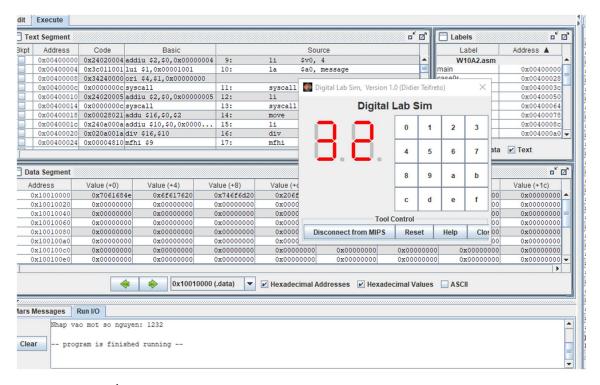
```
bne $t1, 7, case81
    li
         $a0, 0x7
    jal
         SHOW_7SEG_LEFT
    j
         defaultl
case8l:
    bne $t1, 8, case91
         $a0, 0x7F
    li
         SHOW_7SEG_LEFT
    jal
         defaultl
    j
case91:
    bne $t1, 9, defaultl
    li
         $a0, 0x6F
         SHOW_7SEG_LEFT
    jal
    j
         defaultl
defaultl:
    li
         $v0, 10
    syscall
endmain:
# Function SHOW_7SEG_LEFT: turn on/off the 7seg
# param[in] $a0 value to shown
# remark $t0 changed
#-----
SHOW_7SEG_LEFT:
    li $t0, SEVENSEG_LEFT # assign port's address
    sb $a0, 0($t0) # assign new value
    jr $ra
#-----
```

```
# Function SHOW_7SEG_RIGHT: turn on/off the 7seg
# param[in] $a0 value to shown
# remark $t0 changed
#------SHOW_7SEG_RIGHT:
    li $t0, SEVENSEG_RIGHT # assign port's address
    sb $a0, 0($t0) # assign new value
    ir $ra
```

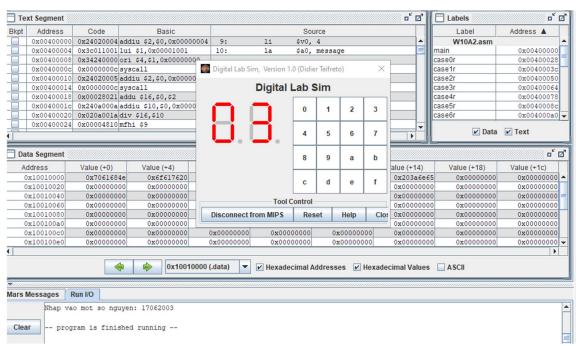
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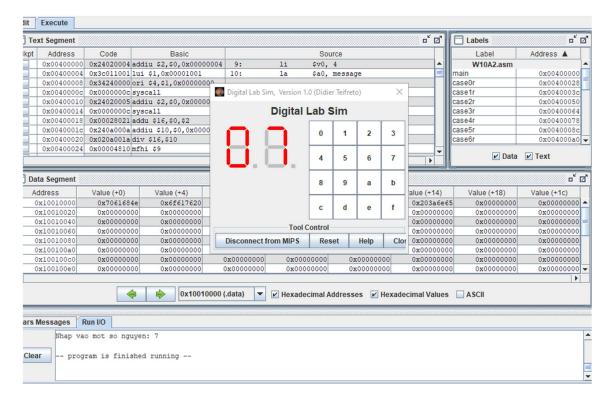
TH2: Nhập số 1232



TH3: Nhập số 17062003



TH4: Nhập số 7



## **Assignment 3**

#### 1. Code

```
.eqv SEVENSEG_LEFT 0xFFFF0011 # Dia chi cua den led 7 doan trai.

# Bit 0 = doan a;

# Bit 1 = doan b; ...

# Bit 7 = dau .

.eqv SEVENSEG_RIGHT 0xFFFF0010 # Dia chi cua den led 7 doan phai
.data

message: .asciiz "Nhap vao mot ky tu: "

.text

main:

li $v0, 4

la $a0, message
```

li \$v0, 12

syscall

```
syscall
     move $s0, $v0
     li
           $t1, -1
For: addi $t1, $t1, 1
     beq $s0, $t1, EndFor
           For
     j
EndFor:
#Vong For dung de lay gia tri Ascii roi luu vao thanh $t1
     move $s0, $t1
           $t2, 10
     li
           $s0, $t2
     div
     mfhi $t1
case0r:
     bne $t1, 0, case1r
     li
           $a0, 0x3F
     jal
           SHOW_7SEG_RIGHT
     j
           defaultr
case1r:
     bne $t1, 1, case2r
     li
           $a0, 0x6
           SHOW_7SEG_RIGHT
     jal
           defaultr
     į
case2r:
     bne $t1, 2, case3r
```

li

jal

j

\$a0, 0x5B

defaultr

SHOW\_7SEG\_RIGHT

case3r: bne \$t1, 3, case4r

li \$a0, 0x4F

jal SHOW\_7SEG\_RIGHT

j defaultr

case4r: bne \$t1, 4, case5r

li \$a0, 0x66

jal SHOW\_7SEG\_RIGHT

j defaultr

case5r: bne \$t1, 5, case6r

li \$a0, 0x6D

jal SHOW\_7SEG\_RIGHT

j defaultr

case6r: bne \$t1, 6, case7r

li \$a0, 0x7D

jal SHOW\_7SEG\_RIGHT

i defaultr

case7r: bne \$t1, 7, case8r

li \$a0, 0x7

jal SHOW\_7SEG\_RIGHT

j defaultr

case8r: bne \$t1, 8, case9r

li \$a0, 0x7F

jal SHOW\_7SEG\_RIGHT

j defaultr

case9r: bne \$t1, 9, defaultr

li \$a0, 0x6F

jal SHOW\_7SEG\_RIGHT

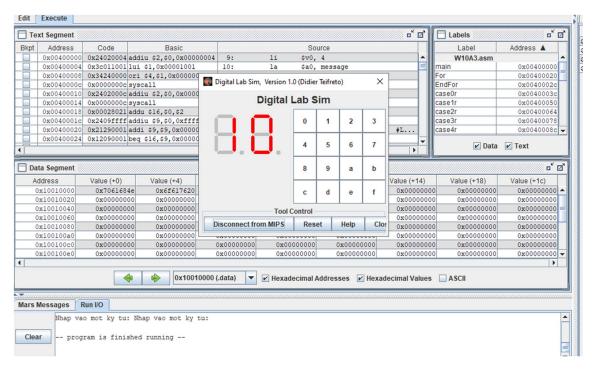
j defaultr

```
defaultr:
     sub $s0, $s0, $t1
     div
           $s0, $t2
     mflo $t3
     div
           $t3, $t2
     mfhi $t1
case0l:
     bne $t1, 0, case11
     li
           $a0, 0x3F
     jal
           SHOW_7SEG_LEFT
     j
           defaultl
case1l:
     bne $t1, 1, case21
     li
           $a0, 0x6
           SHOW_7SEG_LEFT
     jal
           defaultl
     j
case2l:
     bne $t1, 2, case31
     li
           $a0, 0x5B
           SHOW_7SEG_LEFT
     jal
     j
           defaultl
case3l:
           $t1, 3, case4l
     bne
     li
           $a0, 0x4F
           SHOW_7SEG_LEFT
     jal
     j
           defaultl
case4l:
           $t1, 4, case5l
     bne
```

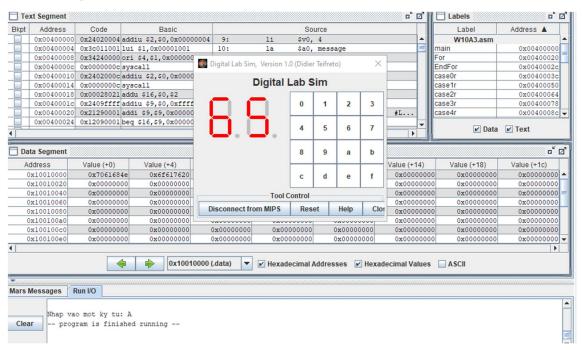
```
li
           $a0, 0x66
     jal
           SHOW_7SEG_LEFT
           defaultl
     j
case5l:
     bne $t1, 5, case61
     li
           $a0, 0x6D
           SHOW_7SEG_LEFT
     jal
     j
           defaultl
case6l:
     bne $t1, 6, case7l
     li
           $a0, 0x7D
           SHOW_7SEG_LEFT
     jal
           defaultl
     j
case7l:
     bne $t1, 7, case81
           $a0, 0x7
     li
     jal
           SHOW_7SEG_LEFT
     j
           defaultl
case8l:
     bne $t1, 8, case91
     li
           $a0, 0x7F
           SHOW_7SEG_LEFT
     jal
           defaultl
     j
case9l:
     bne $t1, 9, defaultl
     li
           $a0, 0x6F
           SHOW_7SEG_LEFT
     jal
     j
           defaultl
```

```
defaultl:
    li
        $v0, 10
    syscall
endmain:
# Function SHOW_7SEG_LEFT: turn on/off the 7seg
# param[in] $a0 value to shown
# remark $t0 changed
#-----
SHOW_7SEG_LEFT:
    li $t0, SEVENSEG LEFT # assign port's address
    sb $a0, 0($t0) # assign new value
    jr $ra
#-----
# Function SHOW_7SEG_RIGHT: turn on/off the 7seg
# param[in] $a0 value to shown
# remark $t0 changed
#-----
SHOW_7SEG_RIGHT:
    li $t0, SEVENSEG_RIGHT # assign port's address
    sb \$a0, 0(\$t0) # assign new value
    jr $ra
```

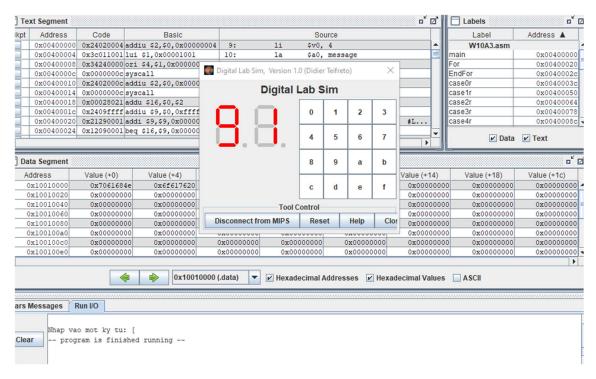
Mở Digital Lab Sim trong Tool, kết nối nó với MIPS, chạy và hiển thị kết quả TH1: Không nhập gì ngoài Enter



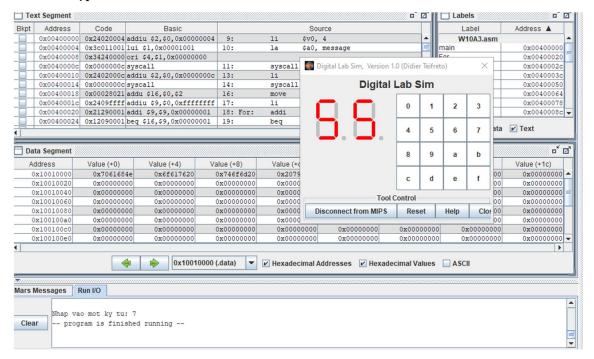
TH2: Nhập chữ A



TH3: Nhập dấu [



#### TH5: Nhập số 7



# <u>Assignment 4</u>

#### 1. Code

.eqv MONITOR\_SCREEN 0x10010000

.eqv CUTEPINK 0x00FF69B4

.eqv WHITE 0x00FFFFFF

```
.text
     li
            $k0, MONITOR_SCREEN
     li
            $s0, 2
     li
            $t0, -1
                                    # Khoi tao j
For1: addi $t0, $t0, 1
     beq $t0, 8, Exit
     li
                                    # Khoi tao i
            $t1, -1
For2: addi $t1, $t1, 1
     beq $t1, 8, EndFor2
      div
           $t0, $s0
     mfhi $t2
      div
            $t1, $s0
     mfhi $t3
     bne $t2, 0, Next
     bne
          $t3, 0, Paint2
     j
            Paint1
Next:
           $t3, 0, Paint2
      beq
Paint1:
     sll
            $s1, $t0, 3
      add
           $s1, $s1, $t1
     sll
            $s1, $s1, 2
     add
           $s2, $s1, $k0
```

\$t4, CUTEPINK

\$t4, 0(\$s2)

For2

li

sw

j

#### Paint2:

\$s1, \$t0, 3 sll add \$s1, \$s1, \$t1 sll \$s1, \$s1, 2 \$s2, \$s1, \$k0 add li \$t4, WHITE \$t4, 0(\$s2) SW j For2

#### EndFor2:

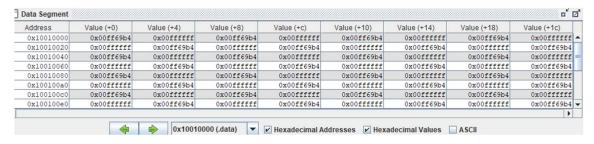
j For1

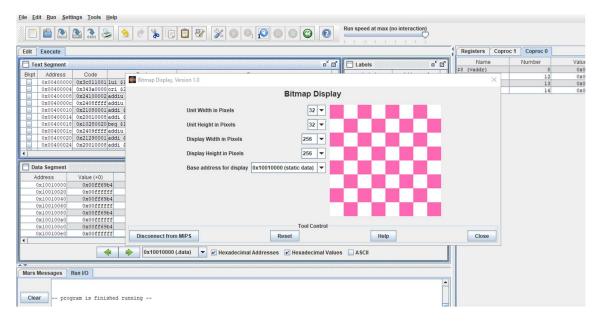
Exit: li \$v0, 10

syscall

### 2. Cách chạy và kết quả

Mở bitmap display trong tool sau khi ấn vào nút thực thi (hình cờ lê), kết nối bitmap với MIPS, chỉnh đô rông, đô dài đơn vi điểm ảnh là 32 pixel, chỉnh độ rộng, độ dài của màn hình bitmap là 256 pixel. Khi đó, ở màn hình bitmap có (256/32)\*(256/32) = 64 điểm ảnh. Chạy để hiện kết quả.





# **Assignment 5**

#### 1. Code

```
.eqv MONITOR_SCREEN 0x10010000
```

.eqv RED 0x00FF0000

.eqv GREEN 0x0000FF00

.data

x1: .asciiz "Nhap x1: "

y1: .asciiz "Nhap y1: "

x2: .asciiz "Nhap x2: "

y2: .asciiz "Nhap y2: "

error1: .asciiz "Error: x2 phai khac x1. Moi nhap lai!\n"

error2: .asciiz "Error: y2 phai khac y1. Moi nhap lai!\n"

.text

li \$k0, MONITOR\_SCREEN

li \$v0, 4

la \$a0, x1

syscall

```
li $v0, 5
syscall
move $s0, $v0
```

li \$v0, 4

la \$a0, y1

syscall

li \$v0, 5

syscall

move \$s1, \$v0

la \$a0, x2

syscall

li \$v0,5

syscall

move \$s2, \$v0

beq \$s2, \$s0, Error1

### NhapY2: li \$v0, 4

la \$a0, y2

syscall

li \$v0, 5

syscall

move \$s3, \$v0

beq \$s3, \$s1, Error2

j Tsugi

```
Error1: li
           $v0, 4
     la
            $a0, error1
     syscall
     j
            NhapX2
Error2:
           li
                 $v0, 4
     la
            $a0, error2
     syscall
     j
           NhapY2
Tsugi:
            $t0, $s0, $s2
     slt
     slt
            $t1, $s1, $s3
           $t0, 0, Case3
     beq
     beq
           $t1, 0, Case2
Case1:
     add
           $v0, $s1, $zero
For1:
           $v0, $s3, Exit
     bgt
      add $v1, $s0, $zero
For2:
     bgt
           $v1, $s2, EndFor2
     beq $v0, $s1, InVien1
     beq
           $v0, $s3, InVien1
      beq
           $v1, $s0, InVien1
           $v1, $s2, InVien1
     beq
     sll
           $t8, $v0, 6
     add
           $t8, $t8, $v1
     sll
            $t8, $t8, 2
```

```
li
           $a1, GREEN
     add
           $a2, $k0, $t8
           $a1, 0($a2)
     SW
     add
           $v1, $v1, 1
     j
           For2
InVien1:
     sll
           $t8, $v0, 6
     add
           $t8, $t8, $v1
           $t8, $t8, 2
     sll
     li
           $a1, RED
     add
           $a2, $k0, $t8
           $a1, 0($a2)
     sw
     add
           $v1, $v1, 1
     j
           For2
EndFor2:
     add
           $v0, $v0, 1
     j
           For1
Case2:
     add
           $v0, $s3, $zero
For3:
           $v0, $s1, Exit
     bgt
     add
           $v1, $s0, $zero
For4: bgt
           $v1, $s2, EndFor4
     beq
           $v0, $s1, InVien2
           $v0, $s3, InVien2
     beq
           $v1, $s0, InVien2
     beq
```

beq

\$v1, \$s2, InVien2

```
sll
           $t8, $v0, 6
     add
           $t8, $t8, $v1
     sll
            $t8, $t8, 2
     li
            $a1, GREEN
          $a2, $k0, $t8
      add
           $a1, 0($a2)
      sw
     add
           $v1, $v1, 1
     j
           For4
InVien2:
     sll
            $t8, $v0, 6
     add
           $t8, $t8, $v1
     sll
            $t8, $t8, 2
            $a1, RED
     li
     add $a2, $k0, $t8
           $a1, 0($a2)
      SW
           $v1, $v1, 1
     add
     j
            For4
EndFor4:
           $v0, $v0, 1
     add
           For3
      j
Case3:
     beq
           $t1, 0, Case4
           $v0, $s1, $zero
     add
For5:
           $v0, $s3, Exit
     bgt
     add
           $v1, $s2, $zero
For6:
     bgt
           $v1, $s0, EndFor6
```

```
beq
           $v0, $s1, InVien3
     beq
           $v0, $s3, InVien3
           $v1, $s0, InVien3
     beq
           $v1, $s2, InVien3
     beq
     sll
            $t8, $v0, 6
     add
           $t8, $t8, $v1
     sll
            $t8, $t8, 2
     li
            $a1, GREEN
     add
           $a2, $k0, $t8
            $a1, 0($a2)
     sw \\
     add
           $v1, $v1, 1
     j
           For6
InVien3:
     sll
            $t8, $v0, 6
     add
           $t8, $t8, $v1
     sll
            $t8, $t8, 2
     li
            $a1, RED
           $a2, $k0, $t8
     add
            $a1, 0($a2)
     sw
     add
           $v1, $v1, 1
     j
           For6
EndFor6:
     add
           $v0, $v0, 1
     j
            For5
Case4:
     add
           $v0, $s3, $zero
For7:
```

bgt

\$v0, \$s1, Exit

```
add
           $v1, $s2, $zero
For8:
            $v1, $s0, EndFor8
      bgt
     beq
           $v0, $s1, InVien4
           $v0, $s3, InVien4
      beq
      beq
           $v1, $s0, InVien4
           $v1, $s2, InVien4
      beq
     sll
            $t8, $v0, 6
     add
           $t8, $t8, $v1
     sll
            $t8, $t8, 2
     li
            $a1, GREEN
           $a2, $k0, $t8
      add
            $a1, 0($a2)
      SW
     add
           $v1, $v1, 1
     j
           For8
InVien4:
     sll
            $t8, $v0, 6
      add
           $t8, $t8, $v1
     sll
            $t8, $t8, 2
     li
            $a1, RED
      add
           $a2, $k0, $t8
            $a1, 0($a2)
      SW
      add
           $v1, $v1, 1
     j
            For8
EndFor8:
     add
           $v0, $v0, 1
```

For7

\$v0, 10

j

Exit: li

Mở bitmap display trong tool sau khi ấn vào nút thực thi (hình cờ lê), kết nối bitmap với MIPS, chỉnh độ rộng, độ dài đơn vị điểm ảnh là 8 pixel, chỉnh độ rộng, độ dài của màn hình bitmap là 512 pixel. Khi đó, ở màn hình bitmap có (512/8)\*(512/8) = 4096 điểm ảnh. Chạy để hiện kết quả.

• Với  $(x_1;y_1) = (40;60)$  và  $(x_2;y_2) = (15;9)$ :

