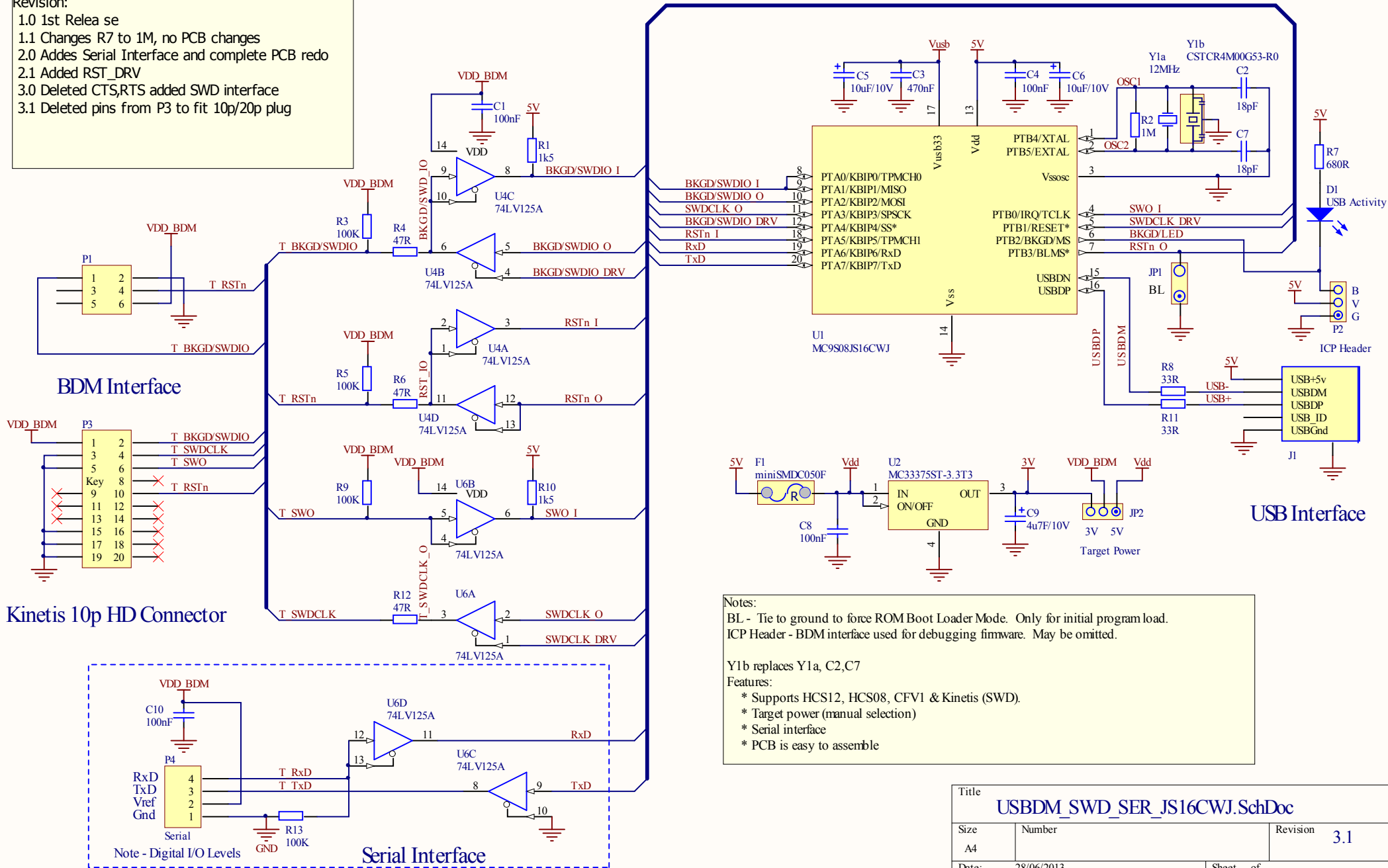


Revision:
 1.0 1st Release
 1.1 Changes R7 to 1M, no PCB changes
 2.0 Adds Serial Interface and complete PCB redo
 2.1 Added RST_DRV
 3.0 Deleted CTS,RTS added SWD interface
 3.1 Deleted pins from P3 to fit 10p/20p plug



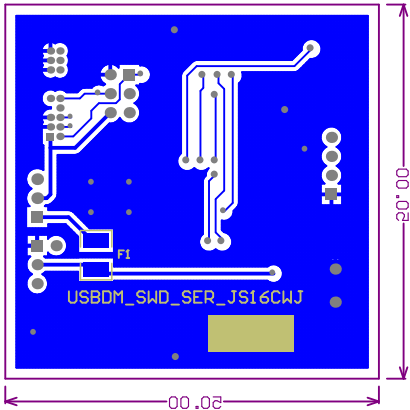
Notes:
 BL - Tie to ground to force ROM Boot Loader Mode. Only for initial program load.
 ICP Header - BDM interface used for debugging firmware. May be omitted.

Y1b replaces Y1a, C2,C7

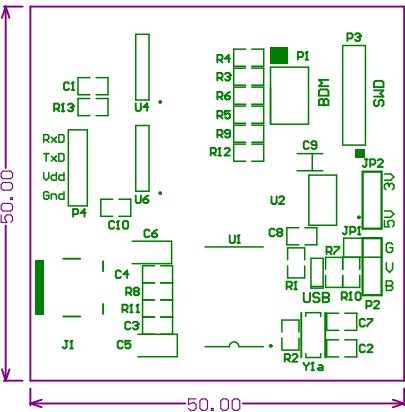
Features:
 * Supports HCS12, HCS08, CFV1 & Kinetis (SWD).
 * Target power (manual selection)
 * Serial interface
 * PCB is easy to assemble

Title		
USBDM_SWD_SER_JS16CWJ.SchDoc		
Size	Number	Revision
A4		3.1
Date:	28/06/2013	Sheet of
File:	C:\Users\...USBDM_SWD_SER_JS16CWJ.SchDoc	Drawn By:

usbdm_swd_ser_js16cwj.bcpdoc



Board Outline this layer



USBDM_SWD_SER_JS16CJ.PcbDoc

usbdm_swd_ser_js16cm1.bcpdoc

USBDM_SWD_SER_JS16CWJ

F1



20.00

20.00

Board Outline this layer

USBDM_JS16CWJ_V2

[illegible]