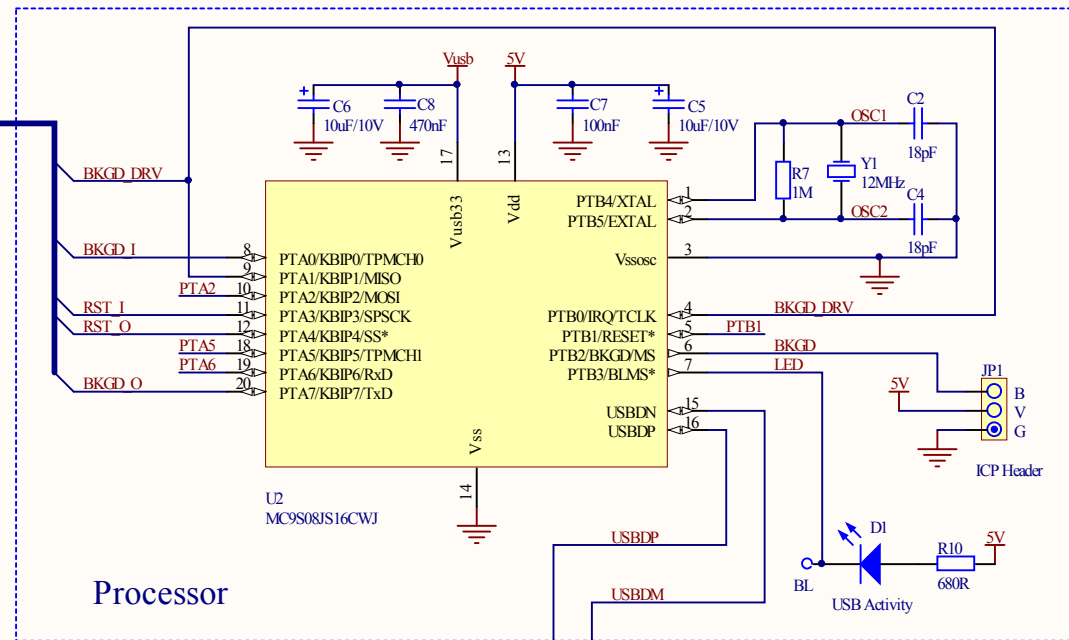
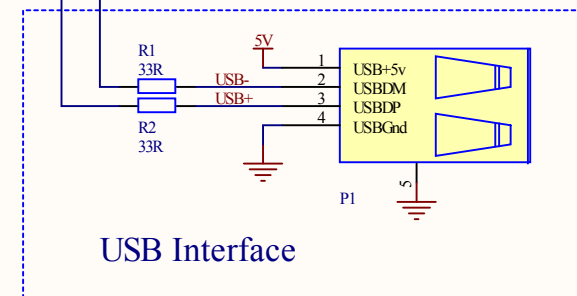


BDM Interface



Processor



USB Interface

Revision:

1.0 1st Release

1.1 Changes R7 to 1M, no PCB changes

Notes:

BL - Tie to ground to force ROM Boot Loader Mode. Only for initial program load.
 ICP Header - BDM interface used for debugging firmware. May be omitted.
 Z1 - Protection device - may be omitted

This version only supports HCS12, HCS08, & CFV1.
 It does not support RS08 or CFV2,3,4 devices
 It does not supply target power.

Title

USBDM_JS16CWJ.SchDoc

Size
A4

Number

Revision

1.0

Date: 20/10/2010

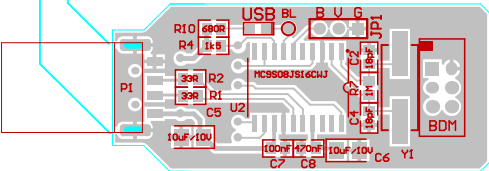
Sheet of

File: C:\Peter\USBDM_JS16CWJ.SchDoc

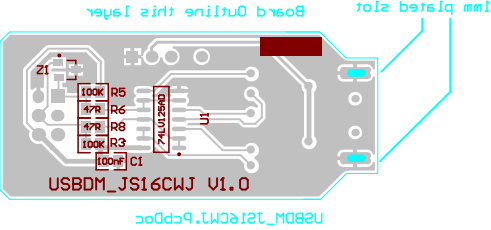
Drawn By:

1mm plated slot

Board Outline this layer

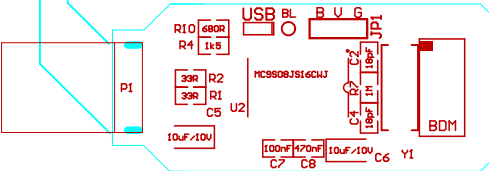


USBDM_JS16CWJ.PcbDoc

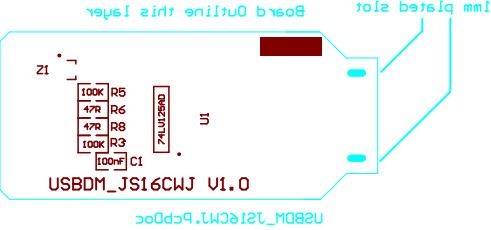


1mm plated slot

Board Outline this layer



USBDM_JS16CWJ.PcbDoc



USBDM_SOIC20_JS16(Minimal)