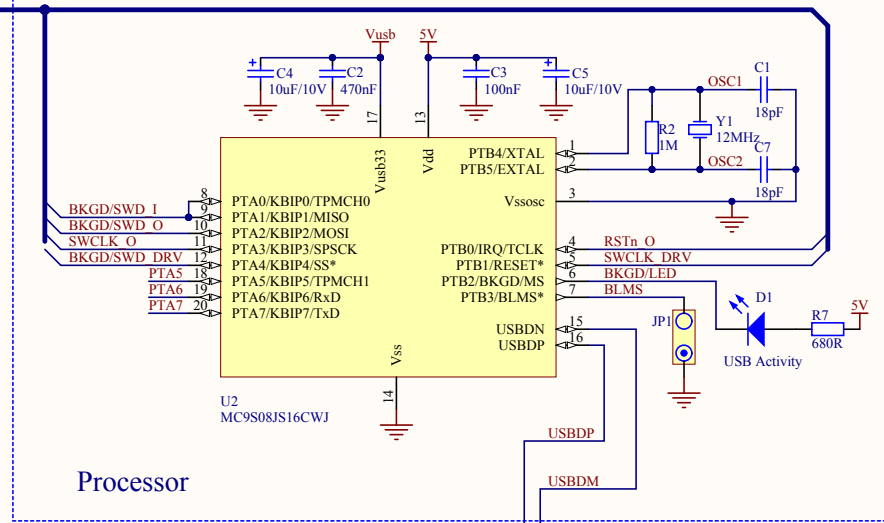
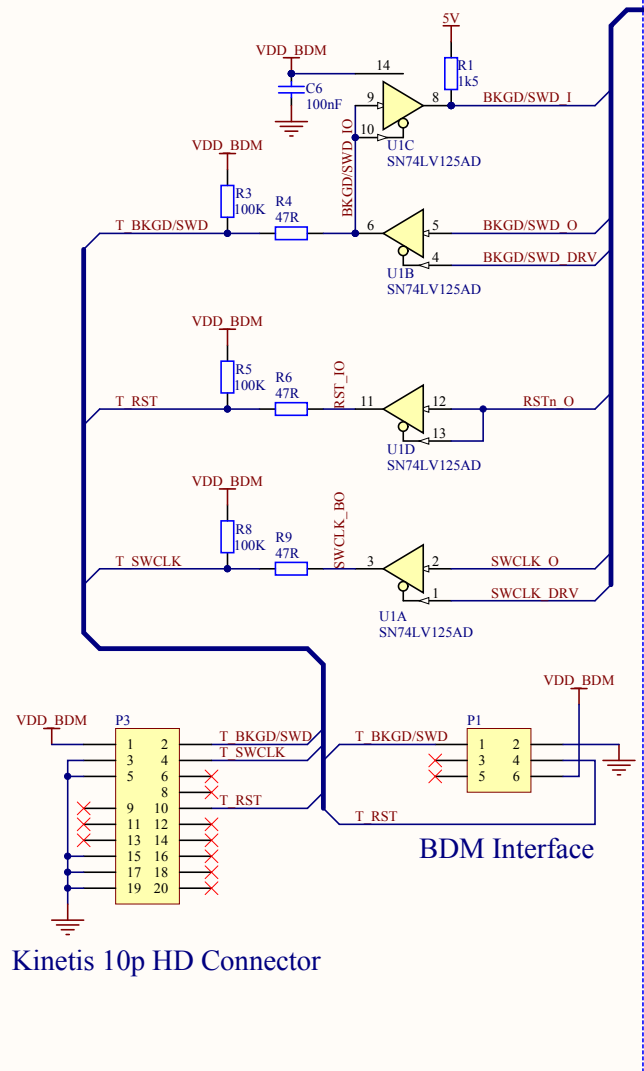


A

B

C

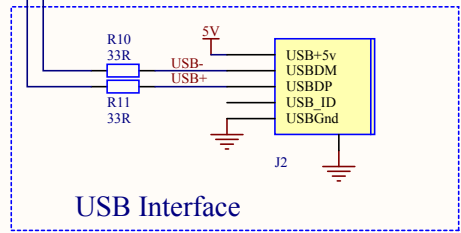
D



NOTE:
 TI SN74LV125 devices must be used since they are 5V I/O tolerant.
 Similar devices from other manufacturers may not be.

Revision:
 1.0 1st Release
 1.1 Added missing Gnd connection on PCB

Notes:
 JP1 - Tie to ground to force ROM Boot Loader Mode. Only for initial program load.
 This version only supports HCS12, HCS08, & CFV1, Kinetis (SWD)
 It does not support RS08 or CFV2,3,4 devices
 It does not supply target power.
 It does not have external target reset detection.



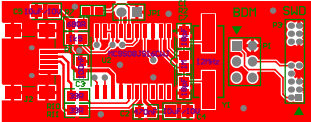
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Size	Number		Revision	
A4			1.1	
Date:	6/03/2014		Sheet of	
File:	C:\Users\... \USBDM_SWD_JS16CWJ_V2.SchDoc		By:	

A

B

C

D



USBDM_SWD_JS16C4J U1.1

R9

47R

R8

100K

100K

R7

47R

47R

100K

R4

U1

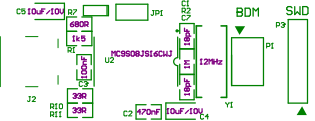
R6

R5

100nF

C6

DS16C43-000



USBDM_SWD_JS16CWI V1.1



USBDM_SOIC20_JS16(Minimal)