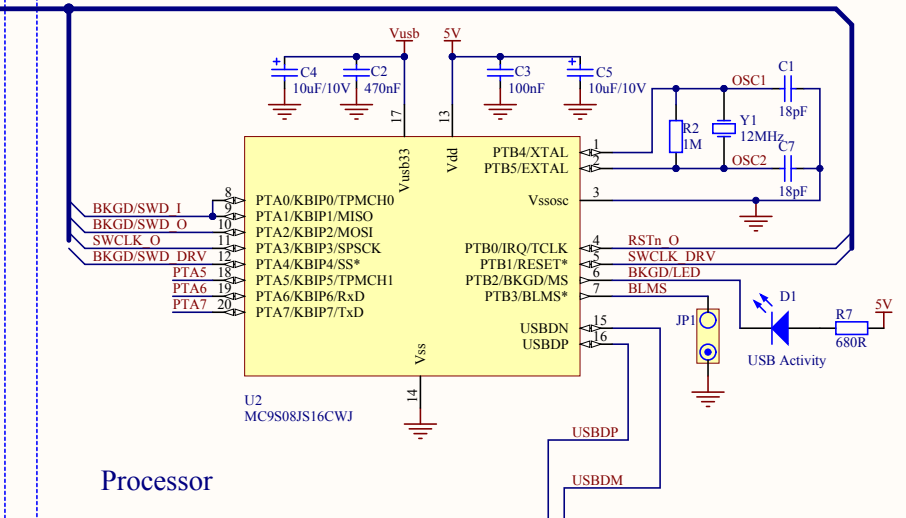


Kinetis 10p HD Connector

BDM Interface



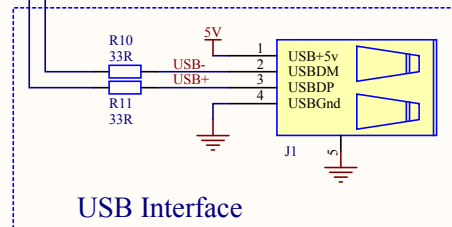
Processor

**NOTE:**  
TI SN74LV125 devices must be used since they are 5V I/O tolerant.  
Similar devices from other manufacturers may not be.

**Revision:**  
1.0 1st Release  
1.1 Added missing Gnd connection on PCB

**Notes:**  
JP1 - Tie to ground to force ROM Boot Loader Mode. Only for initial program load.

This version only supports HCS12, HCS08, & CFV1, Kinetis (SWD)  
It does not support RS08 or CFV2,3,4 devices  
It does not supply target power.  
It does not have external target reset detection.

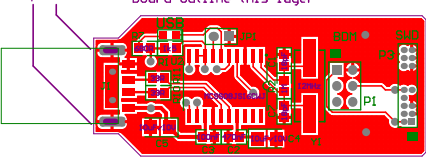


USB Interface

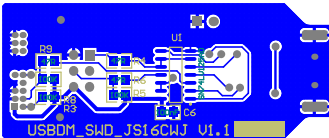
Title		USBDM_SWD_JS16CWJ.SchDoc	
Size	Number	Revision	
A4		1.1	
Date:	24/02/2014	Sheet of	
File:	C:\Users\...USBDM_SWD_JS16CWJ.SchDoc	Download By:	

1mm plated slot

Board Outline this layer



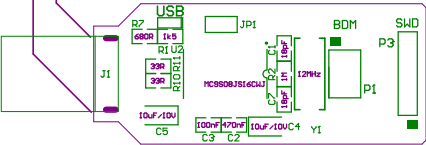
USBDM\_SWD\_JS16CWJ.PcbDoc



USBDM\_SWD\_JS16CWJ V1.1

Imm plated slot

Board Outline this layer



USBDM\_SWD\_JS16CWJ.PcbDoc



USBDM\_SWD\_JS16CWJ V1.1

## USBDM\_SOIC20\_JS16(Minimal)

[illegible]