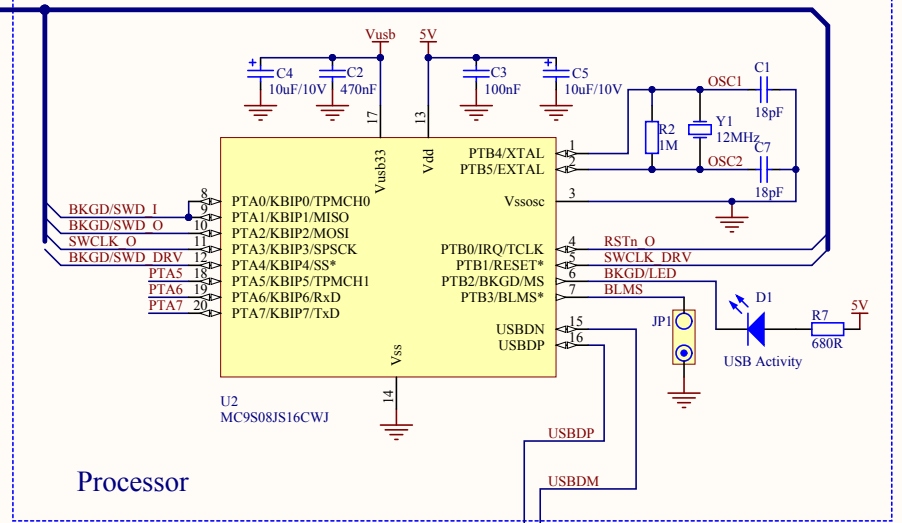
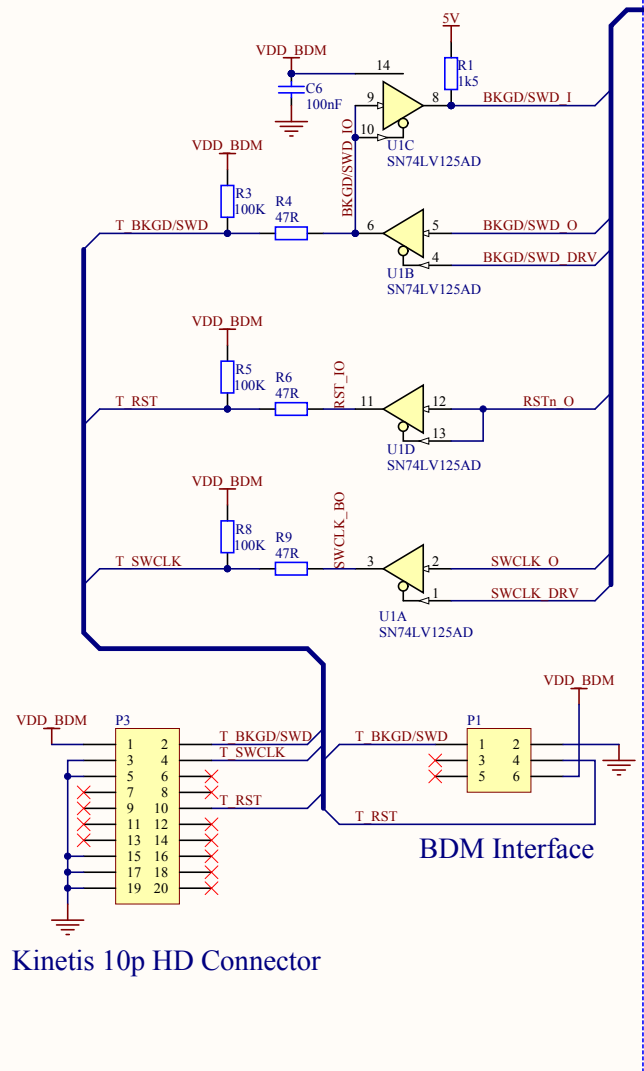


A

B

C

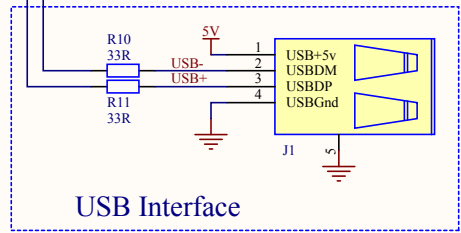
D



NOTE:
 TI SN74LV125 devices must be used since they are 5V I/O tolerant.
 Similar devices from other manufacturers may not be.

Revision:
 1.0 1st Release
 1.1 Added missing Gnd connection on PCB

Notes:
 JP1 - Tie to ground to force ROM Boot Loader Mode. Only for initial program load.
 This version only supports HCS12, HCS08, & CFV1, Kinetis (SWD)
 It does not support RS08 or CFV2,3,4 devices
 It does not supply target power.
 It does not have external target reset detection.



Title		USBDM_SWD_JS16CWJ.SchDoc	
Size	A4	Number	
Date:	24/02/2014	Sheet of	
File:	C:\Users\...USBDM_SWD_JS16CWJ.SchDoc	Download By:	
		Revision	1.1

A

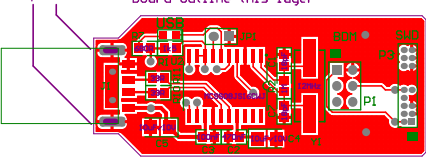
B

C

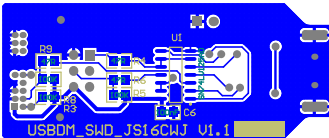
D

1mm plated slot

Board Outline this layer



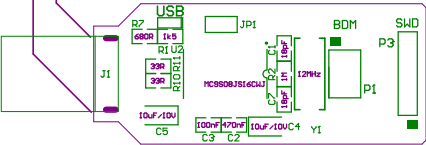
USBDM_SWD_JS16CWJ.PcbDoc



USBDM_SWD_JS16CWJ V1.1

Imm plated slot

Board Outline this layer



USBDM_SWD_JS16CWJ.PcbDoc



R3
R4
R5



R6
R7
R8



C6



SN74LUI25AD

USBDM_SWD_JS16CWJ V1.1



USBDM_SOIC20_JS16(Minimal)

[illegible]