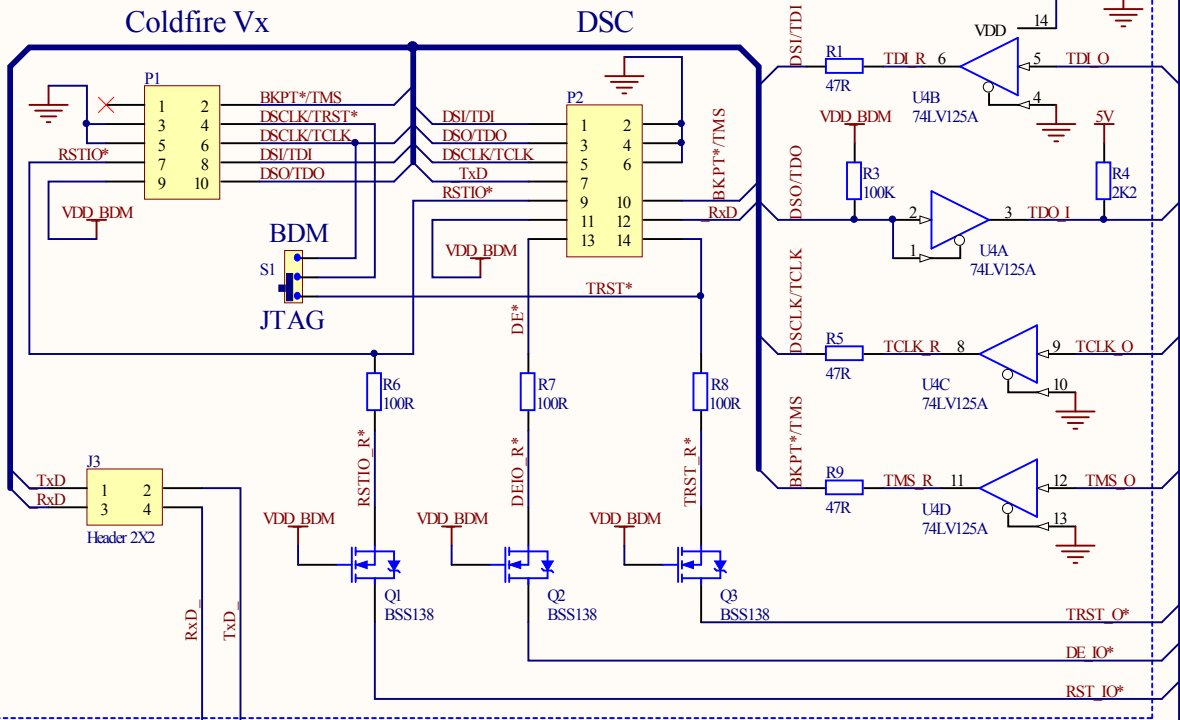
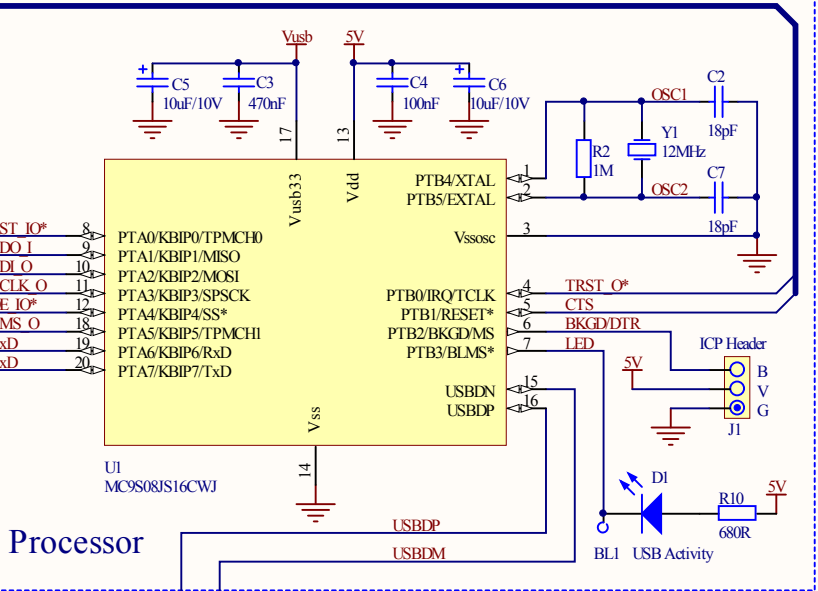
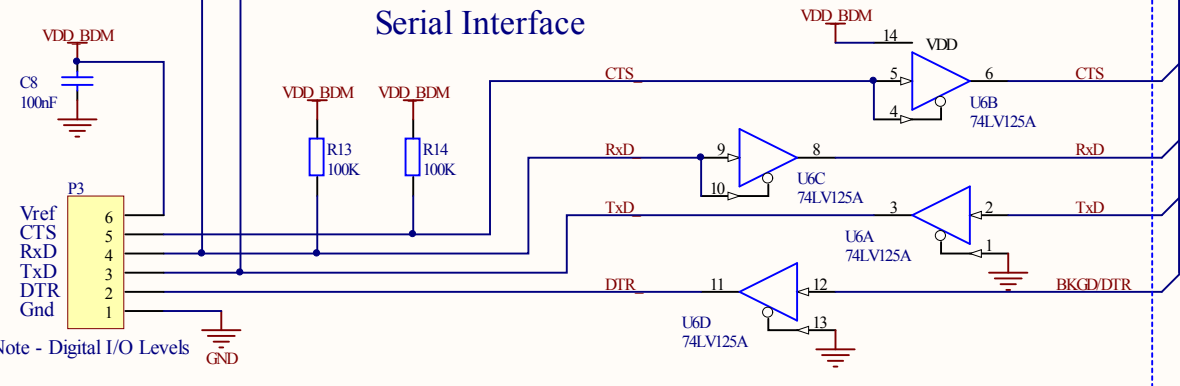


Revision:
- 1.1 1st Release
- 2.0 Added Serial port & complete redo of PCB

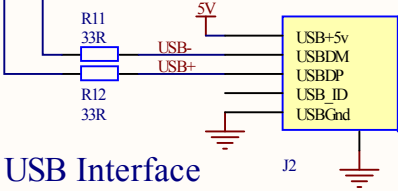
BDM Interfaces



Serial Interface



USB Interface



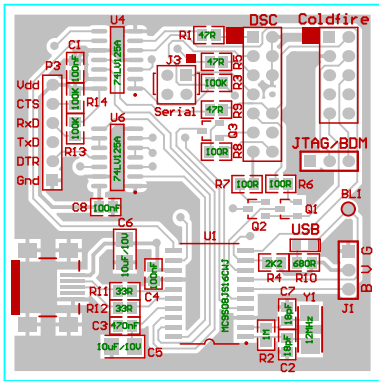
Notes:
BL1 - Tie to ground to force ROM Boot Loader Mode. Only for initial program load.
J1 - ICP Header - BDM interface used for debugging firmware. May be omitted.
S1 - BDM/JTAG - Coldfire BDM (debug) / Coldfire JTAG (unsecure)
Currently DE* & TRST* are unused so associated components may be omitted.
J3 - Link to connect RxD/TxD to spare pins on DSC header

This version only supports MC56F800xx and Coldfire V2-3 devices.
It does not support RS08, HCS08, HCS12 or Coldfire V1.
It does not supply target power.

Title			
USBDM_CF_SER_JS16CWJ.SchDoc			
Size	Number		Revision
A4			2.0
Date:	20/10/2010	Sheet of	
File:	C:\Peter\USBDM CF_SER_JS16CWJ.SchDoc Drawn By:		

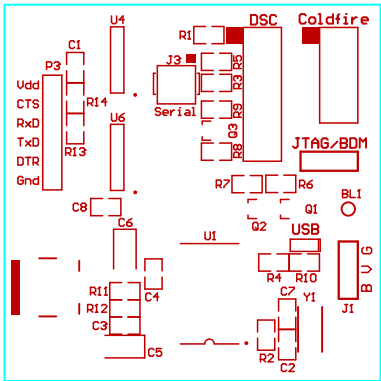
50.00

50.00



Board Outline this layer

50.00



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USBDM_CF_SER_JS16CWJ