Generation of x86-64 Assembly

```
CoFixpoint example_block_1 :=
  Block (Normal 1) [] [
    r(0) <- i(5);
    r(1) <- r(0)
  ] (jump example_block_2).

with example_block_2 :=
  Block (Normal 2) [
    r(2) <- phi [(0, Normal 1); (4, Normal 2)];
    r(3) <- phi [(1, Normal 1); (5, Normal 2)]
  ] [
    r(4) <- r(2) - i(1);
    r(5) <- r(3) * r(4)
  ] (if r(4) <= i(1)
    then example_block_3
    else example_block_2).

with example_block_3 :=
  Block (Normal 3) [] [] (ret r(5)).
```

```
L1:
        mov rcx, 5
        mov rbx, rcx
        jmp L2
L2:
        sub rcx, 1
        imul rbx, rcx
        cmp rcx, 1
        jle L2.1
        jmp L2.2
L2.1:
        jmp L3
L3:
        mov rax, 60
        mov rdi, rbx
        syscall
L2.2:
        jmp L2
```