INSTRUCCIONES DE CARGA Y ALMACENAMIENTO									
Instr.	Ejemplo	Significado	Código de operación						Microinstrucciones
LI	LI Rd, #Slit16	Rd = Slit16	01	Rd	Slit16				WR
LWI	LWI Rd, lit16	Rd = Mem[lit16]	02	Rd	lit16				SDMD SWD WR
LW	LW Rd,lit12(Rt)	Rd = Mem[Rt+lit12]	23	Rd	Rt	lit12			SWD SEXT SOP2 ALUOP=0011 WR LF
SWI	SWI Rd, lit16	Mem[lit16] = Rd	03	Rd	lit16				SR2 SDMD WD
SW	SW Rd, lit12(Rt)	Mem[Rt+lit12] = Rd	04	Rd	Rt	lit12			SR2 SEXT SOP2 ALUOP=0011 WD LF
INSTRUCCIONES ARITMÉTICAS									
ADD	ADD Rd,Rt,Rs	Rd = Rt+Rs	00	Rd	Rt	Rs	S/U	00	SWD WR LF SR ALUOP=0011
SUB	SUB Rd,Rt,Rs	Rd = Rt-Rs	00	Rd	Rt	Rs	S/U	01	SWD WR SR ALUOP=0111 LF
ADDI	ADDI Rd,Rt,#Slit12	Rd = Rt+Slit12	05	Rd	Rt	Slit12			SWD WR LF SR ALUOP=0011 SOP2
SUBI	SUBI Rd,Rt,#Slit12	Rd = Rt-Slit12	06	Rd	Rt	Slit12			SWD WR SR SOP2 ALUOP=0111 LF
INSTRUCCIONES LÓGICAS									
AND	AND Rd,Rt,R	Rd=Rt&Rs	00	Rd	Rt	Rs	S/U	02	SWD WR SR ALUOP=0000 LF
OR	OR Rd,Rt,Rs	Rd=Rt | Rs	00	Rd	Rt	Rs	S/U	03	SWD WR SR ALUOP=0001 LF
XOR	XOR Rd,Rt,Rs	Rd=Rt ^ Rs	00	Rd	Rt	Rs	S/U	04	SWD WR SR ALUOP=0010 LF
NAND	NAND Rd,Rt,Rs	Rd=~(Rt & Rs)	00	Rd	Rt	Rs	S/U	05	SWD WR SR ALUOP=1101 LF
NOR	NOR Rd,Rt,Rs	Rd=~(Rt | Rs)	00	Rd	Rt	Rs	S/U	06	SWD WR SR ALUOP=1100 LF
XNOR	NOR Rd,Rt,Rs	Rd=~(Rt ^ Rs)	00	Rd	Rt	Rs	S/U	07	SWD WR SR ALUOP=0110 LF
NOT	NOT Rd, Rs	Rd = ~Rs	00	Rd	Rs	Rs	S/U	08	SWD WR SR ALUOP=1101 LF
ANDI	ANDI Rd,Rt,#lit12	Rd=Rt & lit12	07	Rd	Rt	lit12			SEXT SOP2 ALUOP=0000 LF SR SWD WR
ORI	ORI Rd,Rt,#lit12	Rd=Rt | lit12	08	Rd	Rt	lit12			SEXT SOP2 ALUOP=0001 LF SR SWD WR

XORI	XORI Rd,Rt,#lit12	Rd=Rt ^ lit12	09	Rd	Rt	lit12			SEXT SOP2 ALUOP=0010 LF SR SWD WR
NANDI	NANDI Rd,Rt,#lit12	Rd=~(Rt & lit12)	10	Rd	Rt	lit12			SEXT SOP2 ALUOP=1101 LF SR SWD WR
NORI	NORI Rd,Rt,#lit12	Rd=~(Rt | lit12)	11	Rd	Rt	lit12			SEXT SOP2 ALUOP=1100 LF SR SWD WR
XNORI	XNORI Rd,Rt,#lit12	Rd=~(Rt ^ lit12)	12	Rd	Rt	lit12			SEXT SOP2 ALUOP=0110 LF SR SWD WR
INSTRUCCIONES DE CORRIMIENTO									
SLL	SLL Rd,Rt,#lit4	Rd=Rt<<lit4	00	Rd	Rt	S/U	lit4	09	SHE DIR WR
SRL	SRL Rd,Rt,#lit4	Rd=Rt>>lit4	00	Rd	Rt	S/U	lit4	10	SHE WR
INSTRUCCIONES DE SALTOS CONDICIONALES E INCONDICIONALES									
BEQI	BEQI Rd,Rt,Slit12	If(Rd==Rt) goto Slit12 PC = PC + Slit12	13	Rd	Rt	Slit12			SR2 LF ALUOP=0111 SOP1 SOP2 ALUOP=0011 SR SDMP WPC
BNEI	BNEI Rd,Rt,Slit12	If(Rd!=Rt) goto Slit12 PC = PC + Slit12	14	Rd	Rt	Slit12			SR2 LF ALUOP=0111 SOP1 SOP2 ALUOP=0011 SR SDMP WPC
BLTI	BLTI Rd,Rt,Slit12	If(Rd<Rt) goto Slit12 PC = PC + Slit12	15	Rd	Rt	Slit12			SR2 LF ALUOP=0111 SOP1 SOP2 ALUOP=0011 SR SDMP WPC
BLETI	BLETI Rd,Rt,Slit12	If(Rd<=Rt) goto Slit12 PC = PC + Slit12	16	Rd	Rt	Slit12			SR2 LF ALUOP=0111 SOP1 SOP2 ALUOP=0011 SR SDMP WPC
BGTI	BGTI Rd,Rt,Slit12	If(Rd>Rt) goto Slit12 PC = PC + Slit12	17	Rd	Rt	Slit12			SR2 LF ALUOP=0111 SOP1 SOP2 ALUOP=0011 SR SDMP WPC
BGETI	BGETI Rd,Rt,Slit12	If(Rd>=Rt) goto Slit12 PC = PC + Slit12	18	Rd	Rt	Slit12			SR2 LF ALUOP=0111 SOP1 SOP2 ALUOP=0011 SR SDMP WPC
B	B lit16	PC = lit16	19	S/U	lit16				WPC
INSTRUCCIONES DE MANEJO DE SUBRUTINAS									
CALL	CALL #lit16	PC(n+1) = lit16	20	S/U	lit16				UP WPC

RET	RET	PC = PC(n-1)	21	S/U	S/U	S/U	S/U	S/U	DW
OTRAS INSTRUCCIONES									
NOP	NOP		22	S/U	S/U	S/U	S/U	S/U	------