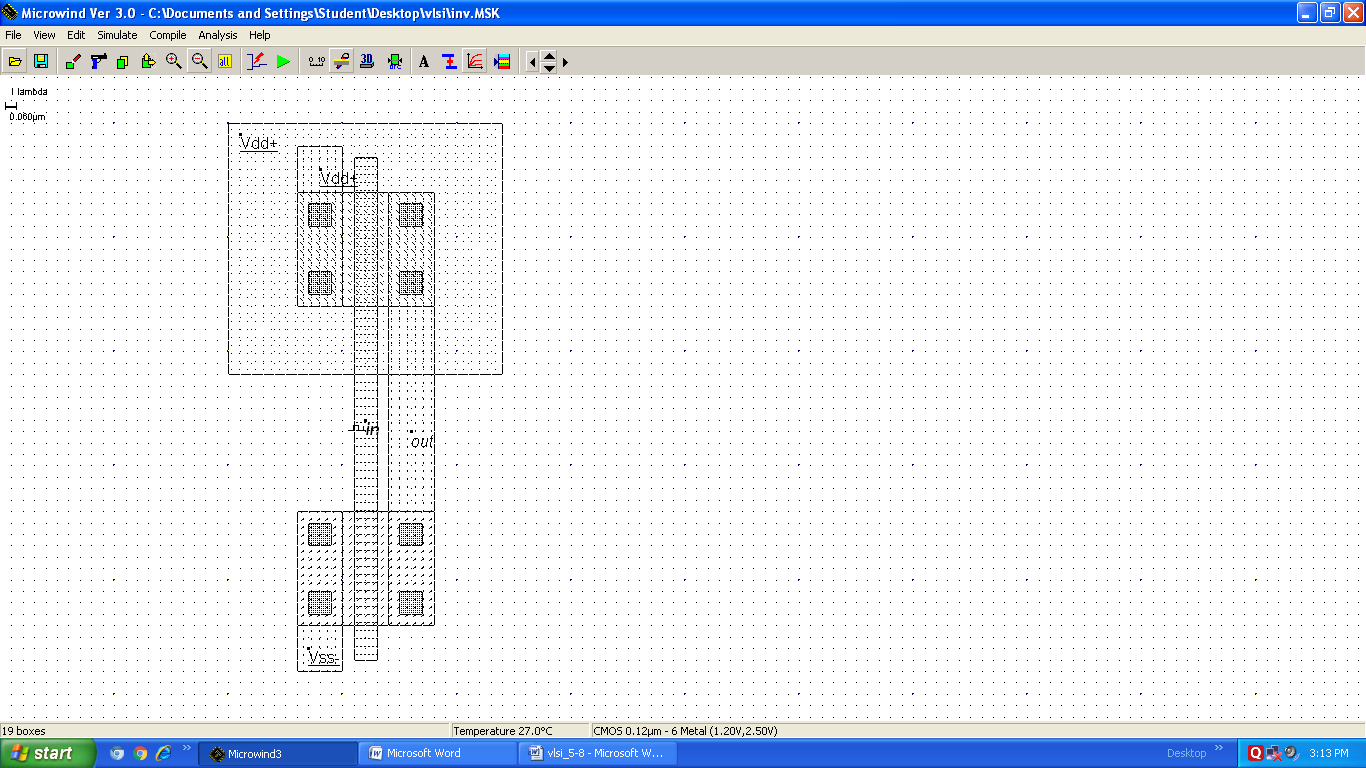
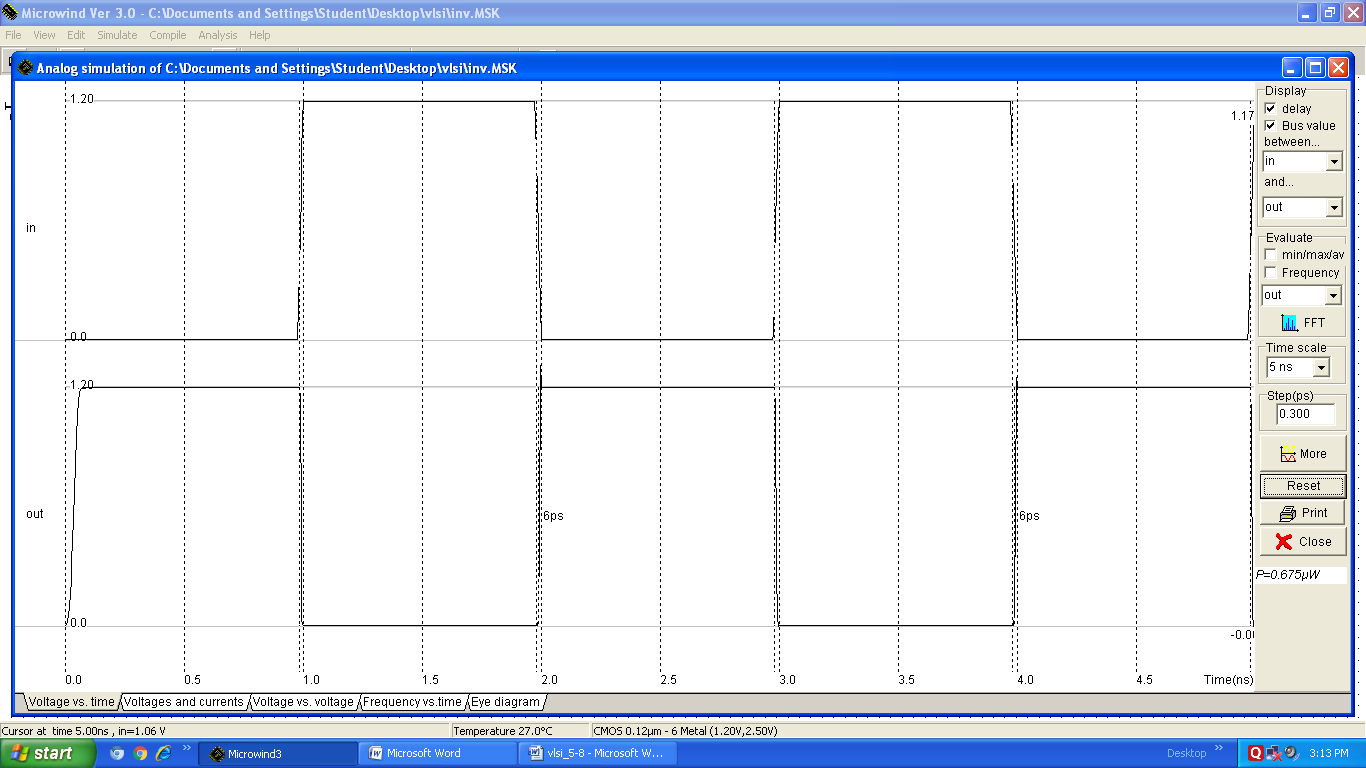
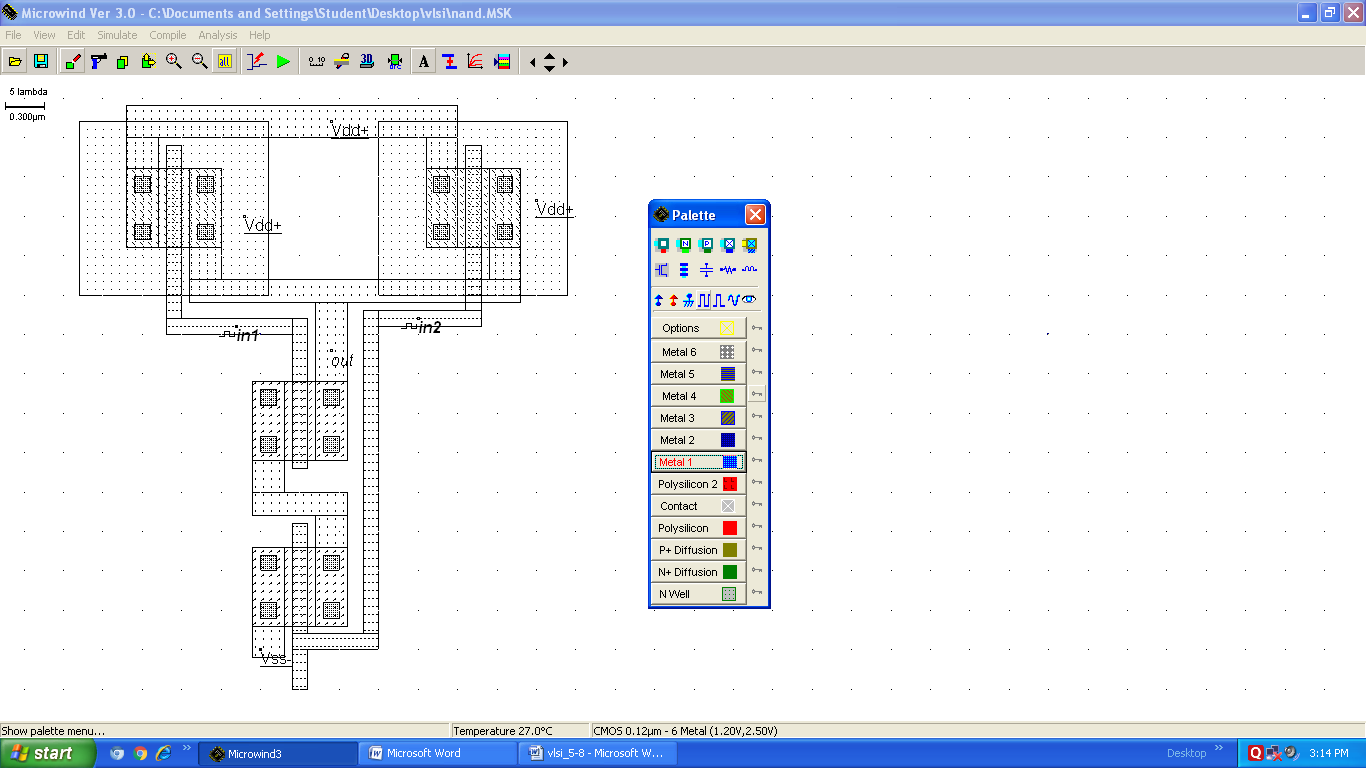
Exp 5.1 Inverter



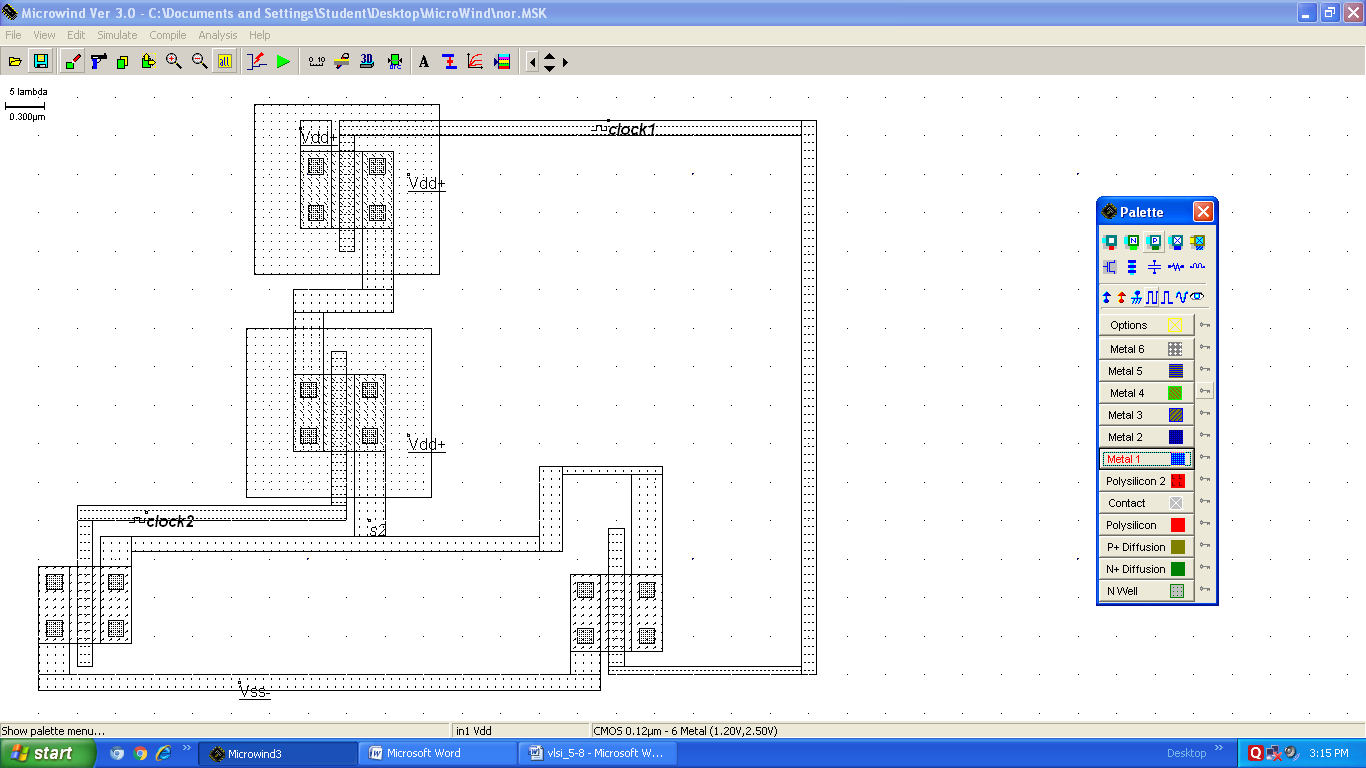


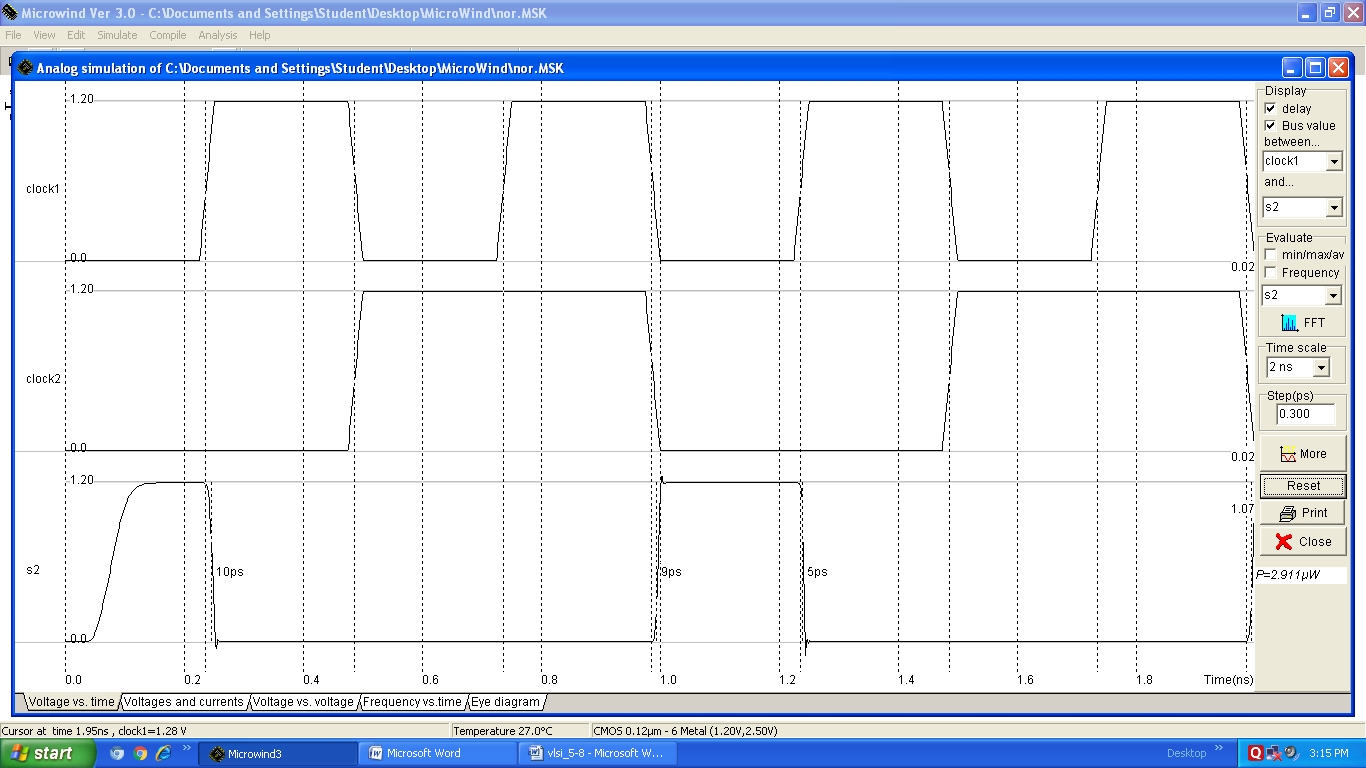
Exp 5.2 Nand



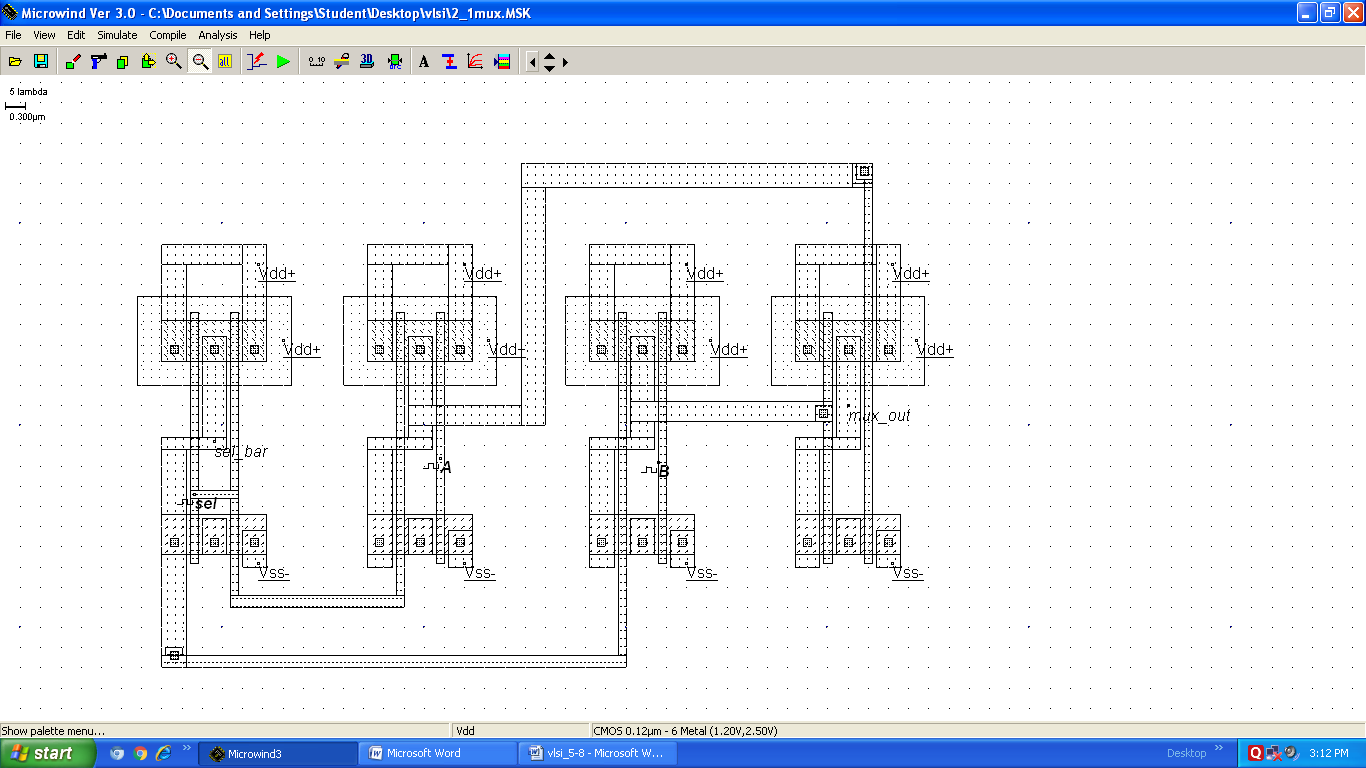


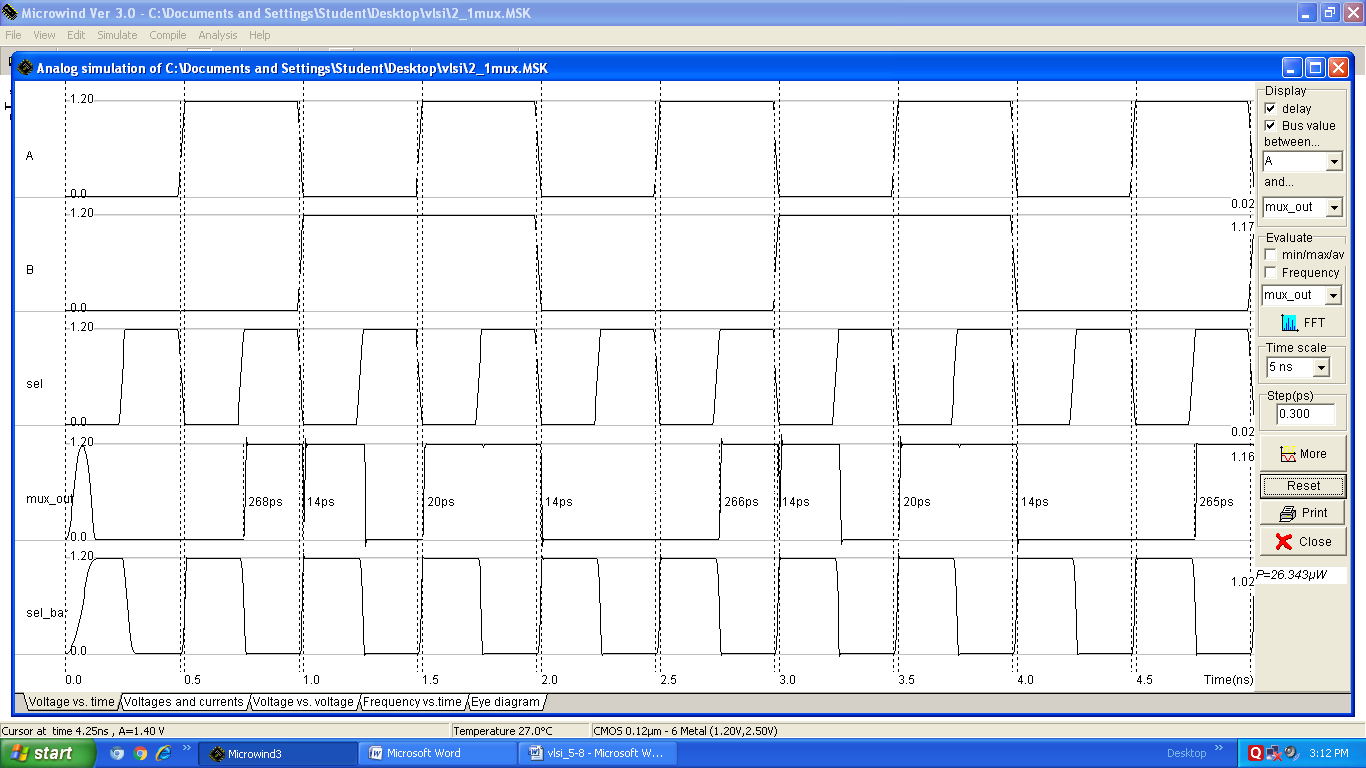
Exp 5.3 Nor



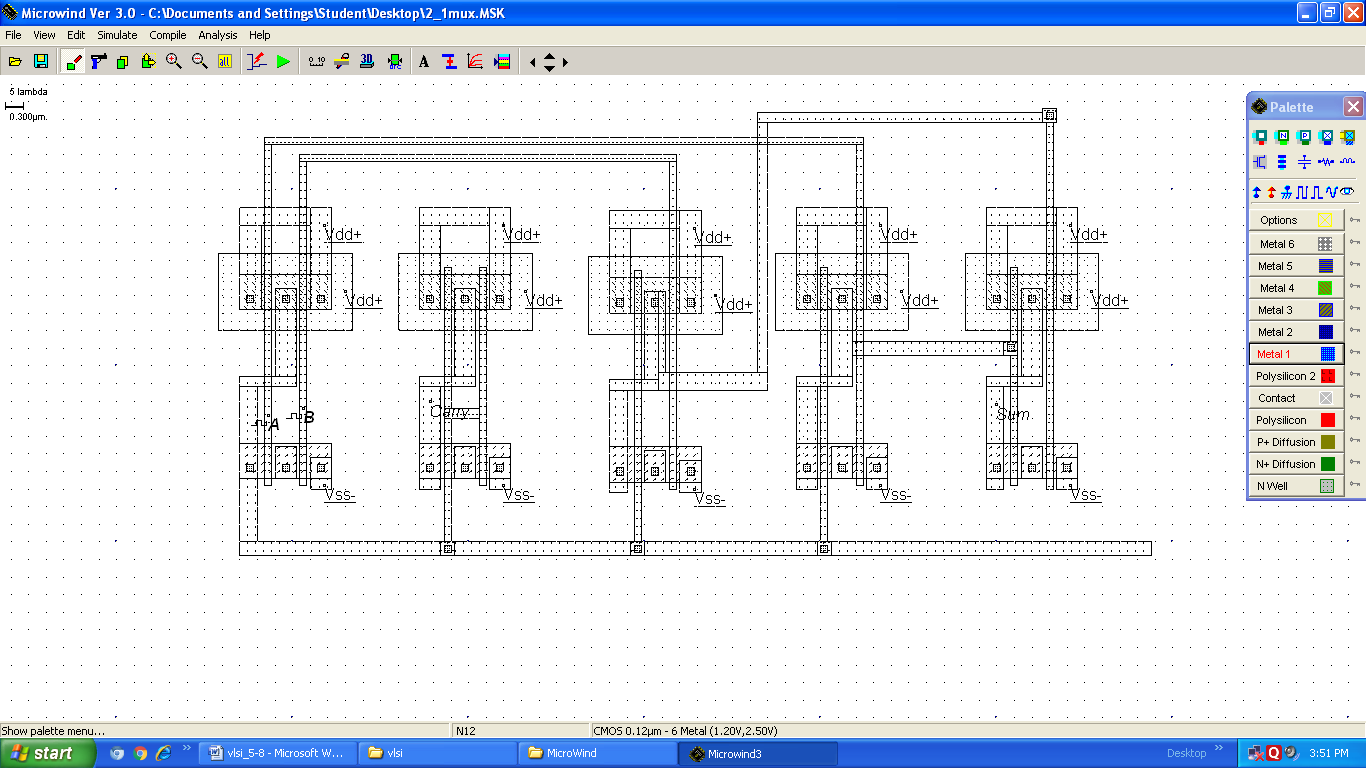


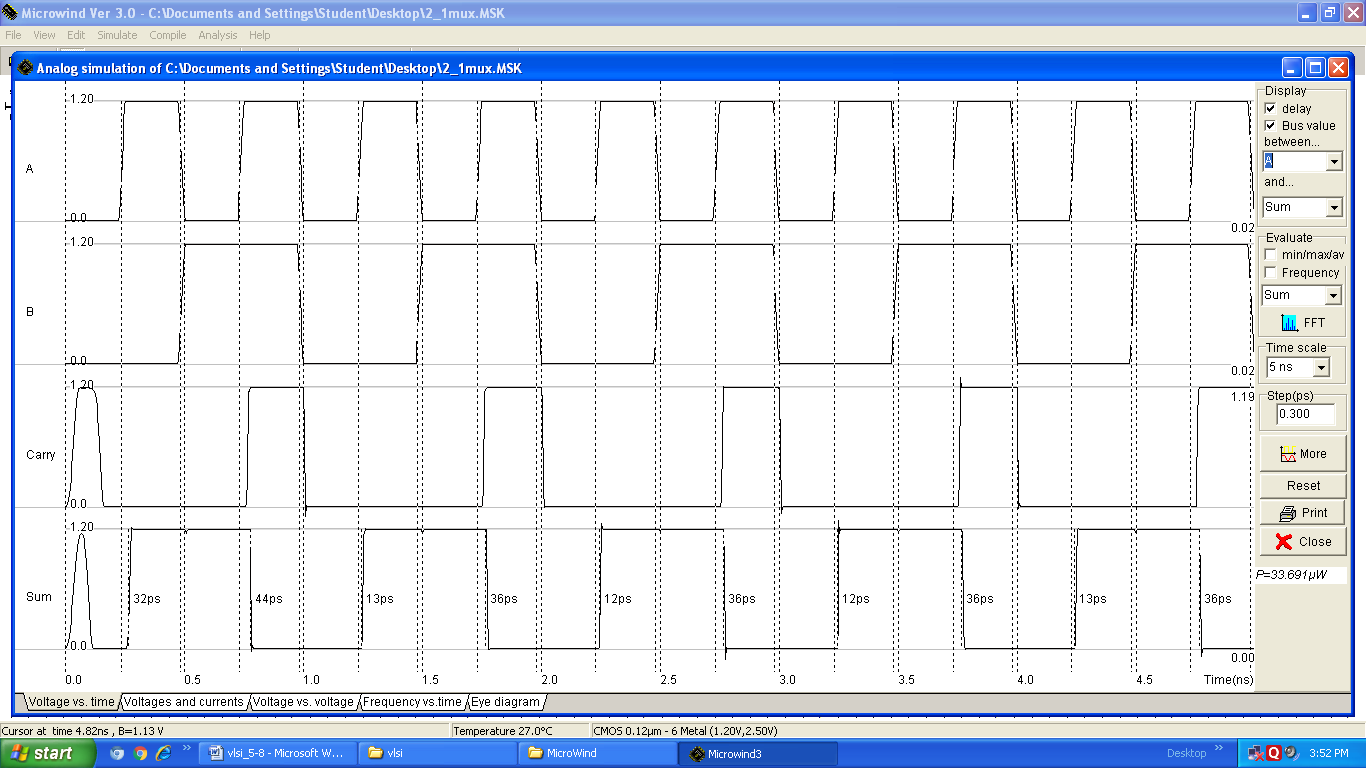
Exp 6. 2:1 Mux





Exp 8.1 Half Adder





Exp. 7 SRAM Cell

