

Asociación en paralelo de impedancia

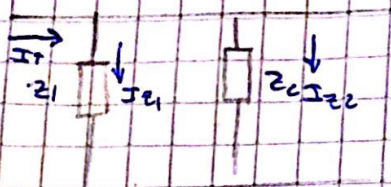
Admitancia $Y = \frac{1}{Z}$

$$[Y] = \frac{1}{Z}; \Omega^{-1}; \frac{V}{V} ; \frac{S}{1}$$

Siemens

$$Y = \frac{1}{R + jX} = G + jB \rightarrow \text{Susceptancia}$$

Conductancia



$$I_T = I_{Z1} + I_{Z2}$$

$$I_T = \frac{V_Z}{Z_1} + \frac{V_Z}{Z_2}$$

$$I_T = V_Z \cdot \left(\frac{1}{Z_1} + \frac{1}{Z_2} \right) = V_Z \frac{Z_2 + Z_1}{Z_1 \cdot Z_2}$$

$$\frac{1}{Z_T} = Y_T = \frac{1}{Z_1} + \frac{1}{Z_2}$$

$$Y_T = Y_1 + Y_2$$

$$Z_C = \frac{j}{\omega C} \rightarrow Y_C = \frac{\omega C}{-j} \cdot \frac{j}{j} = j\omega C$$

$$Z_L = j\omega L \rightarrow Y_L = \frac{1}{j\omega L} = \frac{-j}{\omega L}$$

Capacitor ideal para el uso que se le da
Low ESR don menor cosas
Resistencia Serie opul

Si Y_1 e Y_2 son cap

$$Y_T = Y_1 + Y_2$$

$$Y_T = j\omega C_1 + j\omega C_2$$

$$Y_T = j\omega (C_1 + C_2)$$

