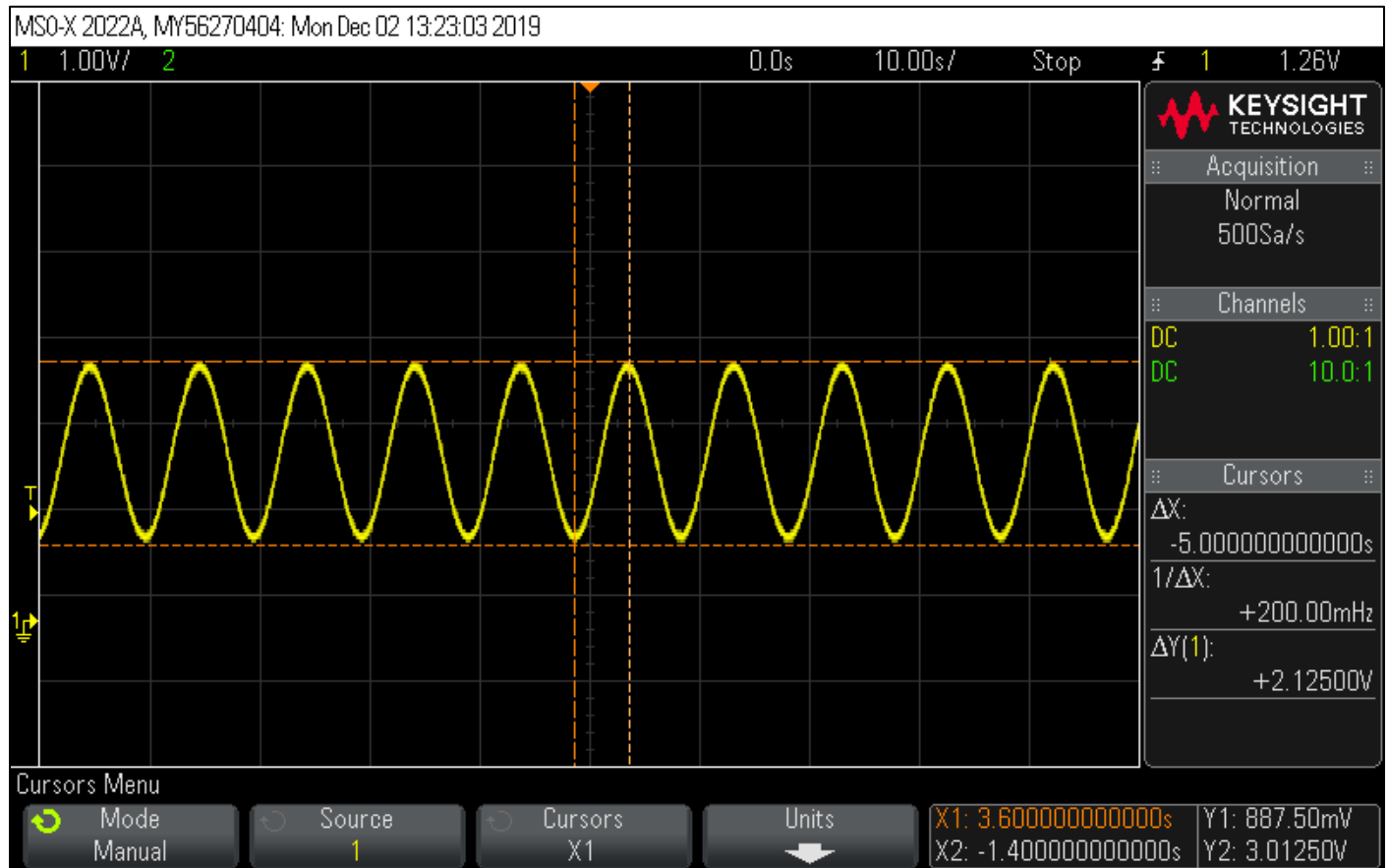


## Principles of Embedded Software

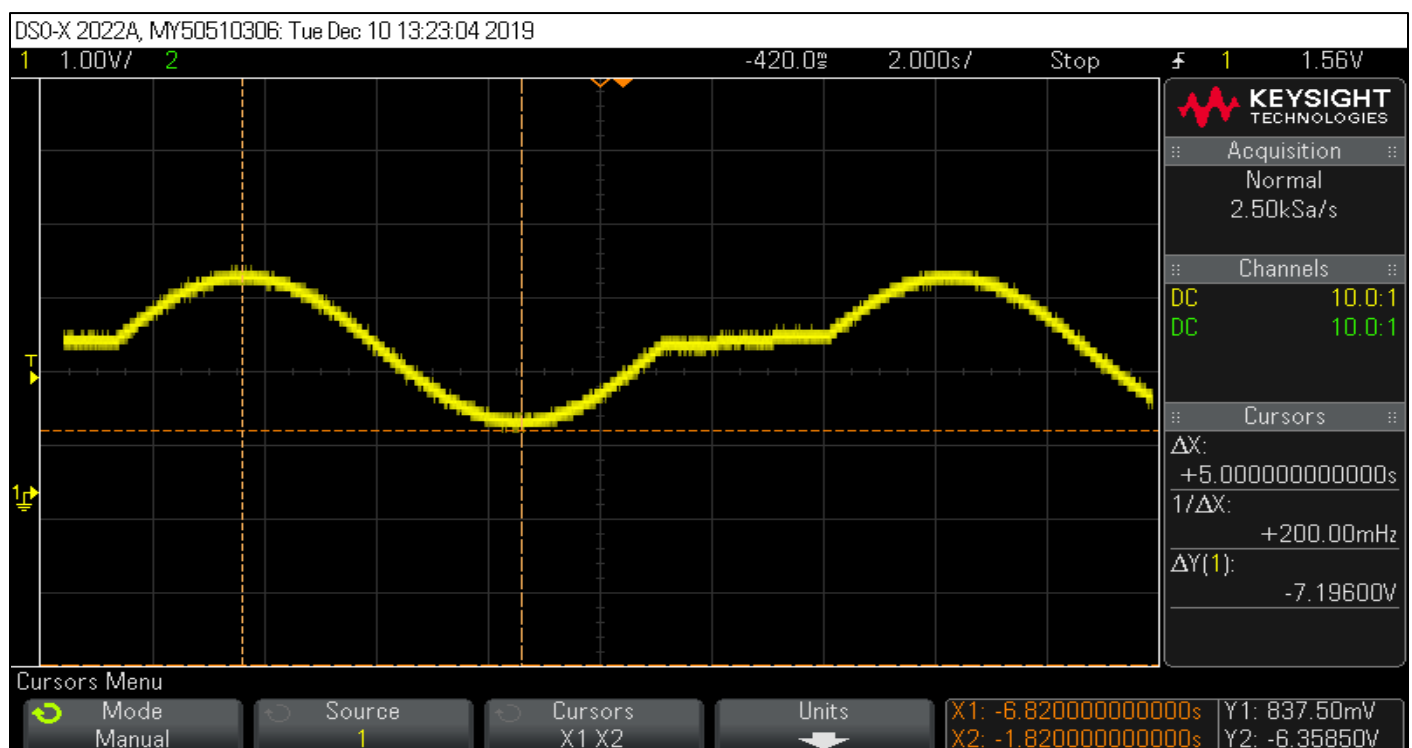
### Project – 6: FreeRTOS, ADC, DAC, DMA, DSP

Project By – Gitanjali Suresh (gisu9983) and Sarayu Managoli (sama2321)

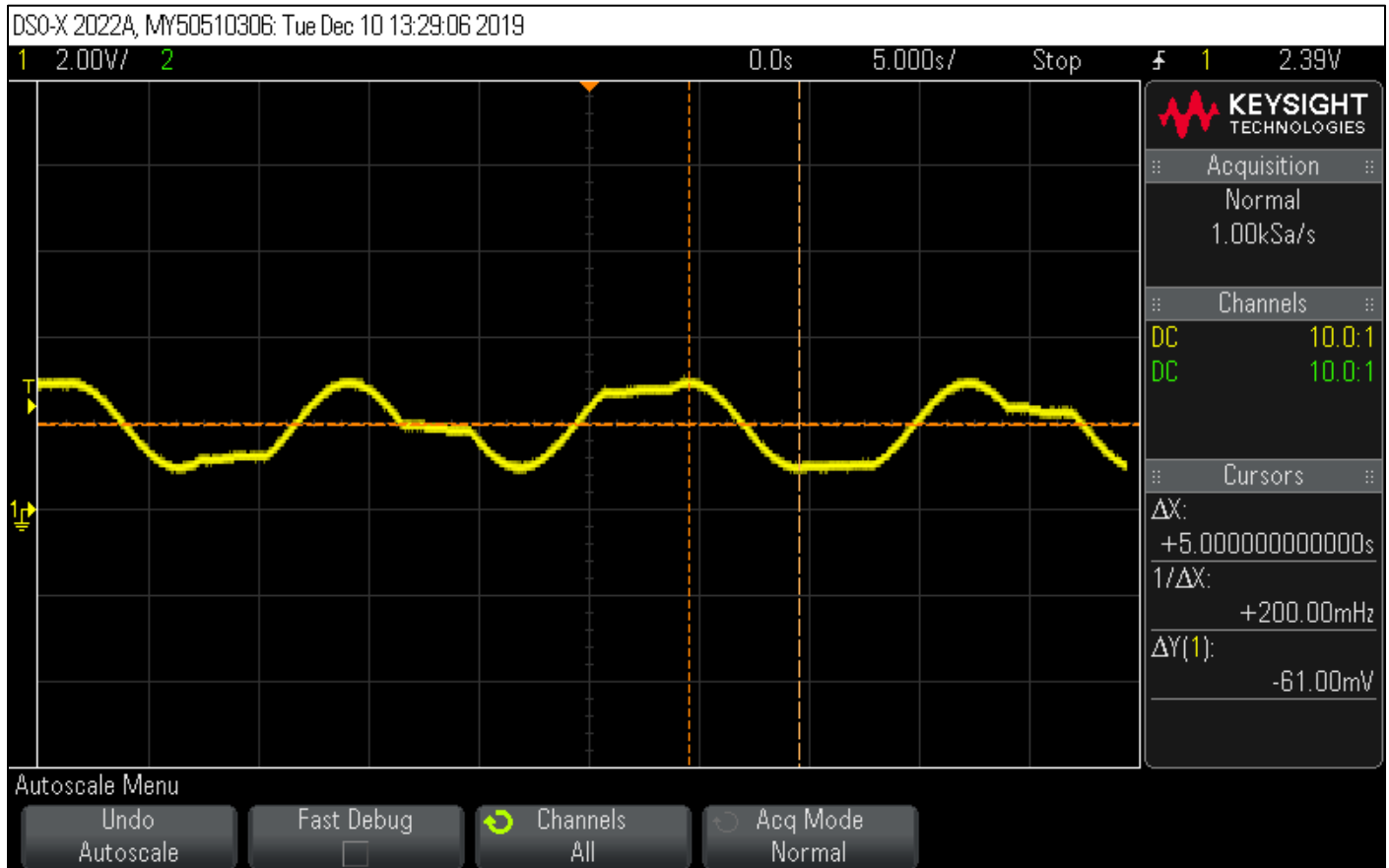
1. Sine wave screenshot without DMA transfer with a peak to peak time of 5s (Program 1).



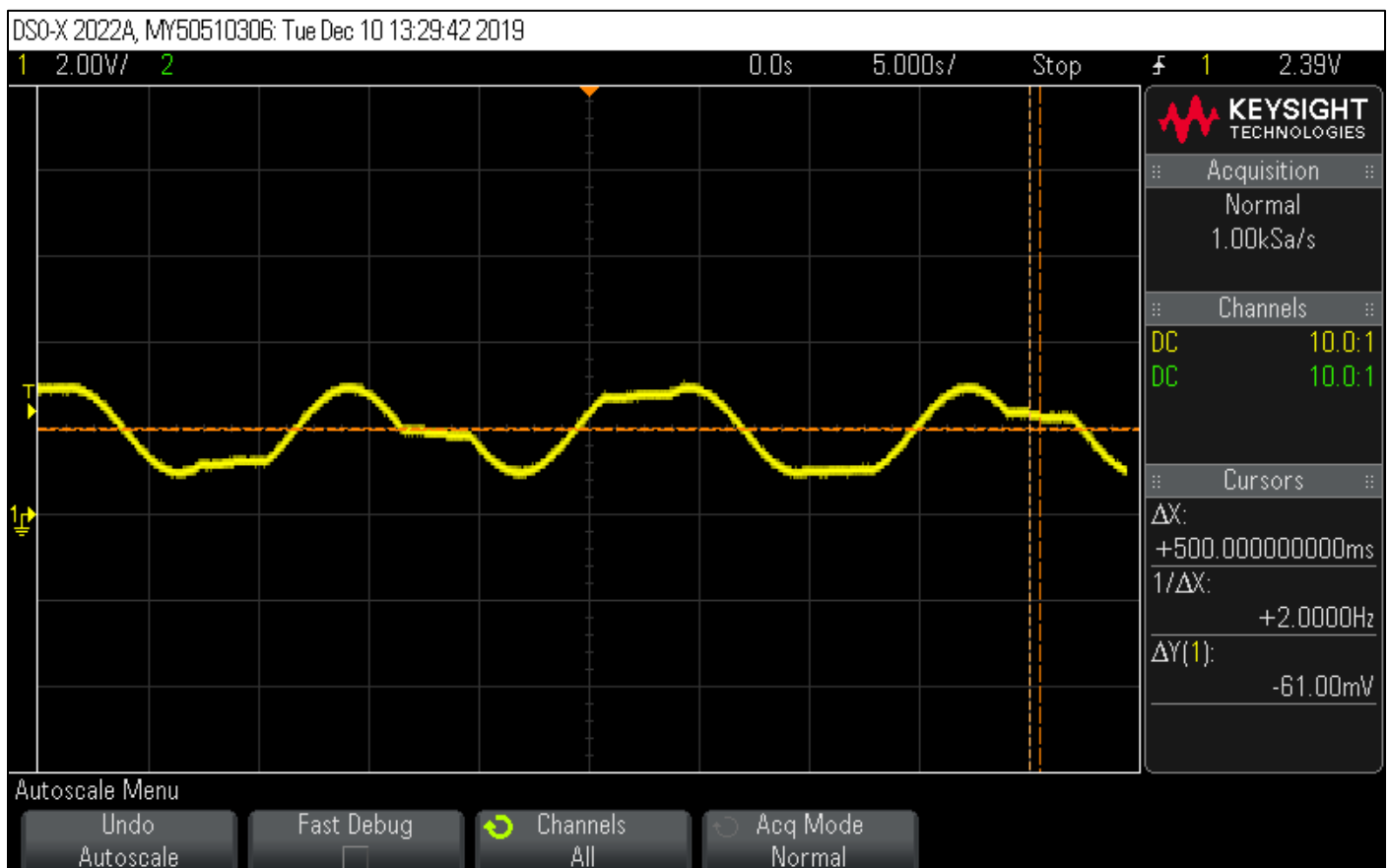
2. Sine wave screenshot with DMA transfer (depicted with a break in the sine wave) with a peak to peak time of 5s. (Buffer size – 100) (Program 2)



3. Sine wave screenshot with DMA transfer (depicted with a break in the sine wave) with a peak to peak time of 5s. (Buffer size equals 64. Hence, incomplete sine wave.) (Program 2)



4. Screenshot of the delay of 0.5 second using mutex before initiating DMA transfer. (Program 2)



5. Sine wave screenshot without DMA transfer with a peak to peak time of 5s at the ADC pin, PTE20.  
(Program 2)

