

Neural network computing for knight's tour problems

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Abstract

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This paper presents the first algorithm for finding a knight's tour on the general chessboard where the proposed algorithm is based on neural network computing. In the knight's tour problem a knight must traverse all of the squares on an $m \times n$ chessboard but visit every square once and only once and return to the originated square where the knight moves in an L -shape route.

Keywords. Knight's tour problems; neural networks; parallel algorithms; combinatorial optimization.

1. Introduction

The earliest attempt to find a knight's tour on the chessboard was made by Leonhard Euler in 1759 [2]. A knight must traverse all of the squares on an $m \times n$ chessboard but visit every square once and only once and return to the originated square where the knight moves in an L -shape route. Many great mathematicians including De Moivre, Vandermonde [9], Warnsdorff [10], Pratt [7], Roget, Legendre [5] and De Lavernede [3] attempted to solve the general problem. Their methods are based on either divide-and-conquer or backtracking dedicated for solving the 8×8 chessboard knight's tour problem. No general methods have been given to the problem in the last two centuries ([1]). W.W.R. Ball and H.S.M. Coxeter reintroduced the problem in their book in 1892 and 1974. A parallel algorithm for finding a knight's tour on the $m \times n$ chessboard is presented in this paper. The problem is to find a constrained Hamiltonian circuit which belongs to NP-complete problems. However, we are not sure whether the knight's tour problem is NP-complete or not. The artificial neural network computing makes it possible to provide the elegant algorithm in parallel which takes advantage of the simplified biological neural computation. Several examples are shown to demonstrate the capability of our parallel algorithm.

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2. Neural representation and motion equations

A two-dimensional triangular network representation is introduced. The system requires $p(p-1)/2$ processing elements or hysteresis McCulloch-Pitts neurons where p is the number of squares on an $m \times n$ chessboard. Each state of the neurons represents a path. In order to establish a knight's path on a square on the chessboard, two neurons must be fired because one is for coming into the square and the other is for going out of the square. In our algorithm the following motion equation for the ij th neuron is used to solve the problem:

if $d_{ij} = 1$ then

$$\frac{dU_{ij}}{dt} = - \left(\sum_{k=1}^p V_{ik} d_{ik} - 2 \right) - \left(\sum_{k=1}^p V_{kj} d_{kj} - 2 \right)$$

if $d_{ij} = 0$ then

$$\frac{dU_{ij}}{dt} = 0 ,$$

where the upper triangular elements in the two-dimensional array are only used: V_{ij} for $i < j$ and V_{ij} for $i > j$ is given by V_{ji} . The state of V_{ij} actually represents a knight's tour between the i th and the j th square. In other words, the upper triangular neural array represents the nondirected adjacency matrix in order to find a Hamiltonian circuit. In Eq. (1) d_{ij} is 1 if the move from the i th square to the j th square is legal or valid, and 0 otherwise. The first term and the second term are to determine a move between the i th and the j th square if it is valid. The first term forces the ij th neuron to have two valid moves from the i th square and the second term from the j th square respectively. The state of V_{ij} is updated by the hysteresis McCulloch-Pitts function: $V_{ij} = f(U_{ij}) = 1$ if $U_{ij} > 3$, 0 if $U_{ij} < 0$, and no changes otherwise.

Figure 1 shows how the knight moves in an L-shape route. In a 3×4 chessboard knight's tour problem, 12 squares are numbered from left to right and from top to bottom as shown in Fig. 2a. From #2 square there are three valid moves: V_{28} , V_{29} and $V_{2,11}$ where only two moves are needed to find the Hamiltonian circuit.

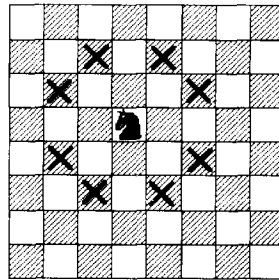


Fig. 1. The knight moves in an L-shape route.

Figure 2(b) shows the $p(p-1)/2 = 66$ neurons for this problem where the black squares indicate their outputs are ones. Figure 2(b) depicts the two closed loops as shown in Fig. 2(c) which is not the Hamiltonian circuit. Unfortunately there is no solution for the 3×4 knight's tour problem. In 1943 Schuh [8] stated that the number of squares has to be even which is necessary but not sufficient.

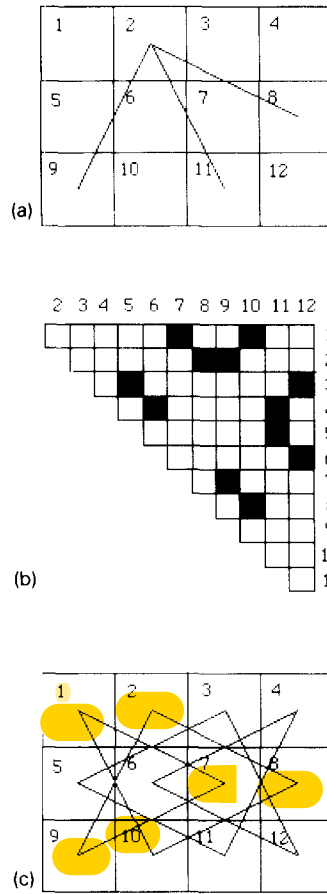


Fig. 2. (a) A 3×4 chessboard; (b) Neural representation; (c) Unsatisfactory solution.

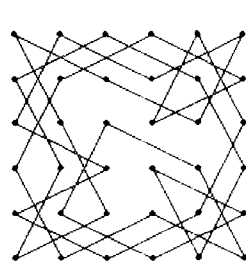


Fig. 3. 6×6 chessboard.

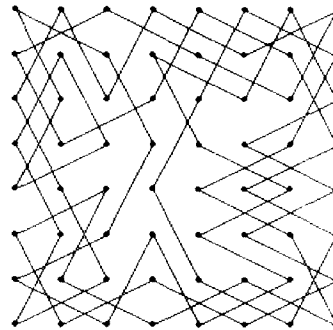
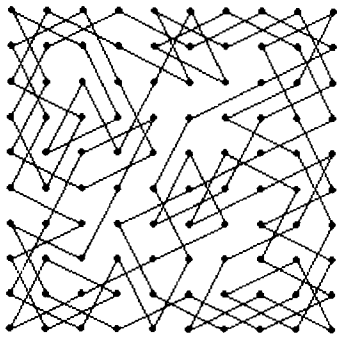
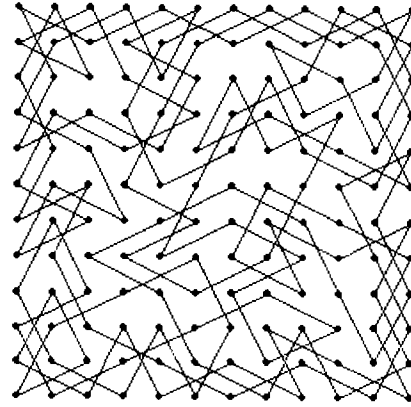


Fig. 4. 8×8 chessboard.

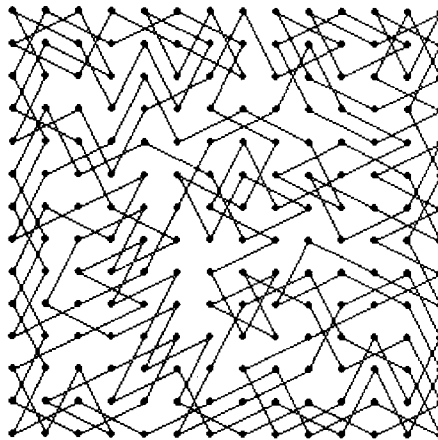
3. Results

We have developed the simulator on a Macintosh SE/30 based on Eq. (1) to verify our algorithm. *Figures 3 through 10* show solutions for 6×6 , 8×8 , 10×10 , 12×12 ,

Fig. 5. 10×10 chessboard.Fig. 6. 12×12 chessboard.

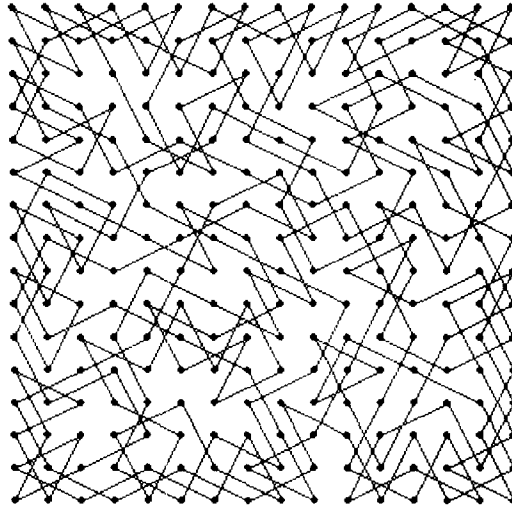
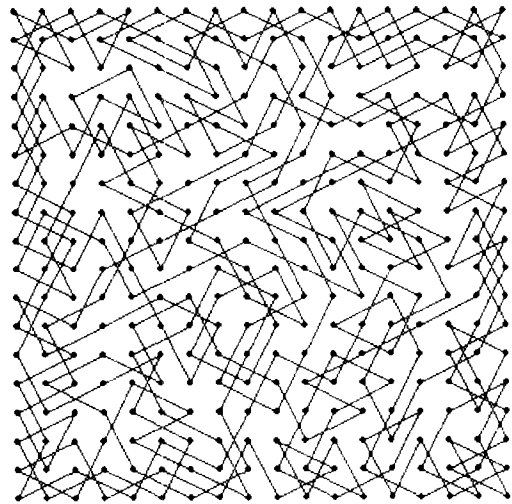
14×14 , 16×16 , 18×18 and 20×20 chessboard knight's tour problems respectively.

The average number of iteration steps takes less than 100 steps. We have observed that the larger the problem, the more often the state of the system converged to the unsatisfactory solution with several closed loops. Without the hysteresis property, the state of the system tends to oscillate and hardly converges to the solution.

Fig. 7. 14×14 chessboard.

References

- [1] R.W.W. Ball and H.S.M. Coxeter, *Mathematical Recreations & Essays*, 1892 (1st ed.) and 1974 (12th ed.) (University of Toronto Press).
- [2] Euler, L., *Memoire de Berlin for 1759* (Berlin, 1766) 310–337.
- [3] De Lavernede, *Memoires de l'Academie Royale du Gard* (Nimes, 1839) 151–179.
- [4] K.C. Lee and Y. Takefuji, Finding a knight's tour on an $N \times M$ chessboard with $O(NM)$ hysteresis McCulloch-Pitts neurons, submitted.
- [5] Legendre, *Theorie des Nombres*, 2nd ed. (Paris, 1830) 165.

Fig. 8. 16×16 chessboard.Fig. 9. 18×18 chessboard.

- [6] W.S. McCulloch and W.H. Pitts, *Bull. Mathemat. Biophys.* 5 (1943) 115.
- [7] Pratt, *Studies of Chess*, sixth ed. (London, 1825).
- [8] F. Schuh, *The Master Book of Mathematical Recreations* (Dover, New York, 1968).
- [9] Vandermonde, *L'Histoire de l'Academie des Sciences for 1771* (Paris, 1774) 566–574.
- [10] H.C. Warnsdorff, *Des Rosselsprunget einfachste und allgemeinste Losung* (Schmalkalden, 1823).

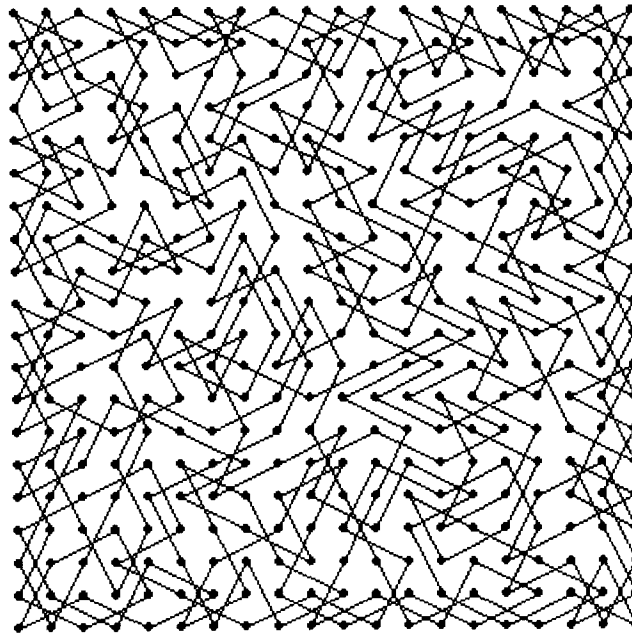


Fig. 10. 20 × 20 chessboard.



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