

NO.	Instruction	Addr.	Label	ASM	Comment
0	00000013	0	__start:	addi x0, x0, 0	nop
1	00402103	4		lw x2, 4(x0)	x2 = 0000 0008
2	00802203	8		lw x4, 8(x0)	x4 = 0000 0010
3	00c02283	C		lw x5, 12(x0)	x5 = 0000 0014
4	01002303	10		lw x6, 16(x0)	x6 = ffff 0000
5	01402383	14		lw x7, 20(x0)	x7 = 0fff 0000
6	306850f3	18		csrrwi x1, 0x306, 16	x1=0;[6] = 0000 0010(d16)
7	306020f3	1C		csrr x1, 0x306	x1 = [6] (16)
8	306310f3	20		csrrw x1, 0x306, x6	x1 = d16; [6]=ffff 0000
9	306020f3	24		csrr x1, 0x306	x1 = ffff 0000
10	00000013	28		addi x0, x0, 0	
11	07800093	2C		addi x1, x0, 120	x1 = d120
12	30509073	30		csrw 0x305, x1	[5] = x1 = 120(mtvec)
13	00000013	34		addi x0, x0, 0	
14	00000073	38		ecall	
15	00000013	3C		addi x0, x0, 0	
16	00000012	40		addi x0, x0, 0	# illegal inst
17	00000013	44		addi x0, x0, 0	
18	07f02083	48		lw x1, 127(x0)	# don't care
19	08002083	4C		lw x1, 128(x0)	# L access fault
20	00000013	50		addi x0, x0, 0	
21	08102023	54		sw x1, 128(x0)	# S access fault
22	00000013	58		addi x0, x0, 0	
23	00000013	5C		addi x0, x0, 0	
24	00000013	60		addi x0, x0, 0	
25	00000013	64		addi x0, x0, 0	
26	00000013	68		addi x0, x0, 0	
27	00000013	6C		addi x0, x0, 0	
28	00000013	70		addi x0, x0, 0	

NO.	Instruction	Addr.	Label	ASM	Comment
29	00000067	74		jr x0	
30	34102cf3	78	trap:	csrr x25, 0x341	# mepc
31	34202df3	7C		csrr x27, 0x342	# mcause
32	30002e73	80		csrr x28, 0x300	# mstatus
33	34302ef3	84		csrr x29, 0x343	# mtval
34	34402f73	88		csrr x30, 0x344	# mip
35	004c8113	8C		addi x2, x25, 4	
36	34111073	90		csrw 0x341, x2	
37	30200073	94		mret	# 30200073 mret
38	00000013	98		addi x0, x0, 0	
39	00000013	9C		addi x0, x0, 0	
40	00000013	A0		addi x0, x0, 0	
41	00000013	A4		addi x0, x0, 0	