



## 1. Description

### 1.1. Project

|                 |                    |
|-----------------|--------------------|
| Project Name    | new_turnstiles     |
| Board Name      | STM32F3DISCOVERY   |
| Generated with: | STM32CubeMX 6.15.0 |
| Date            | 07/14/2025         |

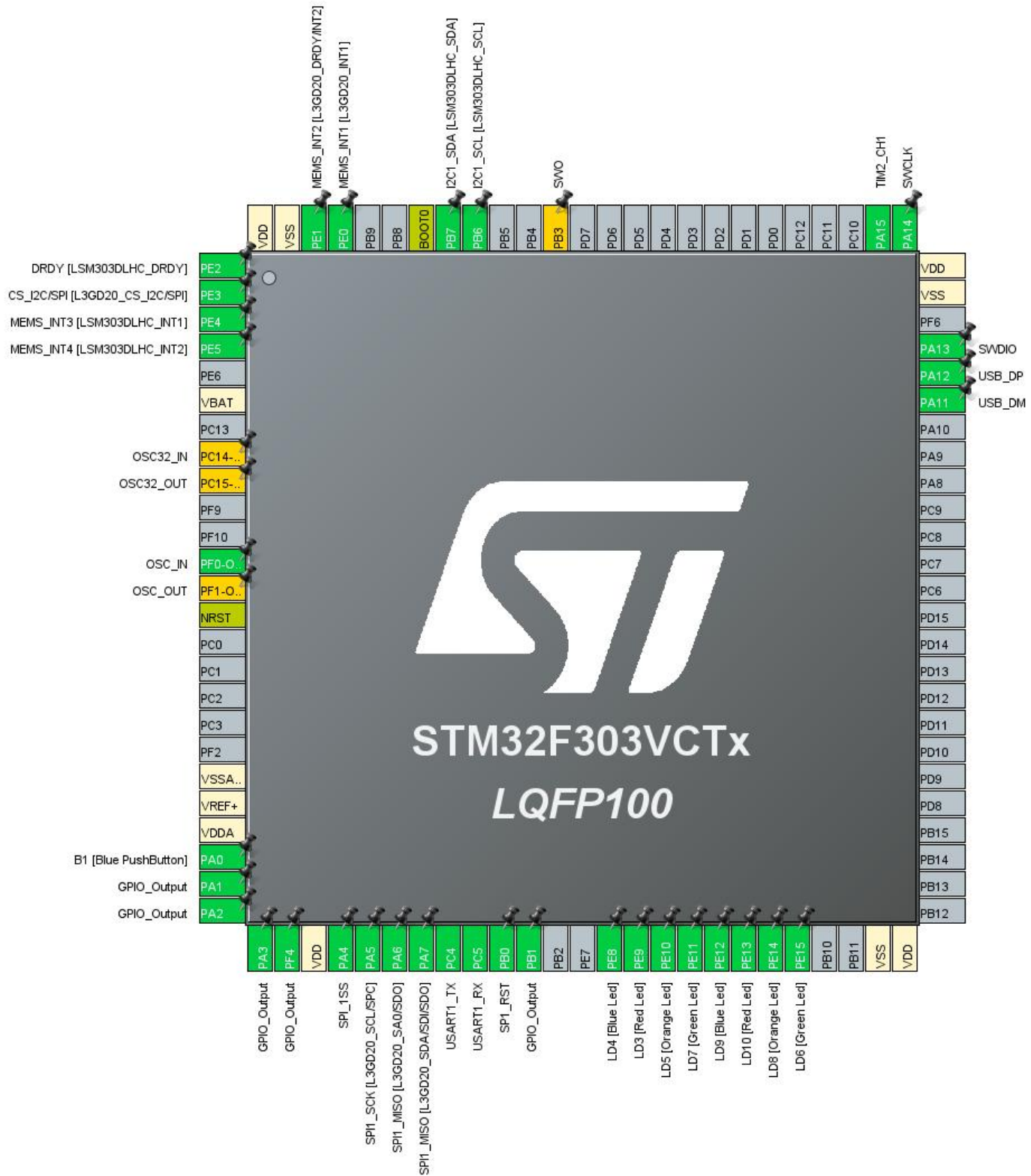
### 1.2. MCU

|                |               |
|----------------|---------------|
| MCU Series     | STM32F3       |
| MCU Line       | STM32F303     |
| MCU name       | STM32F303VCTx |
| MCU Package    | LQFP100       |
| MCU Pin number | 100           |

### 1.3. Core(s) information

|         |               |
|---------|---------------|
| Core(s) | Arm Cortex-M4 |
|---------|---------------|

## 2. Pinout Configuration



### 3. Pins Configuration

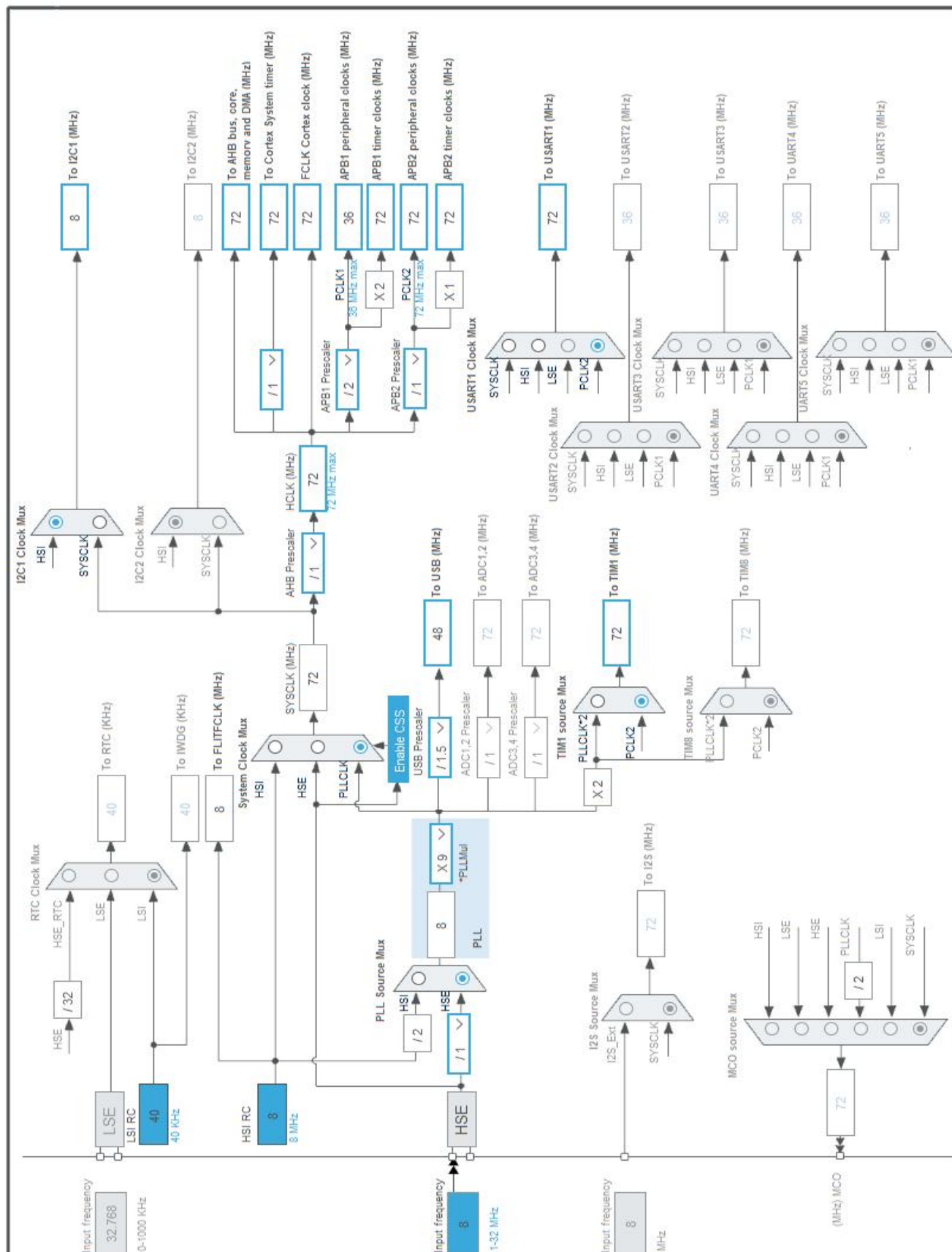
| Pin Number<br>LQFP100 | Pin Name<br>(function after<br>reset) | Pin Type | Alternate<br>Function(s) | Label                             |
|-----------------------|---------------------------------------|----------|--------------------------|-----------------------------------|
| 1                     | PE2                                   | I/O      | GPIO_EXTI2               | DRDY<br>[LSM303DLHC_DRDY]         |
| 2                     | PE3 *                                 | I/O      | GPIO_Output              | CS_I2C/SPI<br>[L3GD20_CS_I2C/SPI] |
| 3                     | PE4                                   | I/O      | GPIO_EXTI4               | MEMS_INT3<br>[LSM303DLHC_INT1]    |
| 4                     | PE5                                   | I/O      | GPIO_EXTI5               | MEMS_INT4<br>[LSM303DLHC_INT2]    |
| 6                     | VBAT                                  | Power    |                          |                                   |
| 8                     | PC14-OSC32_IN **                      | I/O      | RCC_OSC32_IN             | OSC32_IN                          |
| 9                     | PC15-OSC32_OUT **                     | I/O      | RCC_OSC32_OUT            | OSC32_OUT                         |
| 12                    | PF0-OSC_IN                            | I/O      | RCC_OSC_IN               | OSC_IN                            |
| 13                    | PF1-OSC_OUT **                        | I/O      | RCC_OSC_OUT              | OSC_OUT                           |
| 14                    | NRST                                  | Reset    |                          |                                   |
| 20                    | VSSA/VREF-                            | Power    |                          |                                   |
| 21                    | VREF+                                 | Power    |                          |                                   |
| 22                    | VDDA                                  | Power    |                          |                                   |
| 23                    | PA0 *                                 | I/O      | GPIO_Input               | B1 [Blue PushButton]              |
| 24                    | PA1 *                                 | I/O      | GPIO_Output              |                                   |
| 25                    | PA2 *                                 | I/O      | GPIO_Output              |                                   |
| 26                    | PA3 *                                 | I/O      | GPIO_Output              |                                   |
| 27                    | PF4 *                                 | I/O      | GPIO_Output              |                                   |
| 28                    | VDD                                   | Power    |                          |                                   |
| 29                    | PA4 *                                 | I/O      | GPIO_Output              | SPI_1SS                           |
| 30                    | PA5                                   | I/O      | SPI1_SCK                 | SPI1_SCK<br>[L3GD20_SCL/SPC]      |
| 31                    | PA6                                   | I/O      | SPI1_MISO                | SPI1_MISO<br>[L3GD20_SA0/SDO]     |
| 32                    | PA7                                   | I/O      | SPI1_MOSI                | SPI1_MISO<br>[L3GD20_SDA/SDI/SDO] |
| 33                    | PC4                                   | I/O      | USART1_TX                |                                   |
| 34                    | PC5                                   | I/O      | USART1_RX                |                                   |
| 35                    | PB0 *                                 | I/O      | GPIO_Output              | SP1_RST                           |
| 36                    | PB1 *                                 | I/O      | GPIO_Output              |                                   |
| 39                    | PE8 *                                 | I/O      | GPIO_Output              | LD4 [Blue Led]                    |
| 40                    | PE9 *                                 | I/O      | GPIO_Output              | LD3 [Red Led]                     |
| 41                    | PE10 *                                | I/O      | GPIO_Output              | LD5 [Orange Led]                  |

| Pin Number<br>LQFP100 | Pin Name<br>(function after<br>reset) | Pin Type | Alternate<br>Function(s) | Label                           |
|-----------------------|---------------------------------------|----------|--------------------------|---------------------------------|
| 42                    | PE11 *                                | I/O      | GPIO_Output              | LD7 [Green Led]                 |
| 43                    | PE12 *                                | I/O      | GPIO_Output              | LD9 [Blue Led]                  |
| 44                    | PE13 *                                | I/O      | GPIO_Output              | LD10 [Red Led]                  |
| 45                    | PE14 *                                | I/O      | GPIO_Output              | LD8 [Orange Led]                |
| 46                    | PE15 *                                | I/O      | GPIO_Output              | LD6 [Green Led]                 |
| 49                    | VSS                                   | Power    |                          |                                 |
| 50                    | VDD                                   | Power    |                          |                                 |
| 70                    | PA11                                  | I/O      | USB_DM                   |                                 |
| 71                    | PA12                                  | I/O      | USB_DP                   |                                 |
| 72                    | PA13                                  | I/O      | SYS_JTMS-SWDIO           | SWDIO                           |
| 74                    | VSS                                   | Power    |                          |                                 |
| 75                    | VDD                                   | Power    |                          |                                 |
| 76                    | PA14                                  | I/O      | SYS_JTCK-SWCLK           | SWCLK                           |
| 77                    | PA15                                  | I/O      | TIM2_CH1                 |                                 |
| 89                    | PB3 **                                | I/O      | SYS_JTDO-TRACESWO        | SWO                             |
| 92                    | PB6                                   | I/O      | I2C1_SCL                 | I2C1_SCL<br>[LSM303DLHC_SCL]    |
| 93                    | PB7                                   | I/O      | I2C1_SDA                 | I2C1_SDA<br>[LSM303DLHC_SDA]    |
| 94                    | BOOT0                                 | Boot     |                          |                                 |
| 97                    | PE0                                   | I/O      | GPIO_EXTI0               | MEMS_INT1<br>[L3GD20_INT1]      |
| 98                    | PE1                                   | I/O      | GPIO_EXTI1               | MEMS_INT2<br>[L3GD20_DRDY/INT2] |
| 99                    | VSS                                   | Power    |                          |                                 |
| 100                   | VDD                                   | Power    |                          |                                 |

\* The pin is affected with an I/O function

\*\* The pin is affected with a peripheral function but no peripheral mode is activated

## 4. Clock Tree Configuration



## 1. Power Consumption Calculator report

### 1.1. Microcontroller Selection

|           |               |
|-----------|---------------|
| Series    | STM32F3       |
| Line      | STM32F303     |
| MCU       | STM32F303VCTx |
| Datasheet | DS9118_Rev13  |

### 1.2. Parameter Selection

|             |     |
|-------------|-----|
| Temperature | 25  |
| Vdd         | 3.6 |

### 1.3. Battery Selection

|                   |                 |
|-------------------|-----------------|
| Battery           | Li-SOCL2(A3400) |
| Capacity          | 3400.0 mAh      |
| Self Discharge    | 0.08 %/month    |
| Nominal Voltage   | 3.6 V           |
| Max Cont Current  | 100.0 mA        |
| Max Pulse Current | 200.0 mA        |
| Cells in series   | 1               |
| Cells in parallel | 1               |

#### 1.4. Sequence

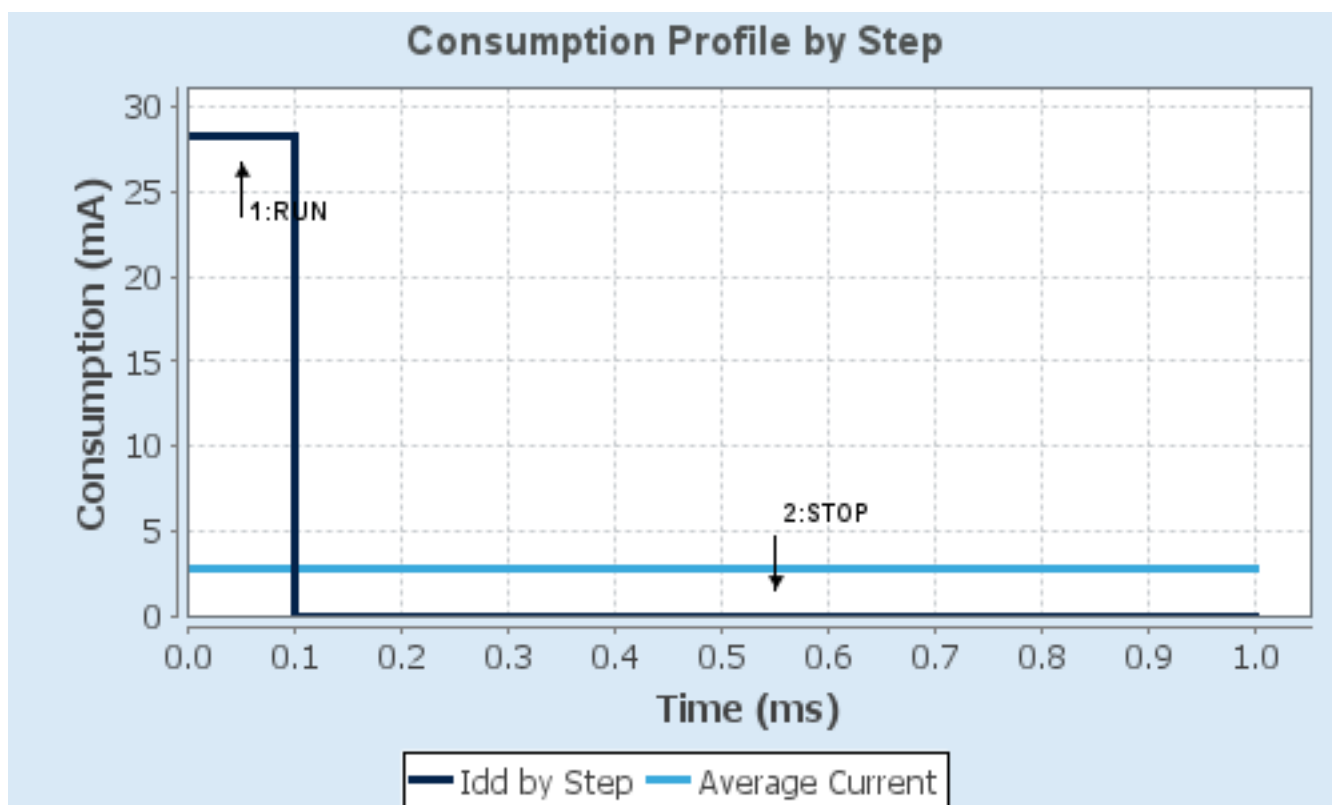
|                               |             |               |
|-------------------------------|-------------|---------------|
| <b>Step</b>                   | Step1       | Step2         |
| <b>Mode</b>                   | RUN         | STOP          |
| <b>Vdd</b>                    | 3.6         | 3.6           |
| <b>Voltage Source</b>         | Battery     | Battery       |
| <b>Range</b>                  | No Scale    | No Scale      |
| <b>Fetch Type</b>             | FLASH       | n/a           |
| <b>CPU Frequency</b>          | 72 MHz      | 0 Hz          |
| <b>Clock Configuration</b>    | HSE PLL     | Regulator LP  |
| <b>Clock Source Frequency</b> | 8 MHz       | 0 Hz          |
| <b>Peripherals</b>            |             |               |
| <b>Additional Cons.</b>       | 0 mA        | 0 mA          |
| <b>Average Current</b>        | 28.24 mA    | 10.85 $\mu$ A |
| <b>Duration</b>               | 0.1 ms      | 0.9 ms        |
| <b>DMIPS</b>                  | 63.0        | 0.0           |
| <b>Ta Max</b>                 | 100.83      | 105           |
| <b>Category</b>               | In DS Table | In DS Table   |

#### 1.5. Results

|               |                               |                 |            |
|---------------|-------------------------------|-----------------|------------|
| Sequence Time | 1 ms                          | Average Current | 2.83 mA    |
| Battery Life  | 1 month, 19 days,<br>12 hours | Average DMIPS   | 63.0 DMIPS |

#### 1.6. Chart





## 2. Software Project

### 2.1. Project Settings

| Name                              | Value  |
|-----------------------------------|--|
| Project Name                      | new_turnstiles   |
| Project Folder                    | C:\Users\ladyc\Documents\GitHub\APC_SmartTurnstiles\new_turnstiles |
| Toolchain / IDE                   | STM32CubeIDE   |
| Firmware Package Name and Version | STM32Cube FW_F3 V1.11.5  |
| Application Structure             | Advanced   |
| Generate Under Root               | Yes  |
| Do not generate the main()        | No   |
| Minimum Heap Size                 | 0x200  |
| Minimum Stack Size                | 0x400  |

### 2.2. Code Generation Settings

| Name  | Value                                 |
|---|---------------------------------------|
| STM32Cube MCU packages and embedded software                    | Copy only the necessary library files |
| Generate peripheral initialization as a pair of '.c/.h' files   | No                                    |
| Backup previously generated files when re-generating            | No                                    |
| Keep User Code when re-generating                               | Yes                                   |
| Delete previously generated files when not re-generated         | Yes                                   |
| Set all free pins as analog (to optimize the power consumption) | No                                    |
| Enable Full Assert  | No                                    |

### 2.3. Advanced Settings - Generated Function Calls

| Rank | Function Name       | Peripheral Instance Name |
|------|---------------------|--------------------------|
| 1    | SystemClock_Config  | RCC                      |
| 2    | MX_GPIO_Init        | GPIO                     |
| 3    | MX_I2C1_Init        | I2C1                     |
| 4    | MX_SPI1_Init        | SPI1                     |
| 5    | MX_USB_PCD_Init     | USB                      |
| 6    | MX_TIM1_Init        | TIM1                     |
| 7    | MX_TIM2_Init        | TIM2                     |
| 8    | MX_USART1_UART_Init | USART1                   |

## 3. Peripherals and Middlewares Configuration

### 3.1. I2C1

#### I2C: I2C

##### 3.1.1. Parameter Settings:

###### Timing configuration:

|                               |                     |
|-------------------------------|---------------------|
| I2C Speed Mode                | Standard Mode       |
| I2C Speed Frequency (KHz)     | 100                 |
| Rise Time (ns)                | 100                 |
| Fall Time (ns)                | 100                 |
| Coefficient of Digital Filter | 0                   |
| Analog Filter                 | Enabled             |
| Timing                        | <b>0x00201D2B *</b> |

###### Slave Features:

|                                  |          |
|----------------------------------|----------|
| Clock No Stretch Mode            | Disabled |
| General Call Address Detection   | Disabled |
| Primary Address Length selection | 7-bit    |
| Dual Address Acknowledged        | Disabled |
| Primary slave address            | 0        |

### 3.2. RCC

#### High Speed Clock (HSE): BYPASS Clock Source

##### 3.2.1. Parameter Settings:

###### System Parameters:

|                   |                    |
|-------------------|--------------------|
| VDD voltage (V)   | 3.3                |
| Prefetch Buffer   | Enabled            |
| Flash Latency(WS) | 2 WS (3 CPU cycle) |

###### RCC Parameters:

|                                |      |
|--------------------------------|------|
| HSI Calibration Value          | 16   |
| HSE Startup Timeout Value (ms) | 100  |
| LSE Startup Timeout Value (ms) | 5000 |

### 3.3. SPI1

#### Mode: Full-Duplex Master

### 3.3.1. Parameter Settings:

#### **Basic Parameters:**

|              |                 |
|--------------|-----------------|
| Frame Format | Motorola        |
| Data Size    | <b>8 Bits *</b> |
| First Bit    | MSB First       |

#### **Clock Parameters:**

|                           |                       |
|---------------------------|-----------------------|
| Prescaler (for Baud Rate) | <b>32 *</b>           |
| Baud Rate                 | <b>2.25 MBits/s *</b> |
| Clock Polarity (CPOL)     | Low                   |
| Clock Phase (CPHA)        | 1 Edge                |

#### **Advanced Parameters:**

|                 |          |
|-----------------|----------|
| CRC Calculation | Disabled |
| NSSP Mode       | Enabled  |
| NSS Signal Type | Software |

### **3.4. SYS**

**Debug: Serial Wire**

**Timebase Source: SysTick**

### **3.5. TIM1**

**Clock Source : Internal Clock**

### 3.5.1. Parameter Settings:

#### **Counter Settings:**

|   |               |
|---|---------------|
| Prescaler (PSC - 16 bits value)                       | <b>72-1 *</b> |
| Counter Mode  | Up            |
| Counter Period (AutoReload Register - 16 bits value ) | 65535         |
| Internal Clock Division (CKD)                         | No Division   |
| Repetition Counter (RCR - 16 bits value)              | 0             |
| auto-reload preload                                   | Disable       |

#### **Trigger Output (TRGO) Parameters:**

|                               |  |
|-------------------------------|--|
| Master/Slave Mode (MSM bit)   | Disable (Trigger input effect not delayed) |
| Trigger Event Selection TRGO  | Reset (UG bit from TIMx_EGR)               |
| Trigger Event Selection TRGO2 | Reset (UG bit from TIMx_EGR)               |

### 3.6. TIM2

#### Channel1: Input Capture direct mode

##### 3.6.1. Parameter Settings:

###### Counter Settings:

|   |             |
|---|-------------|
| Prescaler (PSC - 16 bits value)                       | 72-1 *      |
| Counter Mode  | Up          |
| Counter Period (AutoReload Register - 32 bits value ) | 4294967295  |
| Internal Clock Division (CKD)                         | No Division |
| auto-reload preload                                   | Disable     |

###### Trigger Output (TRGO) Parameters:

|                              |  |
|------------------------------|--|
| Master/Slave Mode (MSM bit)  | Disable (Trigger input effect not delayed) |
| Trigger Event Selection TRGO | Reset (UG bit from TIMx_EGR)               |

###### Input Capture Channel 1:

|                             |             |
|-----------------------------|-------------|
| Polarity Selection          | Rising Edge |
| IC Selection                | Direct      |
| Prescaler Division Ratio    | No division |
| Input Filter (4 bits value) | 0           |

### 3.7. USART1

#### Mode: Asynchronous

##### 3.7.1. Parameter Settings:

###### Basic Parameters:

|             |                           |
|-------------|---------------------------|
| Baud Rate   | 38400                     |
| Word Length | 8 Bits (including Parity) |
| Parity      | None                      |
| Stop Bits   | 1                         |

###### Advanced Parameters:

|                |                      |
|----------------|----------------------|
| Data Direction | Receive and Transmit |
| Over Sampling  | 16 Samples           |
| Single Sample  | Disable              |

###### Advanced Features:

|                               |         |
|-------------------------------|---------|
| Auto Baudrate                 | Disable |
| TX Pin Active Level Inversion | Disable |
| RX Pin Active Level Inversion | Disable |
| Data Inversion                | Disable |

|                         |         |
|-------------------------|---------|
| TX and RX Pins Swapping | Disable |
| Overrun                 | Enable  |
| DMA on RX Error         | Enable  |
| MSB First               | Disable |

### 3.8. USB

#### mode: Device (FS)

##### 3.8.1. Parameter Settings:

###### Basic Parameters:

|                    |                     |
|--------------------|---------------------|
| Speed              | Full Speed 12MBit/s |
| Physical interface | Internal Phy        |

###### Power Parameters:

|                  |          |
|------------------|----------|
| Low Power        | Disabled |
| Battery Charging | Disabled |

\* User modified value

## 4. System Configuration

### 4.1. GPIO configuration

| IP                    | Pin            | Signal            | GPIO mode   | GPIO pull/up pull down      | Max Speed     | User Label                        |
|-----------------------|----------------|-------------------|---|-----------------------------|---------------|-----------------------------------|
| I2C1                  | PB6            | I2C1_SCL          | Alternate Function Open Drain                                   | <b>Pull-up *</b>            | <b>High *</b> | I2C1_SCL<br>[LSM303DLHC_SCL]      |
|                       | PB7            | I2C1_SDA          | Alternate Function Open Drain                                   | <b>Pull-up *</b>            | <b>High *</b> | I2C1_SDA<br>[LSM303DLHC_SDA]      |
| RCC                   | PF0-OSC_IN     | RCC_OSC_IN        | n/a   | n/a                         | n/a           | OSC_IN                            |
| SPI1                  | PA5            | SPI1_SCK          | Alternate Function Push Pull                                    | No pull-up and no pull-down | <b>High *</b> | SPI1_SCK<br>[L3GD20_SCL/SPC]      |
|                       | PA6            | SPI1_MISO         | Alternate Function Push Pull                                    | No pull-up and no pull-down | <b>High *</b> | SPI1_MISO<br>[L3GD20_SA0/SDO]     |
|                       | PA7            | SPI1_MOSI         | Alternate Function Push Pull                                    | No pull-up and no pull-down | <b>High *</b> | SPI1_MISO<br>[L3GD20_SDA/SDI/SDO] |
| SYS                   | PA13           | SYS_JTMS-SWDIO    | n/a   | n/a                         | n/a           | SWDIO                             |
|                       | PA14           | SYS_JTCK-SWCLK    | n/a   | n/a                         | n/a           | SWCLK                             |
| TIM2                  | PA15           | TIM2_CH1          | Alternate Function Push Pull                                    | <b>Pull up *</b>            | Low           |                                   |
| USART1                | PC4            | USART1_TX         | Alternate Function Push Pull                                    | No pull-up and no pull-down | <b>High *</b> |                                   |
|                       | PC5            | USART1_RX         | Alternate Function Push Pull                                    | No pull-up and no pull-down | <b>High *</b> |                                   |
| USB                   | PA11           | USB_DM            | Alternate Function Push Pull                                    | No pull-up and no pull-down | <b>High *</b> |                                   |
|                       | PA12           | USB_DP            | Alternate Function Push Pull                                    | No pull-up and no pull-down | <b>High *</b> |                                   |
| Single Mapped Signals | PC14-OSC32_IN  | RCC_OSC32_IN      | n/a   | n/a                         | n/a           | OSC32_IN                          |
|                       | PC15-OSC32_OUT | RCC_OSC32_OUT     | n/a   | n/a                         | n/a           | OSC32_OUT                         |
|                       | PF1-OSC_OUT    | RCC_OSC_OUT       | n/a   | n/a                         | n/a           | OSC_OUT                           |
|                       | PB3            | SYS_JTDO-TRACESWO | n/a   | n/a                         | n/a           | SWO                               |
| GPIO                  | PE2            | GPIO_EXTI2        | <b>External Event Mode with Rising edge trigger detection *</b> | No pull-up and no pull-down | n/a           | DRDY<br>[LSM303DLHC_DRDY]         |
|                       | PE3            | GPIO_Output       | Output Push Pull  | No pull-up and no pull-down | Low           | CS_I2C/SPI<br>[L3GD20_CS_I2C/SPI] |
|                       | PE4            | GPIO_EXTI4        | <b>External Event Mode with Rising edge trigger detection *</b> | No pull-up and no pull-down | n/a           | MEMS_INT3<br>[LSM303DLHC_INT1]    |
|                       |                |                   |   |                             |               |                                   |

| IP | Pin  | Signal      | GPIO mode   | GPIO pull/up pull down      | Max Speed | User Label                   |
|----|------|-------------|---|-----------------------------|-----------|------------------------------|
|    | PE5  | GPIO_EXTI5  | <b>External Event Mode with Rising edge trigger detection *</b> | No pull-up and no pull-down | n/a       | MEMS_INT4 [LSM303DLHC_INT2]  |
|    | PA0  | GPIO_Input  | Input mode  | No pull-up and no pull-down | n/a       | B1 [Blue PushButton]         |
|    | PA1  | GPIO_Output | Output Push Pull  | No pull-up and no pull-down | Low       |                              |
|    | PA2  | GPIO_Output | Output Push Pull  | No pull-up and no pull-down | Low       |                              |
|    | PA3  | GPIO_Output | Output Push Pull  | No pull-up and no pull-down | Low       |                              |
|    | PF4  | GPIO_Output | Output Push Pull  | No pull-up and no pull-down | Low       |                              |
|    | PA4  | GPIO_Output | Output Push Pull  | No pull-up and no pull-down | Low       | SPI_1SS                      |
|    | PB0  | GPIO_Output | Output Push Pull  | No pull-up and no pull-down | Low       | SP1_RST                      |
|    | PB1  | GPIO_Output | Output Push Pull  | No pull-up and no pull-down | Low       |                              |
|    | PE8  | GPIO_Output | Output Push Pull  | No pull-up and no pull-down | Low       | LD4 [Blue Led]               |
|    | PE9  | GPIO_Output | Output Push Pull  | No pull-up and no pull-down | Low       | LD3 [Red Led]                |
|    | PE10 | GPIO_Output | Output Push Pull  | No pull-up and no pull-down | Low       | LD5 [Orange Led]             |
|    | PE11 | GPIO_Output | Output Push Pull  | No pull-up and no pull-down | Low       | LD7 [Green Led]              |
|    | PE12 | GPIO_Output | Output Push Pull  | No pull-up and no pull-down | Low       | LD9 [Blue Led]               |
|    | PE13 | GPIO_Output | Output Push Pull  | No pull-up and no pull-down | Low       | LD10 [Red Led]               |
|    | PE14 | GPIO_Output | Output Push Pull  | No pull-up and no pull-down | Low       | LD8 [Orange Led]             |
|    | PE15 | GPIO_Output | Output Push Pull  | No pull-up and no pull-down | Low       | LD6 [Green Led]              |
|    | PE0  | GPIO_EXTI0  | <b>External Event Mode with Rising edge trigger detection *</b> | No pull-up and no pull-down | n/a       | MEMS_INT1 [L3GD20_INT1]      |
|    | PE1  | GPIO_EXTI1  | <b>External Event Mode with Rising edge trigger detection *</b> | No pull-up and no pull-down | n/a       | MEMS_INT2 [L3GD20_DRDY/INT2] |

## 4.2. DMA configuration

nothing configured in DMA service



### 4.3. NVIC configuration

#### 4.3.1. NVIC

| Interrupt Table   | Enable | Preenmption Priority | SubPriority |
|---|--------|----------------------|-------------|
| Non maskable interrupt  | true   | 0                    | 0           |
| Hard fault interrupt  | true   | 0                    | 0           |
| Memory management fault   | true   | 0                    | 0           |
| Pre-fetch fault, memory access fault                                      | true   | 0                    | 0           |
| Undefined instruction or illegal state                                    | true   | 0                    | 0           |
| System service call via SWI instruction                                   | true   | 0                    | 0           |
| Debug monitor   | true   | 0                    | 0           |
| Pendable request for system service                                       | true   | 0                    | 0           |
| System tick timer   | true   | 0                    | 0           |
| TIM2 global interrupt   | true   | 0                    | 0           |
| PVD interrupt through EXTI line16   | unused |                      |             |
| Flash global interrupt  | unused |                      |             |
| RCC global interrupt  | unused |                      |             |
| USB high priority or CAN_TX interrupts                                    | unused |                      |             |
| USB low priority or CAN_RX0 interrupts                                    | unused |                      |             |
| TIM1 break and TIM15 interrupts   | unused |                      |             |
| TIM1 update and TIM16 interrupts  | unused |                      |             |
| TIM1 trigger, commutation and TIM17 interrupts                            | unused |                      |             |
| TIM1 capture compare interrupt  | unused |                      |             |
| I2C1 event global interrupt / I2C1 wake-up interrupt through EXTI line 23 | unused |                      |             |
| I2C1 error interrupt  | unused |                      |             |
| SPI1 global interrupt   | unused |                      |             |
| USART1 global interrupt / USART1 wake-up interrupt through EXTI line 25   | unused |                      |             |
| USB high priority interrupt remap   | unused |                      |             |
| USB low priority interrupt remap  | unused |                      |             |
| Floating point unit interrupt   | unused |                      |             |

#### 4.3.2. NVIC Code generation

| Enabled interrupt Table                | Select for init sequence ordering | Generate IRQ handler | Call HAL handler |
|--|-----------------------------------|----------------------|------------------|
| Non maskable interrupt                 | false                             | true                 | false            |
| Hard fault interrupt                   | false                             | true                 | false            |
| Memory management fault                | false                             | true                 | false            |
| Pre-fetch fault, memory access fault   | false                             | true                 | false            |
| Undefined instruction or illegal state | false                             | true                 | false            |
|  |                                   |                      |                  |

| Enabled interrupt Table                 | Select for init<br>sequence ordering | Generate IRQ<br>handler | Call HAL handler |
|---|--------------------------------------|-------------------------|------------------|
| System service call via SWI instruction | false                                | true                    | false            |
| Debug monitor                           | false                                | true                    | false            |
| Pendable request for system service     | false                                | true                    | false            |
| System tick timer                       | false                                | true                    | true             |
| TIM2 global interrupt                   | false                                | true                    | true             |

\* User modified value

## 5. System Views

### 5.1. Category view

#### 5.1.1. Current

#### Middleware

#### System Core

#### Analog

#### Timers

#### Connectivity

#### Multimedia

#### Computing

DMA

GPIO 

IIVIC 

RCC 

SYS 

TIM1 

TIM2 

I2C1 

SPI1 

USART1 

USB 

## 6. Docs & Resources

| Type                    | Link  |
|-------------------------|---|
| BSDL files              | <a href="https://www.st.com/resource/en/bsdl_model/stm32f3_bsdل.zip">https://www.st.com/resource/en/bsdl_model/stm32f3_bsdل.zip</a>   |
| System View Description | <a href="https://www.st.com/resource/en/svd/stm32f3-svd.zip">https://www.st.com/resource/en/svd/stm32f3-svd.zip</a>   |
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| Presentations           | <a href="https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf">https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf</a>   |
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| Presentations           | <a href="https://www.st.com/resource/en/product_presentation/microcontrollers-stm32-family-overview.pdf">https://www.st.com/resource/en/product_presentation/microcontrollers-stm32-family-overview.pdf</a>   |
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| Flyers                  | <a href="https://www.st.com/resource/en/flyer/flpowerstbd.pdf">https://www.st.com/resource/en/flyer/flpowerstbd.pdf</a>   |
| Flyers                  | <a href="https://www.st.com/resource/en/flyer/fldpstpfcl1120.pdf">https://www.st.com/resource/en/flyer/fldpstpfcl1120.pdf</a>   |
| Product Certifications  | <a href="https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf">https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf</a>   |
| Security Bulletin       | <a href="https://www.st.com/resource/en/technical_note/tn1489-security-bulletin-tn1489stpsirt-physical-attacks-on-stm32-and-stm32cube-firmware-stmicroelectronics.pdf">https://www.st.com/resource/en/technical_note/tn1489-security-bulletin-tn1489stpsirt-physical-attacks-on-stm32-and-stm32cube-firmware-stmicroelectronics.pdf</a> |
| Application Notes       | <a href="https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf</a>   |
| Application Notes       | <a href="https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf</a>   |
| Application Notes       | <a href="https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-">https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-</a>   |

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