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VM: Hart 1 switched to kernel page table
VM: Hart 3 switched to kernel page table
VM: Hart 0 switched to kernel page table
[PASS] UART output test
VM: Hart 2 switched to kernel page table
[TEST] VM test start (0x4 harts)
--- vm_func_test: test 1 ---
Mapped VA 0x0 -> PA 0x87ffff000 (R)
Mapped VA 0xa000 -> PA 0x87ffe000 (R W)
Mapped VA 0x200000 -> PA 0x87ffd000 (R X)
Mapped VA 0x40000000 -> PA 0x87ffc000 (R X)
Mapped VA 0x3fffffff000 -> PA 0x87ffb000 (W)
Page table after mapping:
L2 PT @ 0x82182000
.. L1[0x0] pa=0x82183000
... L0[0x0] pa=0x82185000
... ... page 0x0 VA=0x0 -> PA=0x87ffff000 flags=0x3
... ... page 0xa VA=0xa000 -> PA=0x87ffe000 flags=0x7
... L0[0x1] pa=0x82187000
... ... page 0x0 VA=0x200000 -> PA=0x87ffd000 flags=0xb
.. L1[0x1] pa=0x82189000
... L0[0x0] pa=0x82184000
... ... page 0x0 VA=0x40000000 -> PA=0x87ffc000 flags=0xb
.. L1[0xff] pa=0x8218b000
... L0[0x1ff] pa=0x82186000
... ... page 0x1ff VA=0x3fffffff000 -> PA=0x87ffb000 flags=0x5
--- vm_func_test: test 2 ---
Mapped VA 0x0 -> PA 0x87ffff000 (W)
Unmapped VA 0xa000
Unmapped VA 0x40000000
L2 PT @ 0x82182000
.. L1[0x0] pa=0x82183000
... L0[0x0] pa=0x82185000
... ... page 0x0 VA=0x0 -> PA=0x87ffff000 flags=0x5
... L0[0x1] pa=0x82187000
.. L1[0x1] pa=0x82189000
... L0[0x0] pa=0x82184000
... ... page 0x0 VA=0x40000000 -> PA=0x87ffc000 flags=0xb
.. L1[0xff] pa=0x8218b000
... L0[0x1ff] pa=0x82186000
... ... page 0x1ff VA=0x3fffffff000 -> PA=0x87ffb000 flags=0x5
vm_func_test passed!
--- vm_mapping_test ---
Mapping VA 0x100000 -> PA 0x87ffb000 (R W)
Mapping VA 0x8000 -> PA 0x87ffc000 (R W X)
Unmapping VA 0x100000
Unmapping VA 0x8000
vm_mapping_test passed!
[PASS] VM test (0x4 harts)
All tests completed across 4 harts
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