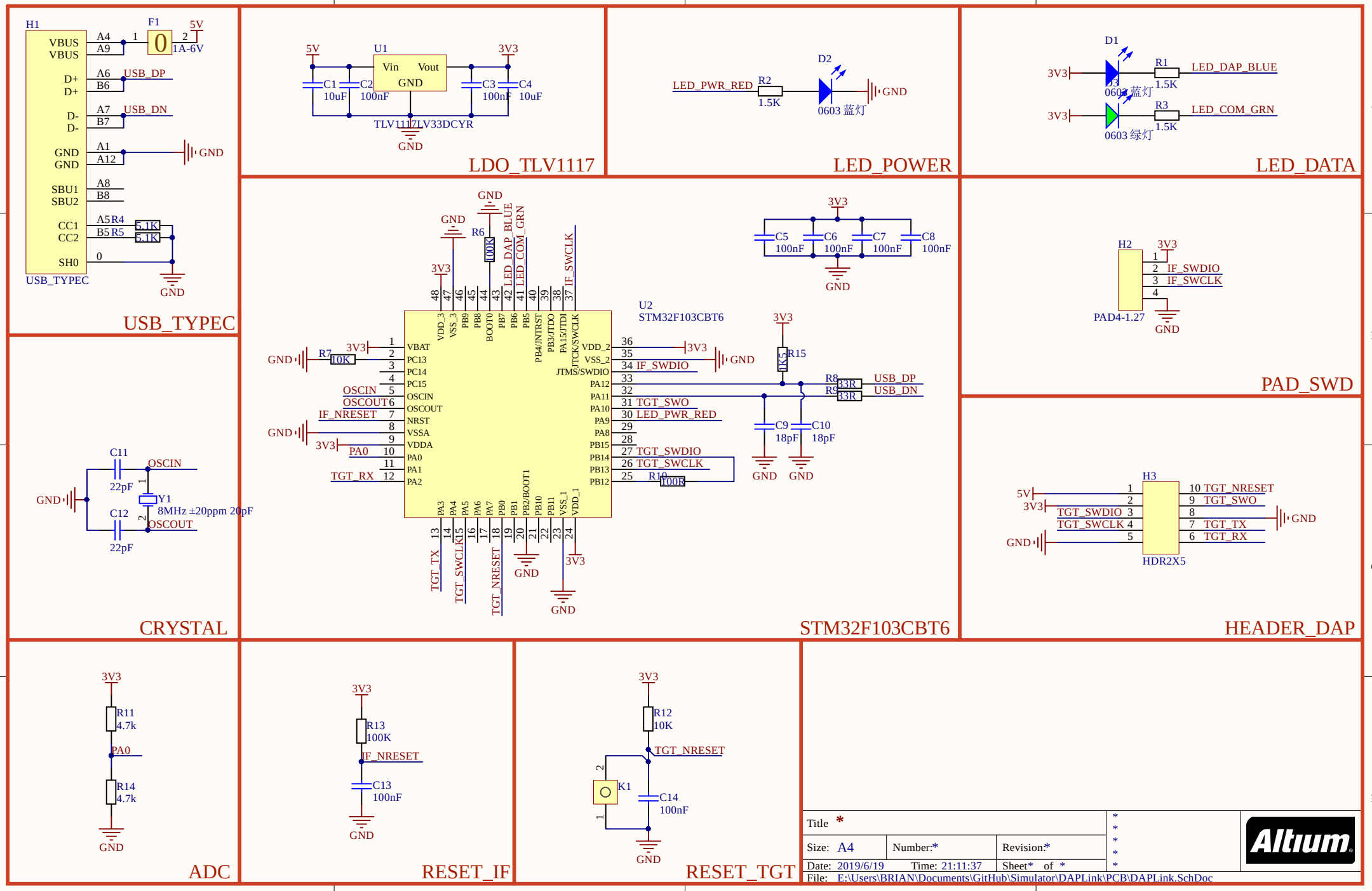
USB_TYPEC
H1
VBUS A4
VBUS A9
D+ A6 USB_DP
D+ B6
D- A7 USB_DN
D- B7
GND A1
GND A12
SBU1 A8
SBU2 B8
CC1 A5 R4 5.1K
CC2 B5 R5 5.1K
SH0 0
USB_TYPEC
F1
5V
1A-6V
GND
LDO_TLV1117
U1
5V
3V3
Vin
Vout
GND
C1 10uF
C2 100nF
C3 100nF
C4 10uF
TLV1117LV33DCYR
GND
LED_POWER
LED_PWR_RED
R2 1.5K
D2
0603 蓝灯
GND
LED_DATA
D1
3V3
R1 1.5K
LED_DAP_BLUE
0603 蓝灯
R3 1.5K
LED_COM_GRN
0603 绿灯
PAD_SWD
H2
3V3
IF_SWDIO
IF_SWCLK
PAD4-1.27
GND
CRYSTAL
C11 22pF
OSCIN
Y1
8MHz ±20ppm 20pF
C12 22pF
OSCOUT
GND
STM32F103CBT6
U2
STM32F103CBT6
VBAT
PC13
PC14
PC15
OSCIN
OSCOUT
NRST
VSSA
VDDA
PA0
PA1
PA2
VDD_2
VSS_2
JTMS/SWDIO
PA12
PA11
PA10
PA9
PA8
PB15
PB14
PB13
PB12
R6 100K
R7 10K
R8 33R
R9 33R
R10 100R
R15 1K5
C5 100nF
C6 100nF
C7 100nF
C8 100nF
C9 18pF
C10 18pF
IF_NRESET
TGT_RX
TGT_TX
TGT_SWCLK
TGT_NRESET
TGT_SWDIO
TGT_SWO
LED_PWR_RED
USB_DP
USB_DN
LED_DAP_BLUE
LED_COM_GRN
IF_SWCLK
HEADER_DAP
H3
5V
3V3
TGT_SWDIO
TGT_SWCLK
GND
TGT_NRESET
TGT_SWO
TGT_TX
TGT_RX
HDR2X5
ADC
R11 4.7k
PA0
R14 4.7k
RESET_IF
R13 100K
IF_NRESET
C13 100nF
RESET_TGT
R12 10K
TGT_NRESET
K1
C14 100nF
Title *
Size: A4
Number:*
Revision:*
Date: 2019/6/19
Time: 21:11:37
Sheet* of *
File: E:\Users\BRIAN\Documents\GitHub\Simulator\DAPLink\PCB\DAPLink.SchDoc
Altium