

REVISION	DESCRIPTION	DATE	APPROVED

Interface description

PROTOCOL SELECTION

PS pin input level has to be defined in dependence to protocol selection.

- PS = 0 activates SPI.
- PS = 1 activates I2C.

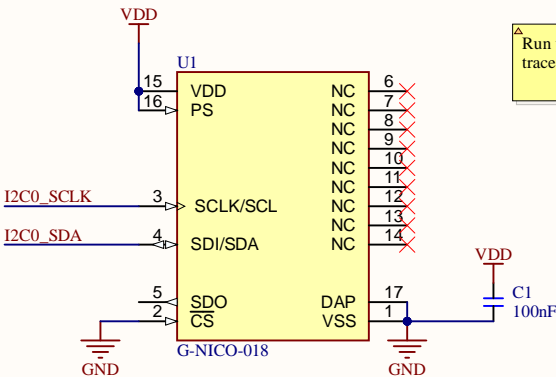
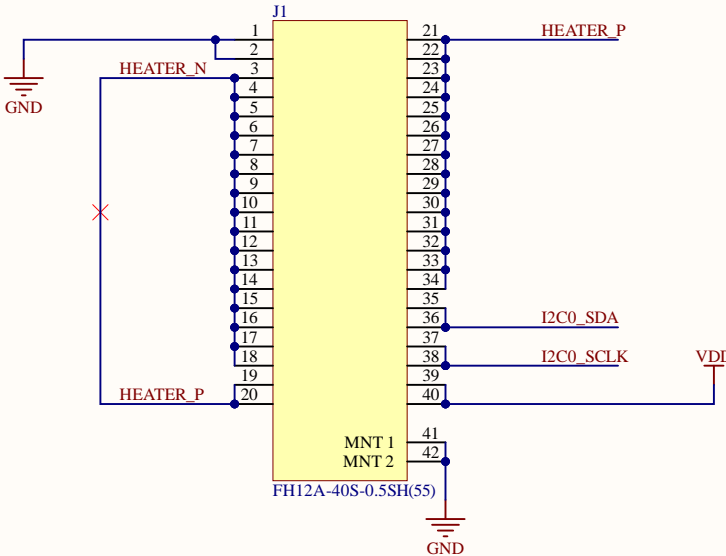
I²C ADDRESS SELECTION

The I²C address can be selected by CSB pin.


- CSB=1 the address is 1110110x.
- CSB=0 the address is 1110111x.

Therefore, two TSYS01 can be interfaced on the same I²C bus.

Run via from die attach pad to heating trace layer



REV			
A	DD	Initial Capture	5/2/2024
B	DD	Changed board shape	8/20/24
C	DD	Changed board shape. Changed FFC connector from 20 pos to 40 pos. Increased pattern and length of heater trace. Increased width of feedlines between connector and heater trace	9/24/24
D	IEO	Reversed pinout from FFC. Fixed layout issues between HEATER_P and HEATER_N	10/23/24

 <div>295 Foster St Suite 202 Littleton, MA 01460 www.odic.com</div>		PROJECT		GHL NAATOS Sample Prep Heater Board RevB ODIC.PrjPcb	
		TITLE		Main Schematic	
APPROVALS		DATE			
DRAWN: DD		5/28/2025			
CHECKED: *					
APPROVED: *					
		SIZE	PROJECT ID	DWG NO.	REV
		B	263-03	GHL-1-21001	D
		FILE NAME Main Schematic.SchDoc			SHEET 1 OF 1