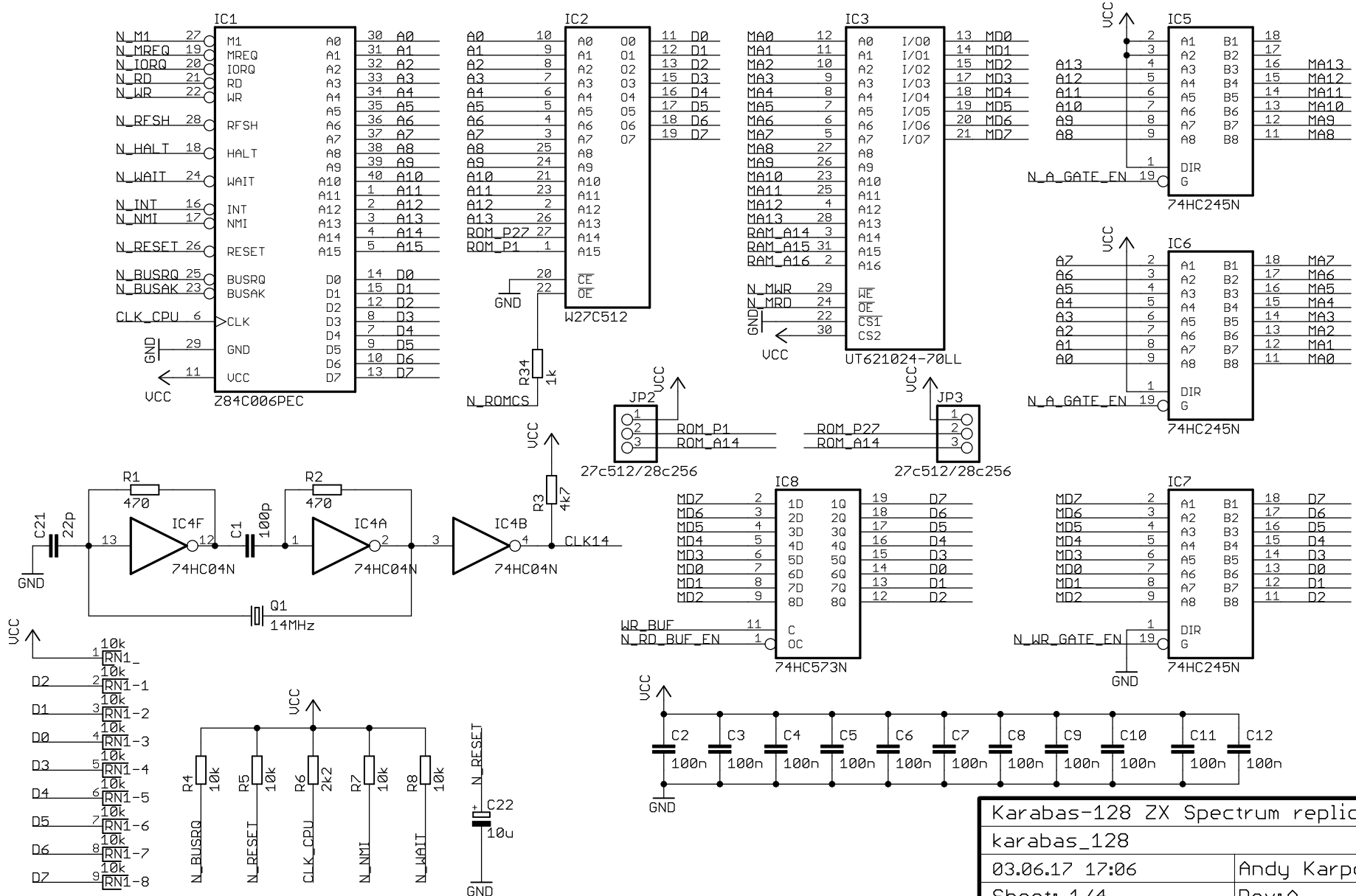
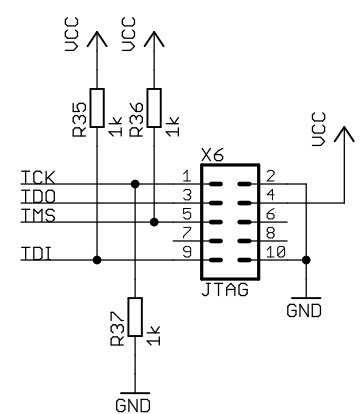


CPU & Memory



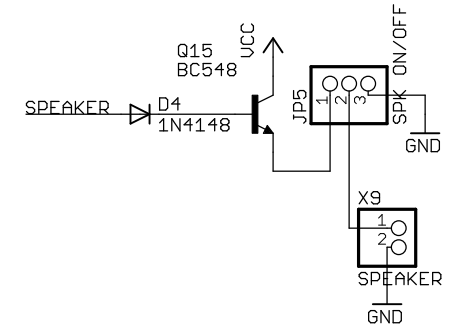


Rev: A

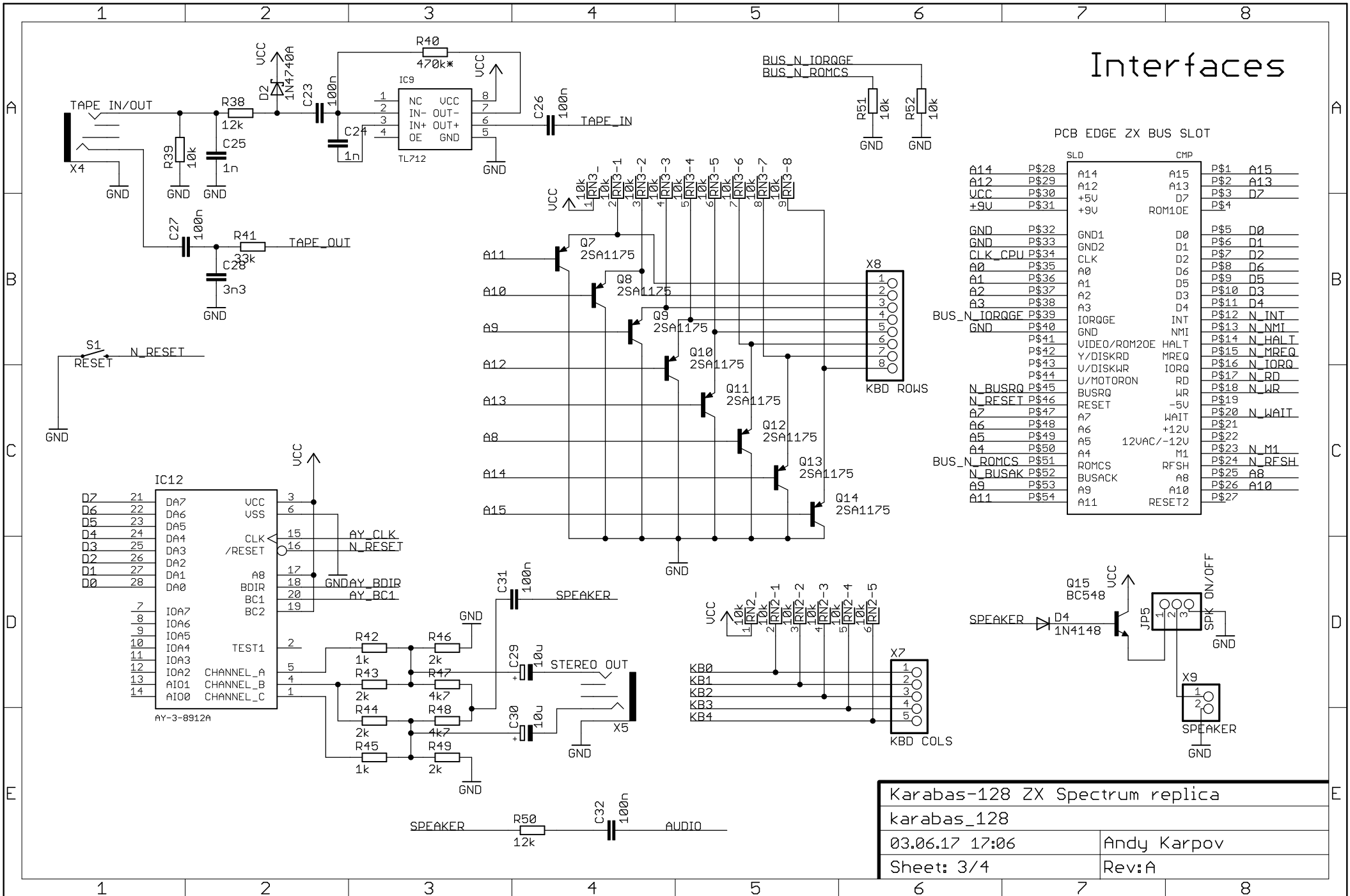
Interfaces

PCB EDGE ZX BUS SLOT

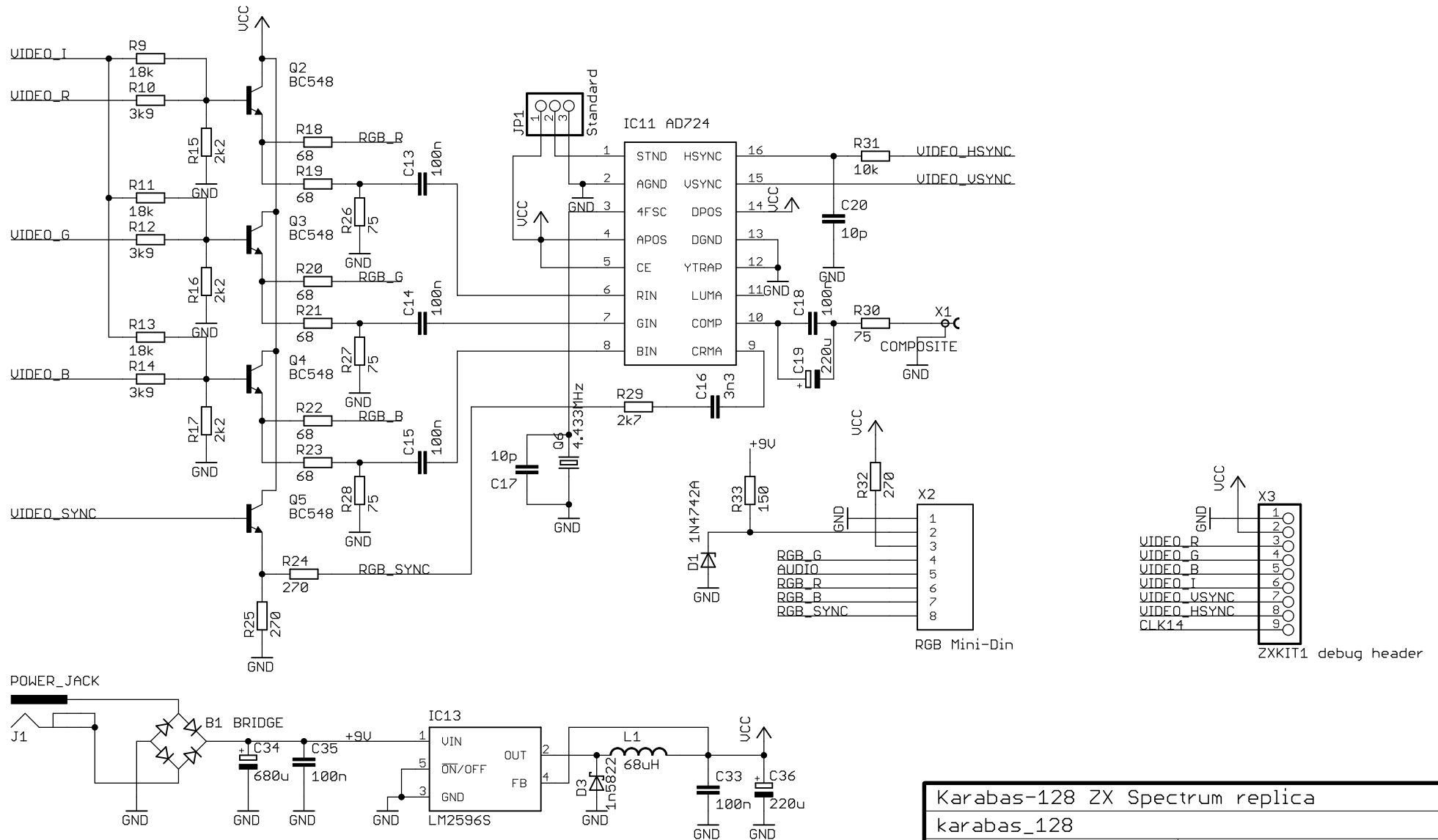
SLD	CMP
A14 P\$28	A15 P\$1
A12 P\$29	A13 P\$2
UCC P\$30	D7 P\$3
+9V P\$31	D7 P\$4
	ROM10E
GND P\$32	D0 P\$5
GND P\$33	D1 P\$6
CLK_CPU P\$34	D2 P\$7
A0 P\$35	D6 P\$8
A1 P\$36	D5 P\$9
A2 P\$37	D3 P\$10
A3 P\$38	D4 P\$11
BUS_N_IORQGE P\$39	P\$12 N_INT
GND P\$40	INT P\$13
	N_NMI
P\$41	P\$14 N_HALT
VIDEO/ROM20E HALT P\$42	P\$15 N_MREQ
Y/DISKRD MREQ P\$43	P\$16 N_IORQ
U/DISKWR IORQ P\$44	P\$17 N_RD
U/MOTORON RD P\$45	P\$18 N_WR
N_BUSRQ P\$46	WR P\$19
N_RESET P\$47	RESET -5V P\$20
A7 P\$48	WAIT P\$21
A6 P\$49	A6 +12V P\$22
A5 P\$50	A5 12VAC/-12V P\$23
A4 P\$51	M1 P\$24
BUS_N_ROMCS P\$52	ROMCS RFSH P\$25
N_BUSAK P\$53	A8 P\$26
A9 P\$54	A10 P\$27
A11 P\$54	RESET2 P\$27
A14	A15
A12	A13
UCC	D7
+9V	D7
GND	D0
GND	D1
CLK_CPU	D2
A0	D6
A1	D5
A2	D3
A3	D4
BUS_N_IORQGE	P\$12 N_INT
GND	INT
	N_NMI
P\$41	P\$14 N_HALT
VIDEO/ROM20E HALT	P\$15 N_MREQ
Y/DISKRD MREQ	P\$16 N_IORQ
U/DISKWR IORQ	P\$17 N_RD
U/MOTORON RD	P\$18 N_WR
N_BUSRQ	WR
N_RESET	RESET -5V
A7	WAIT
A6	A6 +12V
A5	A5 12VAC/-12V
A4	M1
BUS_N_ROMCS	ROMCS RFSH
N_BUSAK	A8
A9	A10
A11	RESET2



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Sheet: 3/4	Rev:A



RGB & Composite output, AC/DC unit



Karabas-128 ZX Spectrum replica

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Sheet: 4/4

Rev:A