

## SRI VENKATESWARA UNIVERSITY

## Provisional Certificate cum Consolidated Marks Memorandum

The same of the sa	The same of the sa	Reg. No.: 201804
This is to copy him	See Sini. Shancing a Raka	has qualified himself / herself for the
Degree of Bachelor of	- George in the RECEPORTS MEST	
he I she having bed	en declare er hane passe	the Examination prescribed therefor in
the PASS	1010) ja	and that he I she has done all that is
necessary for the formal pres	centation of the Degac of Sa-	non Technology.

The following are the marks secured by the candidate:

SUBJECTS	Sessional Marks		University Exam. Marks			Constant of the	essional Marks		University Exam. Marks		
	Max. Marks	Marks Secured	Max. Marks	Pass Marks	Marks	JB)L	N. S.	Secure	Max	Pass Marks	Marks
ENGLISH	50	30	100	35	52	YEAR EMGINEERING CHEMISTRY	50	14	100	35	40
ECONOMICS AND ACCOUNTANCY	50	1000	100	35	59	COMPUTER PROGRAMMING	50.	22	100	25	41
MATHEMATICS-I	50	A COLUMN TO THE REAL PROPERTY OF THE PERTY O	100	35	46	ENGINEERING GRAPHICS	50.	22	108	35	4
MATHEMATICS-11	50		100	35	48	PROGRAMMING LAB	50	40	50	18	4
ENGINEERING PHYSICS	50	1 100,000 1	100	35	40	WORKSHOP PRACTICE	50	30	50	18	4
1 SEMESTER OF 11 YEAR	04	20	100	33	-	11 SEMESTER OF 11 YEAR	4	-20	200	1000	100
MATHEMATICS-III	50	15	100	35	38	CIRCUITS & NETWORKS-11	50	18	100	35	1 3
	50	18	100	35	35	ENGINEERING THERMODYNAMICS	50	10	100	35	1 4
IRCUITS & NETWORKS-1	50	13	100	35	43	ELECTRICAL MEASUREMENTS \$	50	22	100	35	1 4
ELECTROMAGNETIC FIELDS	-			35	41	INSTRUMENTATION		2000	1000		
LECTRONIC DEVICES	50	22	100		10000	ELECTRONIC CIRCUITS	50	27	100	35	5
IGNALS & SYSTEMS	80	21.	100	35	39		50	18	100	35	15
LECTRICAL MACHINES	50		100	35	40	LOGIC CIRCUITS	50	10	100	35	1
LECTRONIC DEVICES LAB	50	31	50	18	20	MATERIALS TECHNOLOGY		33	50	18	1
LECTRICAL GIRCUITS LAB	50	38	50	18	35	ELECTRICAL MACHINES LAB	50		100	1	
	100	200	520	200	1000	ELECTRONIC CIRCUITS LAB	50	34	50	18	1
I SEMESTER OF ILL YEAR	1	1000	-	200		11 SEMESTER OF 111 YEAR		100			100
INEAR CONTROL SYSTEMS	50	29	100.	35	42	MANAGEMENT SCIENCE	50	22	100	35	
ULSE & DIGITAL CIRCUITS	50	20	100	35	7.1	MICROPROCESSOR & APPLICATIONS	50	23	100	35	1
INEAR I.C APPLICATIONS	50	25	100	35	57	MODERN CONTROL THEORY	50	22	100	35	13
LECTRONIC MEASUREMENTS	50	28	100	35	71	ADVANCED IC APPLICATIONS	50	34	100	35	1
NALYTICAL INSTRUMENTATION	50	22	100	35	35	COMMUNICATION SYSTEMS	50	13	100	35	13
ASIC SENSORS AND TRANSDUCERS	50	30	100	35	45	INDUSTRIAL INSTRUMENTATION	50		100	35	1
EASUREMENTS & INSTRUMENTATION	50	30	50	18	28	TRANSDUCERS & CONTROL SYSTEMS	50	35	50	18	10
	1					LAB		200	665	1	133
AB	50	31	50	18	36	IC APPLICATIONS LAB	50	36	50	18	1
ULSE & DIGITAL CIRCUITS LAB	30	91	20			11 SEMESTER OF IV YEAR					10
I SEMESTER OF IV YEAR	50	22	100	35	73	PROCESS DYNAMICS & CONTROL	50	17	100	35	1
OMPUTER ORGANIZATION	50				37	COMPUTER PROCESSES CONTROL	50	32	100	35	
HOUSTRIAL POWER ELECTRONICS	50	29	100	35			20 10 10 10		10000		100
IGITAL SIGNAL PROCESSING	50	14	100	35	42	ROBOTICS AND AUTOMATION	50	36	100	35	
IO-MEDICAL INSTRUMENTATION	50	35	100	35	45	FIBER OPITICS & LASER	50	27	100	35	1
.C. BASED INSTRUMENTATION	50		100	35	35	INSTRUMENTATION	2000		100		10
EURAL NETWORKS AND FUZZY	50		100	35	78	INSTRUMENTATION & PROCESS	50	33	60	18	
0610	-	200			357	CONTROL LAB		100			
CROPROCESSOR LAB	50	33	50	18	35	PROJECT WORK	100	84	100	35	1
BASED INSTRUMENTATION LAB	50	32	50	18	40		4	73	155	4 100	1
		***									
02-06-2008 I to IV :	3881		a eigh								SALES BERNE

Date: Tirupati, (A.P.) II to IV: 3170 (three one seven zero)

SI. No.: TS 000913

ead by:

Compared by



Suptd.:

Assistant Administrative Officer (Exams)

Con Project Constant Con

Note: Any discrepancy in the above entries or between the results published and the marks noted above, must be brought to the notice of the Controller of Examinations, IMMEDIATELY.