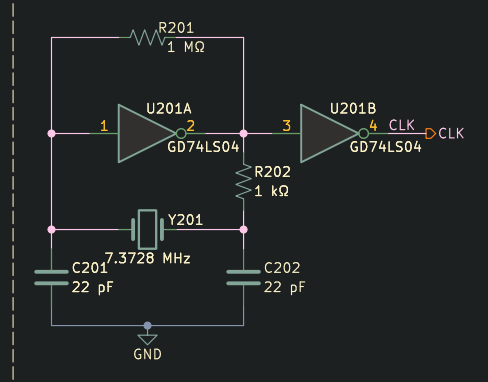
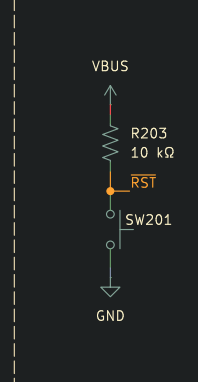


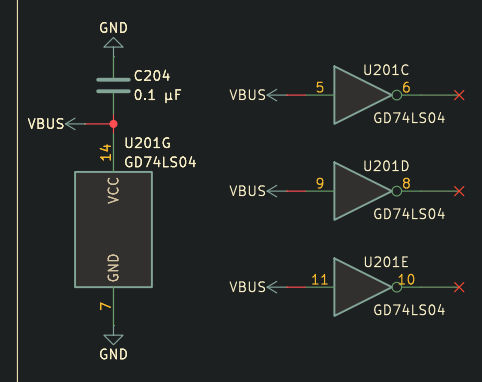
CLOCK



RESET

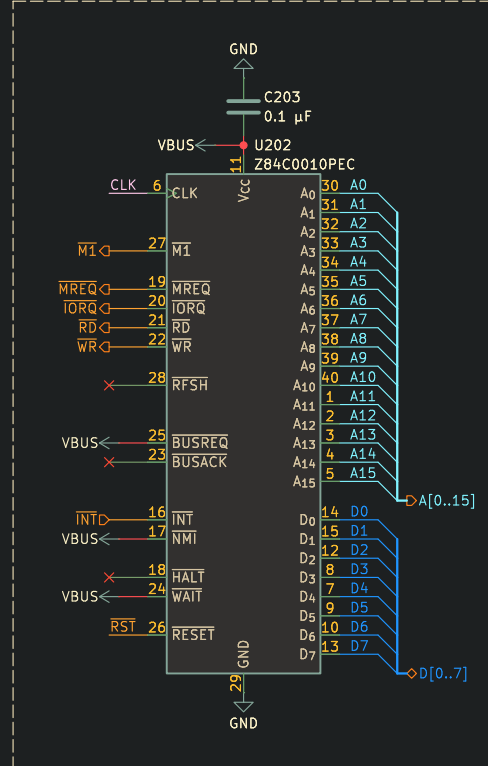


74LS04



Power is provided to the unused inverters to ensure they remain stable at all times.

CPU



Dragos Bajanica

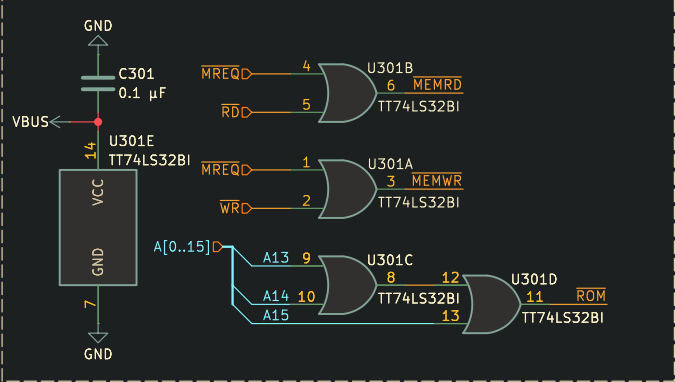
Sheet: /CPU/
File: cpu.kicad_sch

Title: CPU

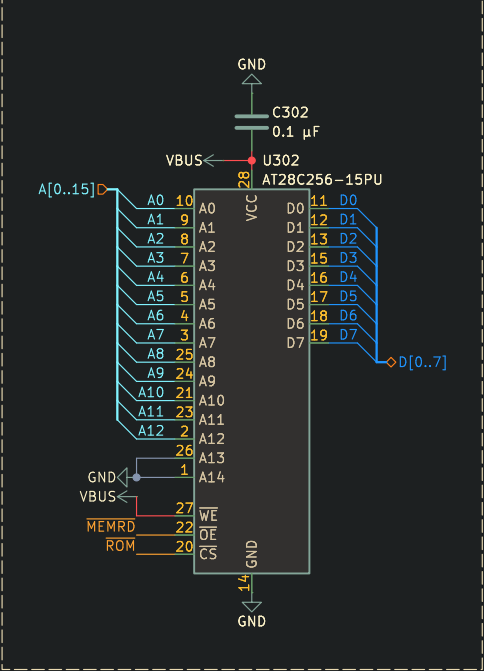
Size: A4 Date: 2025-12-31
KiCad E.D.A. 9.0.6

Rev: A
Id: 2/4

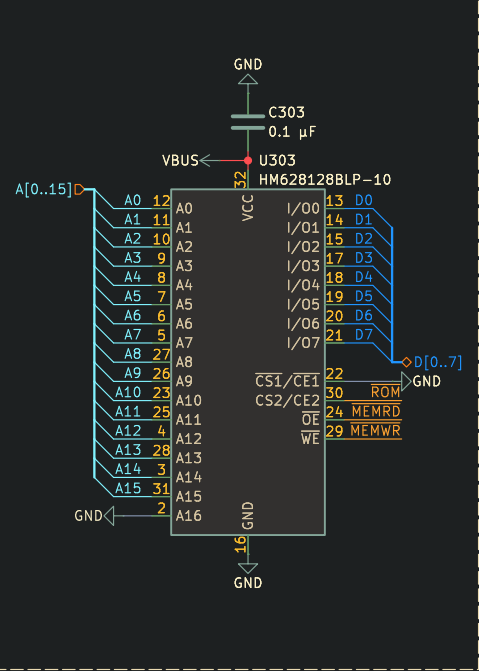
DECODING LOGIC



ROM



RAM



Dragos Bajanica

Sheet: /Memory/
File: memory.kicad_sch

Title: Memory

Size: A4
Date: 2025-12-31

KiCad E.D.A. 9.0.6

Rev: A
Id: 3/4

The diagram shows the J401 TYPE-C-31-M-12 connector with the following connections:

- VBUS:** Connected to A4.
- CC1:** Connected to A5 R401.
- CC2:** Connected to B5 R402.
- D-:** Connected to A7.
- D-:** Connected to B7 D-.
- D+:** Connected to A6.
- D+:** Connected to B6 D+.
- SBU1:** Connected to A8.
- SBU2:** Connected to B8.
- SHIELD:** Connected to S1.
- GND:** Connected to A1.

The **POWER LED** circuit is shown in a dashed box:

- VBUS** is connected to the anode of the LED (D401 LED).
- The cathode of the LED is connected to R403 (1 kΩ).
- R403 is connected to GND.

There is also a power LED to make sure it works.

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