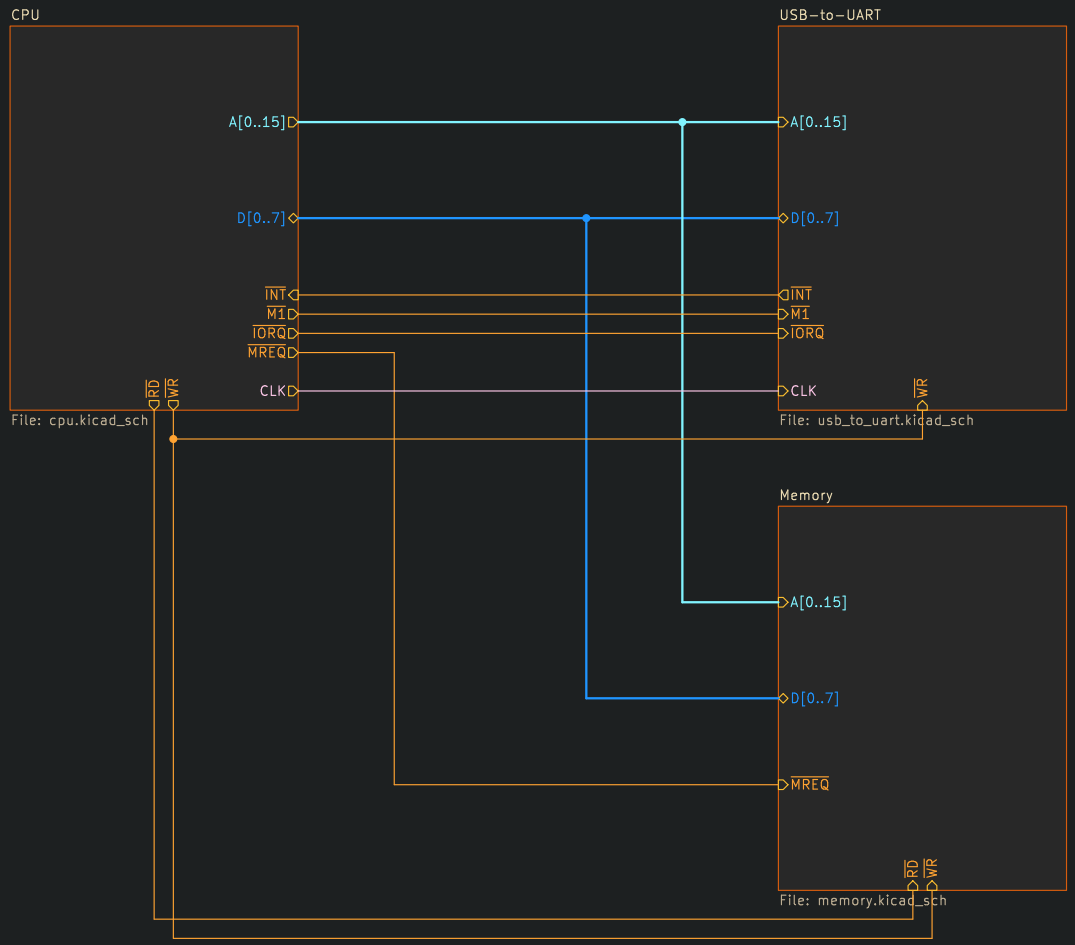
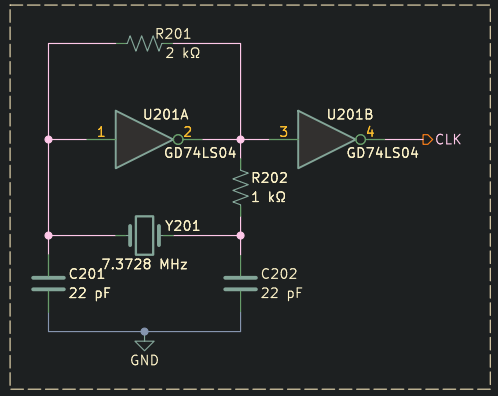


MOUNTING HOLES

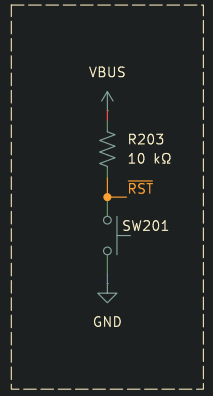
MH101 M3 GND
MH103 M3 GND
MH102 M3 GND
MH104 M3 GND

Dragos Bajanica		
Sheet: /		
File: zed-80.kicad_sch		
Title: zed-80		
Size: A4	Date: 2026-01-01	**Rev: B**
KiCad E.D.A. 9.0.6		Id: 1/4

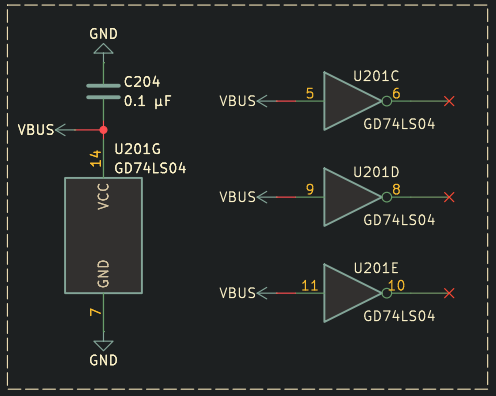
CLOCK



RESET



74LS04



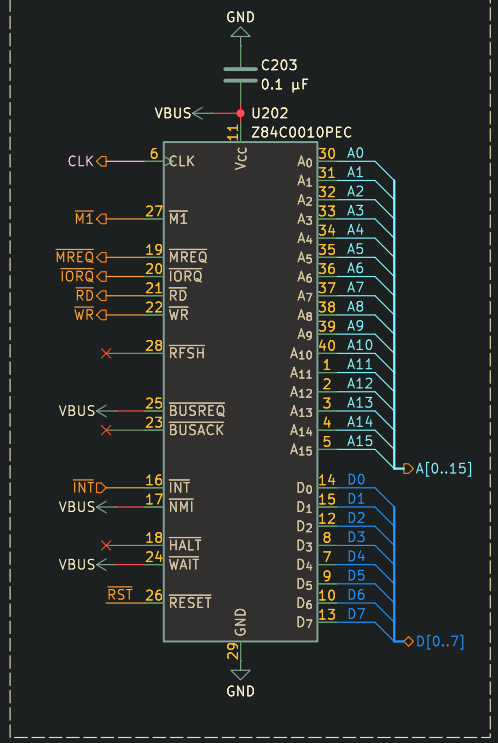
PULL-UP

Power is provided to the unused inverters to ensure they remain stable at all times. This prevents them from picking up any static signals.

The interrupt control line requires to be pulled up to VBUS to ensure it does not float. This is due to the M68B50P's interrupt pin being open drain, so leaving the line floating.

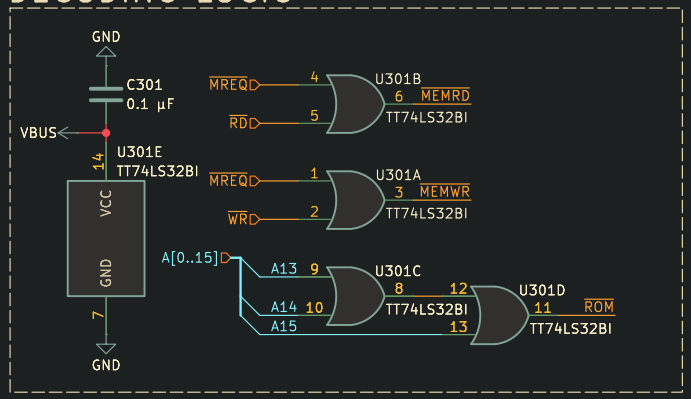
An extra pull-up for the clock is added due to the 74LS04 being weaker and only producing a maximum output voltage of 3.4V, which is not enough for a CMOS chip.

CPU

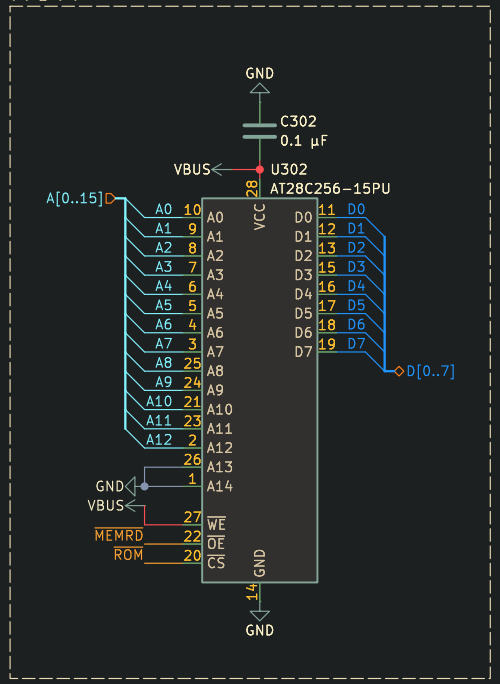


Dragos Bajanica		
Sheet: /CPU/		
File: cpu.kicad_sch		
Title: CPU		
Size: A4	Date: 2026-01-01	**Rev: B**
KiCad E.D.A. 9.0.6		Id: 2/4

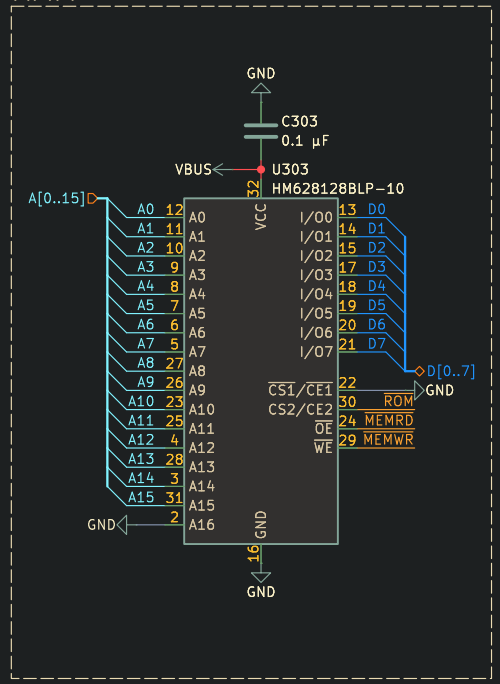
DECODING LOGIC



ROM

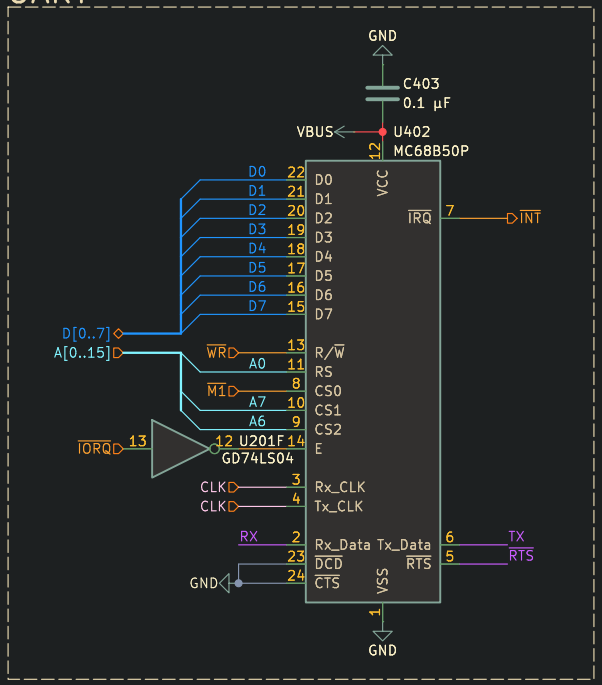


RAM

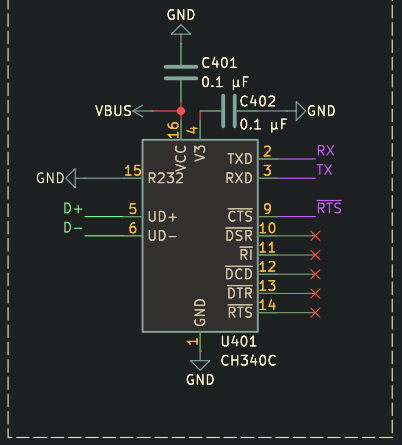


Dragos Bajanica		
Sheet: /Memory/		
File: memory.kicad_sch		
Title: Memory		
Size: A4	Date: 2026-01-01	**Rev: B**
KiCad E.D.A. 9.0.6		Id: 3/4

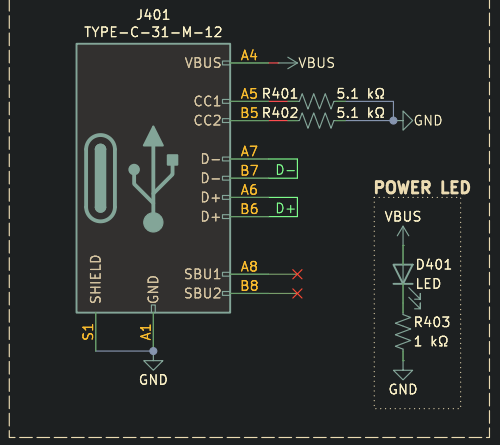
UART



UART–TO–USB



USB–C



5.1 kΩ resistors on both CC1 and CC2 are required to allow current to flow to power the entire computer.

There is also a power LED to make sure it works.

Dragos Bajanica		
Sheet: /USB–to–UART/		
File: usb_to_uart.kicad_sch		
Title: USB–to–UART		
Size: A4	Date: 2026–01–01	**Rev: B**
KiCad E.D.A. 9.0.6		Id: 4/4