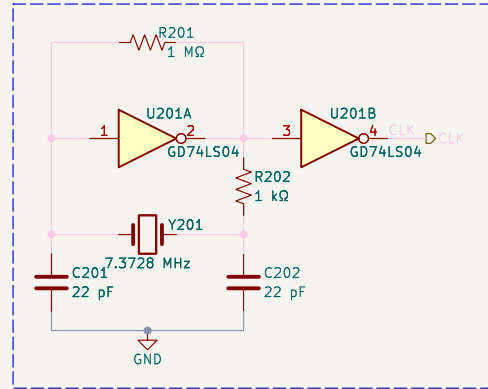
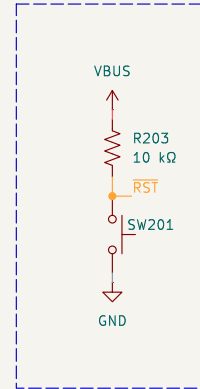


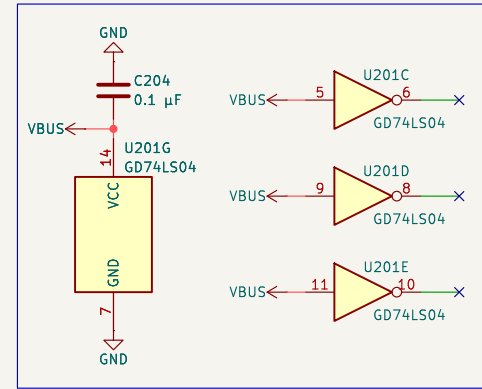
CLOCK



RESET

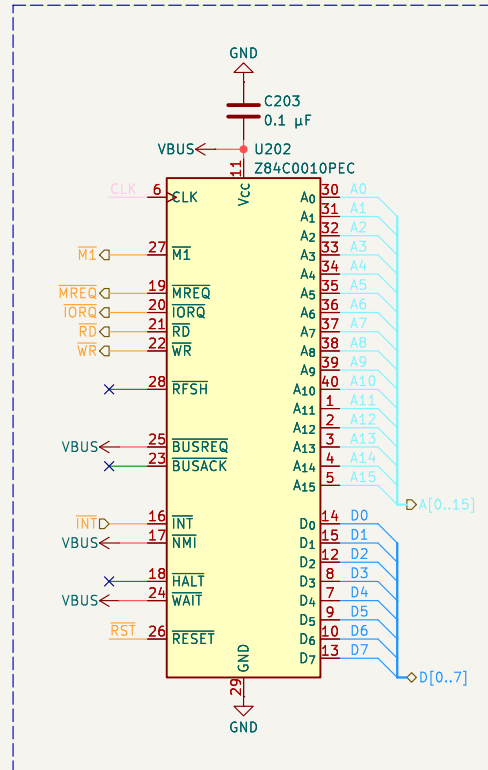


74LS04



Power is provided to the unused inverters to ensure they remain stable at all times.

CPU



Dragos Bajanica

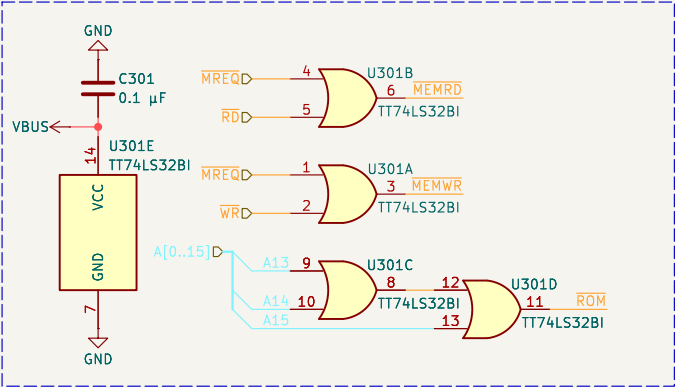
Sheet: /CPU/
File: cpu.kicad_sch

Title: CPU

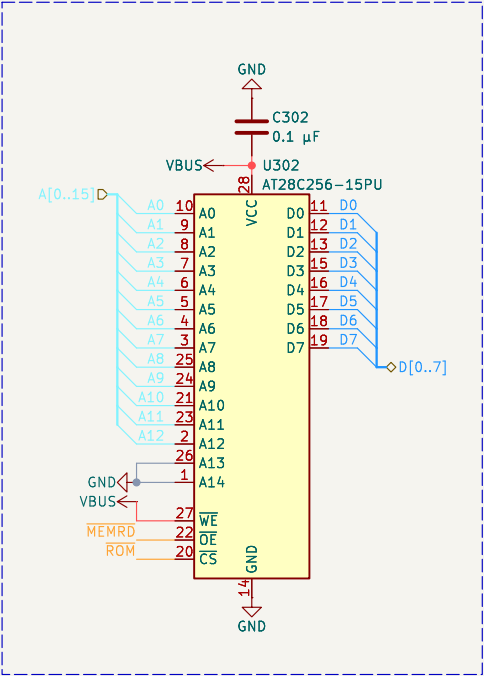
Size: A4 Date: 2025-12-31
KiCad E.D.A. 9.0.6

Rev: A
Id: 2/4

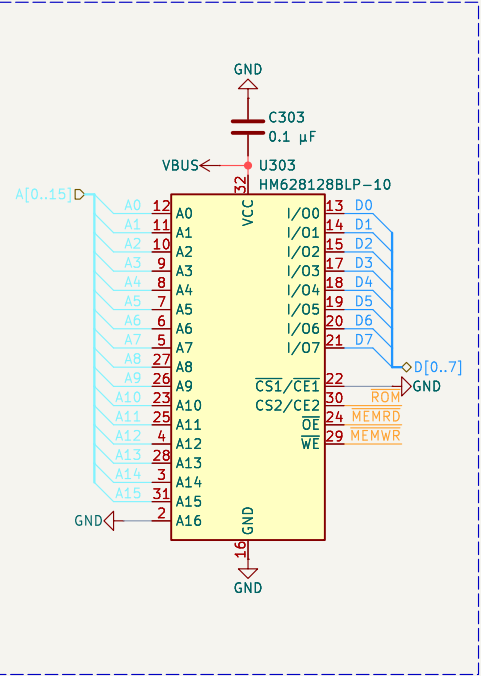
DECODING LOGIC



ROM



RAM



Dragos Bajanica

Sheet: /Memory/
File: memory.kicad_sch

Title: Memory

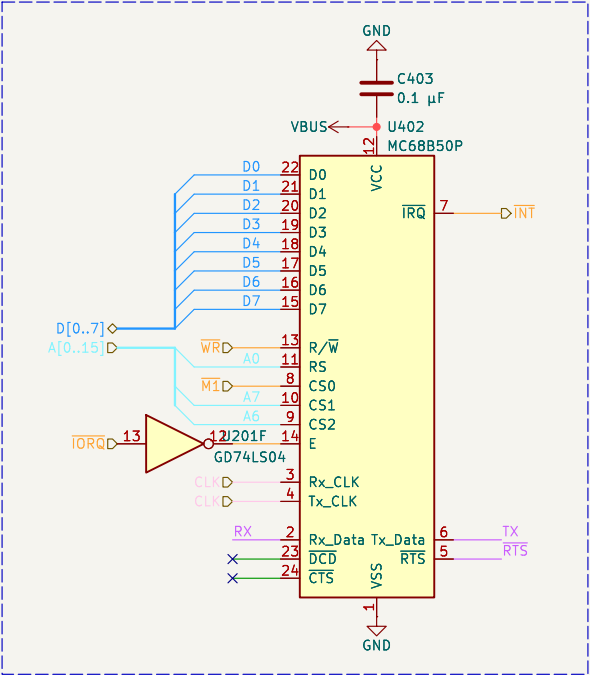
Size: A4
KiCad E.D.A. 9.0.6

Date: 2025-12-31

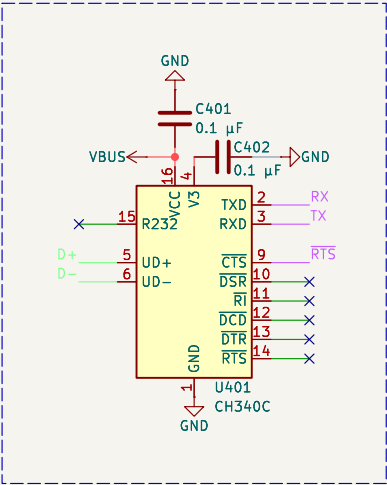
Rev: A

Id: 3/4

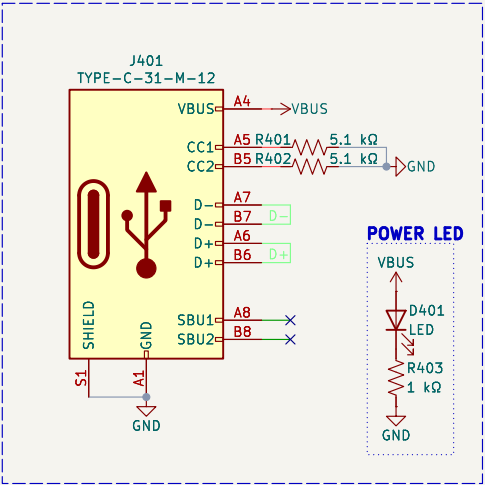
UART



UART-TO-USB



USB-C



5.1 kΩ resistors on both CC1 and CC2 are required to allow current to flow to power the entire computer.
There is also a power LED to make sure it works.