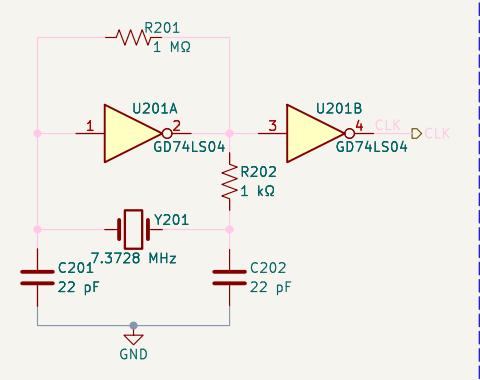
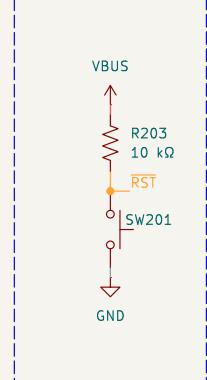


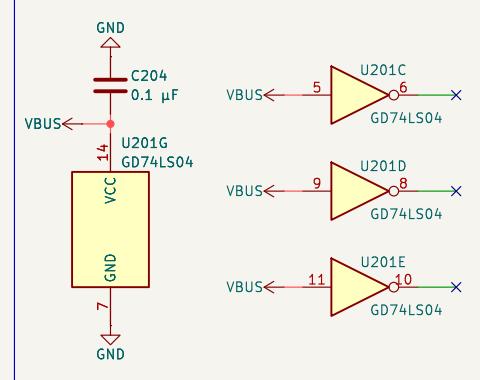
CLOCK



RESET

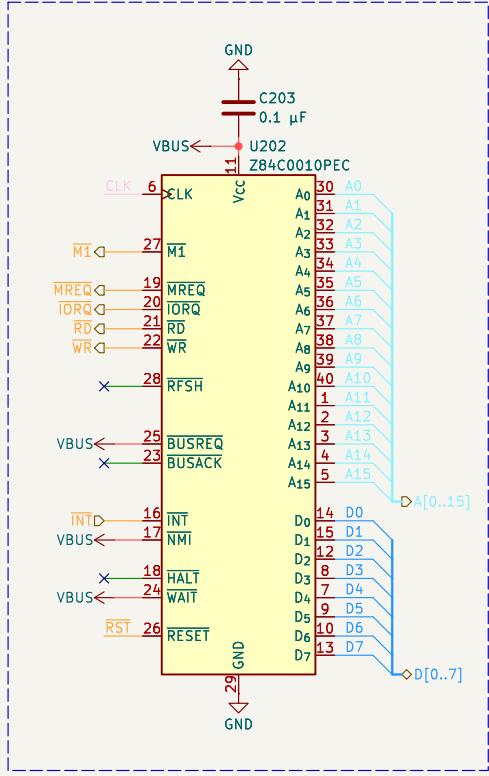


74LS04



Power is provided to the unused inverters to ensure they remain stable at all times.

CPU



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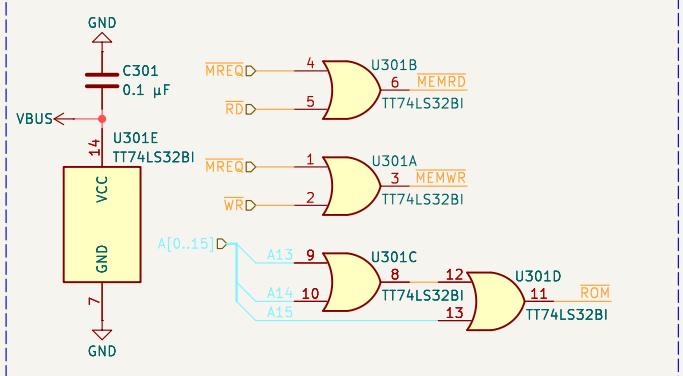
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File: cpu.kicad_sch

Title: CPU

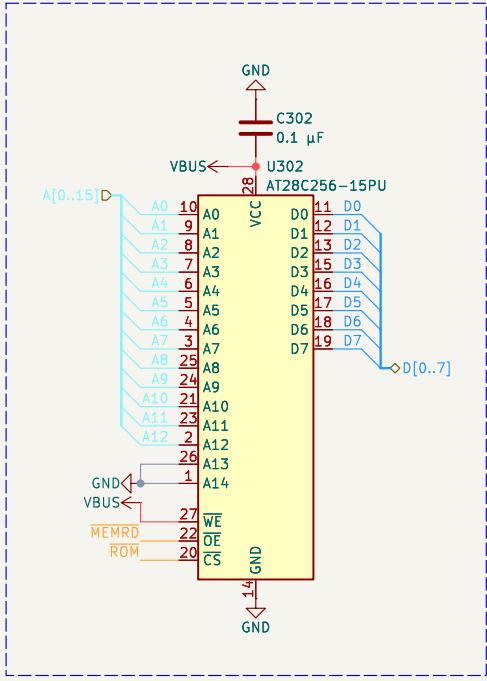
Size: A4 | Date: 2025-12-31
KiCad E.D.A. 9.0.6

Rev: A
Id: 2/4

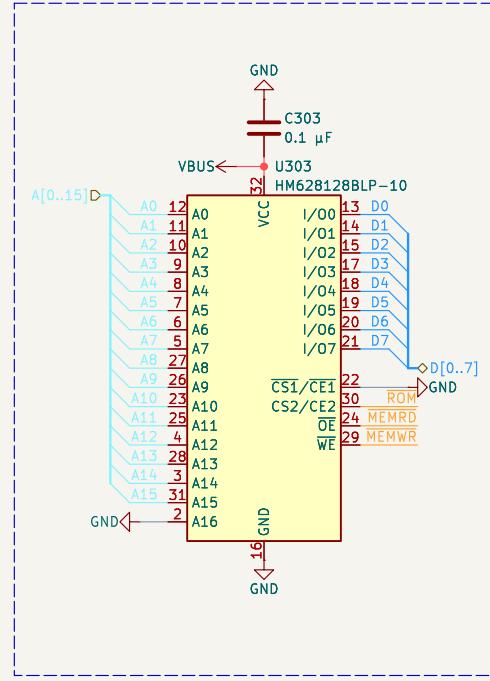
DECODING LOGIC



ROM



RAM



Dragos Bajanica

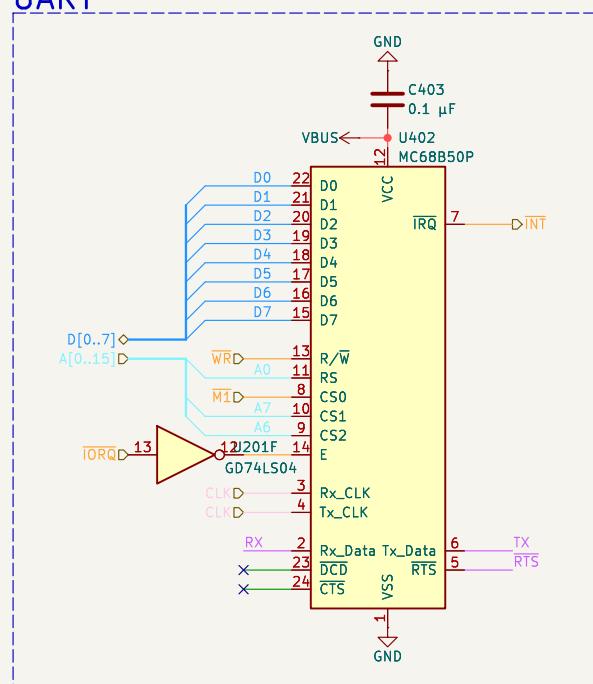
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Title: Memory

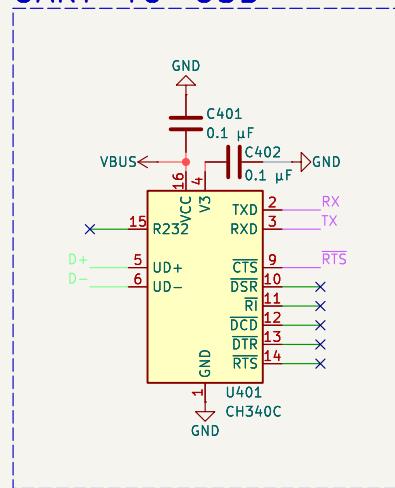
Size: A4 Date: 2025-12-31
KiCad E.D.A. 9.0.6

Rev: A
Id: 3/4

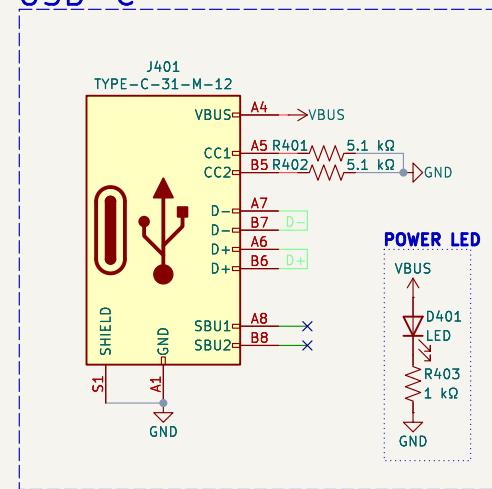
UART



UART-TO-USB



USB-C



5.1 k Ω resistors on both CC1 and CC2 are required to allow current to flow to power the entire computer.

There is also a power LED to make sure it works.

Dragos Bajanica

Sheet: /USB-to-UART/
File: usb_to_uart.kicad_sch

Title: USB-to-UART

Size: A4 Date: 2025-12-31
KiCad E.D.A. 9.0.6

Rev: A
Id: 4/4