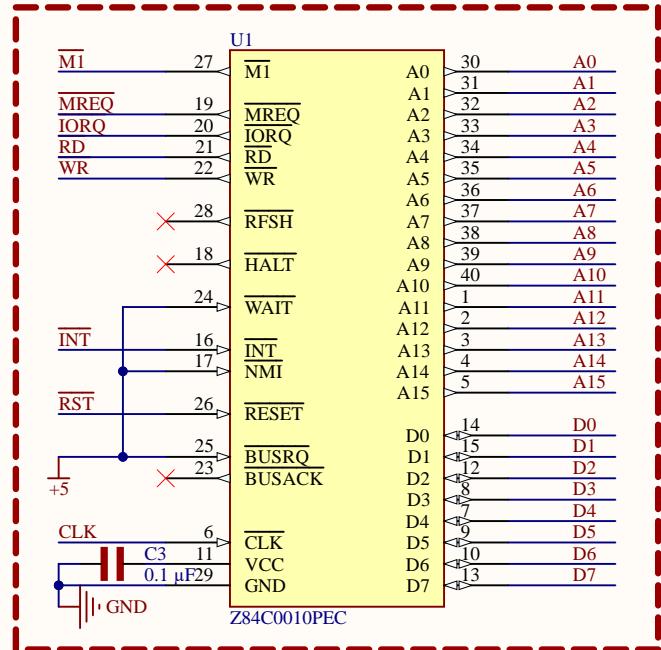
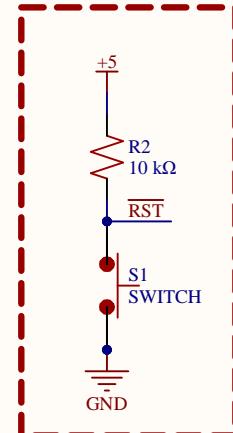
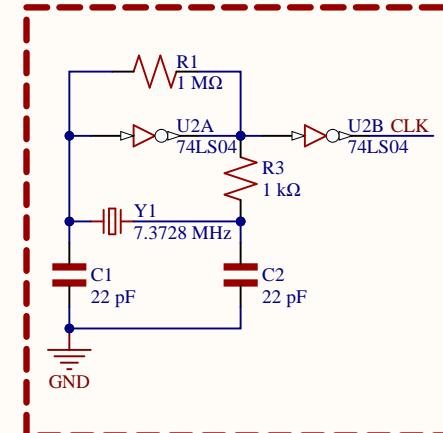
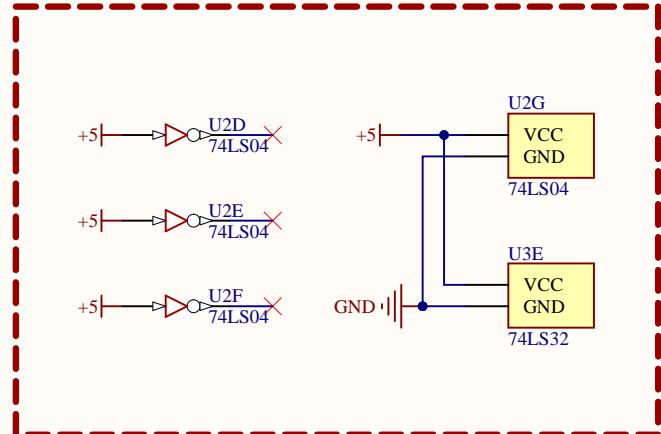


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2

3

4

CPU**RESET****CLOCK****LEFTOVER LOGIC**

Title

Z80

Size

A

Number

1

Revision

1

Date:

12/24/2025

Sheet of

3

File:

C:\Users\.\z80.SchDoc

Drawn By:

Dragos B.

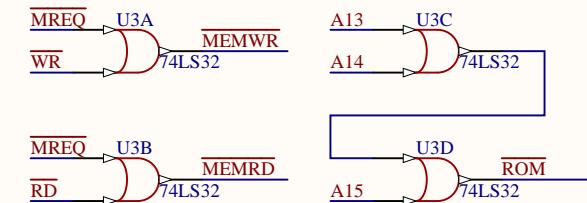
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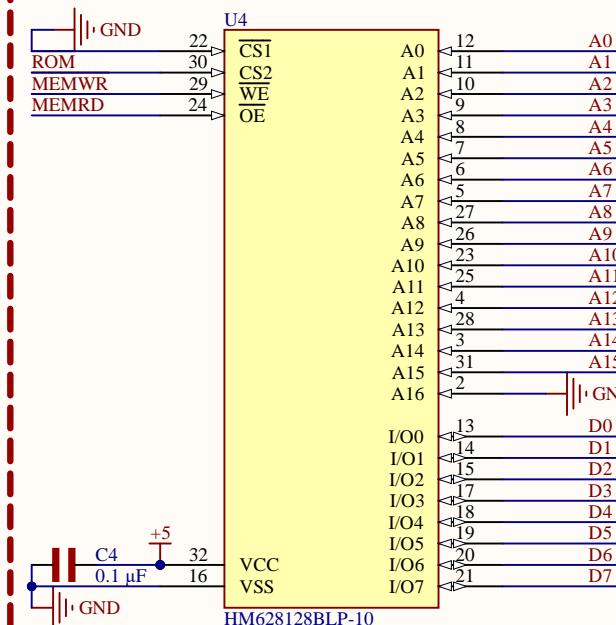
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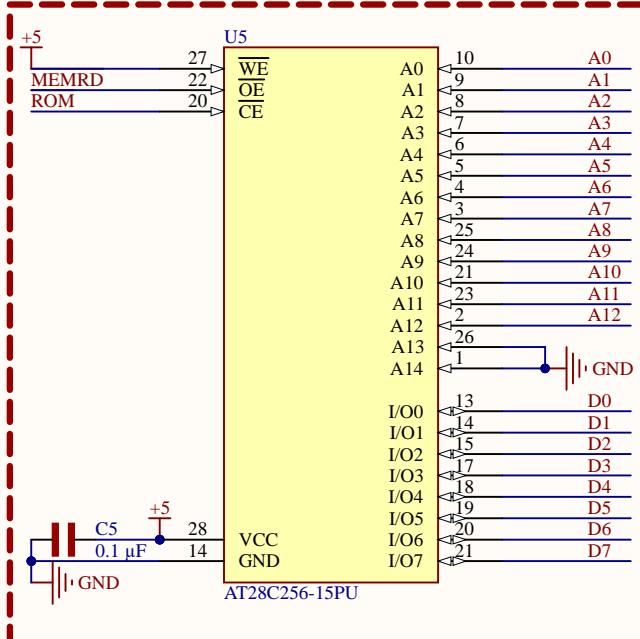
MEMORY LOGIC



RAM



ROM



Title		
Memory		
Size	Number	Revision
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Date:	12/24/2025	Sheet of 3
File:	C:\Users\.\memory.SchDoc	Drawn By: Dragos B.

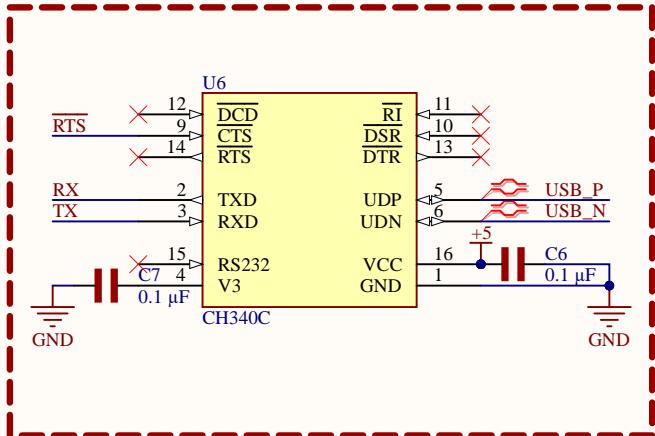
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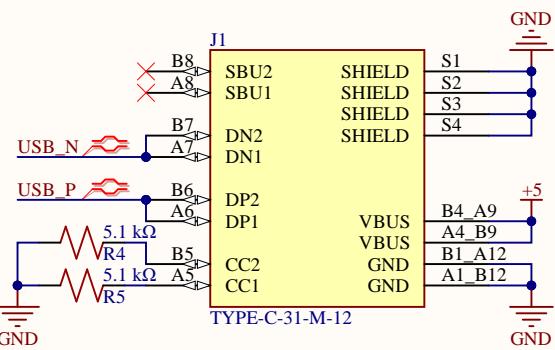
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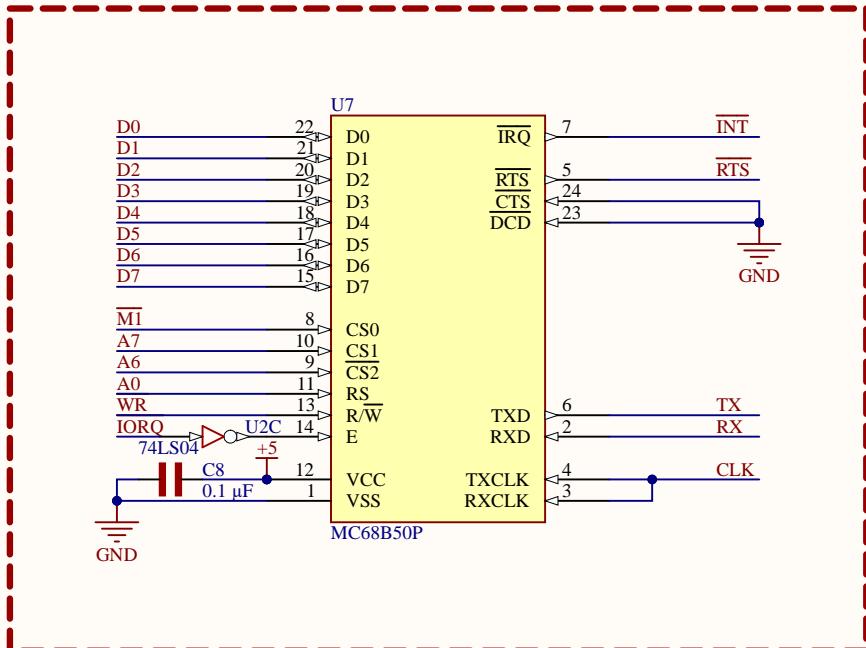
UART TO USB



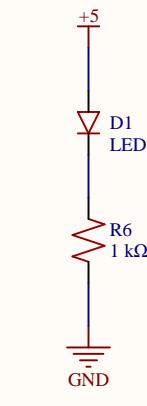
USB-C



UART



POWER LED



Title

UART

Size

A

Number

3

Revision

1

Date: 12/24/2025

File: C:\Users\...\uart.SchDoc

Sheet of 3

Drawn By: Dragos B.

1

2

3

4