Parallelization of the SLINK clustering algorithm

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1 Introduction

The purpose of this project is to implement the SLINK clustering algorithm and to apply parallelization techniques to improve its performance.

All the implementations of the clustering algorithms are written in C++ 20 and they are compiled with the GCC G++ 12.1.1 (Red Hat) compiler, using the -03 and -march=native flags.

In particular, we tested the clustering algorithm implementations on two datasets:

- Accelerometer¹ (A), composed of 153'000 samples, each of which with 5 attributes. For the tests we have performed, we have selected only the last 3 of them;
- Generated (G), composed of 100'000 samples, each of which with 45 randomly generated real attributes uniformly distributed in the range from 0 to 99 included.

We will report the mean execution time of 3 executions of each implementation on the two datasets, run on a machine equipped with an AMD Ryzen 7 1700 CPU with 8 cores and 16 threads, 16 GB of RAM, and running Fedora Workstation 36.

In particular, we measured the time taken by each step of the pseudo-code (reported as S1, S2, S3 and S4) to be executed.

In all the tests we used two std::vector data structures to hold the computed values of π and λ .

Note that the reported total times include also some extra time, which is spent by the various implementations to perform the service operations like allocating and de-allocating the memory, or copying the iterators.

Moreover, for space reasons, in some tests we do not report the results obtained using 2 and 16 threads.

2 Implementations

2.1 Sequential

We first implemented the sequential version of the clustering algorithm. This implementation is just the translation in C++ code of the pseudo-code reported in the slides.

The data is supplied as a std::vector<double *>, i.e., a vector of pointers to heap-allocated arrays, where each of these arrays holds the attributes of one data sample.

DS	S1	S2	S3	S4	Total
A	$19\mathrm{ms}$	$1\mathrm{m}\ 30\mathrm{s}$	$37.330\mathrm{s}$	$20.271\mathrm{s}$	$2\mathrm{m}\ 28\mathrm{s}$
G	$19\mathrm{ms}$	$8\mathrm{m}\ 59\mathrm{s}$	$16.246\mathrm{s}$	$12.802\mathrm{s}$	9 m 28 s

Table 1: Execution times of the sequential implementation

2.2 Multithreaded distance computation

From the previous tests results, we saw that the computation of the distances (i.e., the step 2 of the pseudo-code) is the most expensive step of the algorithm.

Since each distance can be computed independently from the others, their computation can be easily made parallel by using the *OpenMP* library.

The supplied data has the same memory layout as before.

DS	S1	S2	S3	S4	Total
A2	$10\mathrm{ms}$	$43.043\mathrm{s}$	$39.568\mathrm{s}$	$23.286\mathrm{s}$	$1\mathrm{m}\ 46\mathrm{s}$
A4	$11\mathrm{ms}$	$29.641\mathrm{s}$	$36.144\mathrm{s}$	$23.609\mathrm{s}$	$1\mathrm{m}\ 29\mathrm{s}$
A8	$11\mathrm{ms}$	$20.940\mathrm{s}$	$35.260\mathrm{s}$	$23.541\mathrm{s}$	$1\mathrm{m}\ 20\mathrm{s}$
A12	$10\mathrm{ms}$	$24.402\mathrm{s}$	$36.713\mathrm{s}$	$24.204\mathrm{s}$	$1\mathrm{m}\ 25\mathrm{s}$
A16	$11\mathrm{ms}$	$27.201\mathrm{s}$	$47.172\mathrm{s}$	$26.257\mathrm{s}$	$1\mathrm{m}\ 41\mathrm{s}$
G2	$13\mathrm{ms}$	$4\mathrm{m}\ 12\mathrm{s}$	$15.228\mathrm{s}$	$13.855\mathrm{s}$	4 m 41 s
G4	$15\mathrm{ms}$	$2\mathrm{m}\ 23\mathrm{s}$	$15.555\mathrm{s}$	$14.881\mathrm{s}$	$2\mathrm{m}\ 53\mathrm{s}$
G8	$14\mathrm{ms}$	$1\mathrm{m}\ 34\mathrm{s}$	$15.305\mathrm{s}$	$14.977\mathrm{s}$	$2\mathrm{m}~04\mathrm{s}$
G12	$12\mathrm{ms}$	$1\mathrm{m}\ 43\mathrm{s}$	$17.569\mathrm{s}$	$15.308\mathrm{s}$	$2\mathrm{m}\ 16\mathrm{s}$
G16	$12\mathrm{ms}$	$1\mathrm{m}\ 42\mathrm{s}$	$20.592\mathrm{s}$	$15.668\mathrm{s}$	$2\mathrm{m}\ 18\mathrm{s}$

Table 2: Execution times of the first parallel implementation

2.3 SSE and AVX in the computation of the distances

Since the attributes of each data sample to cluster are stored contiguously in memory, we decided to improve the previous solution using the SSE and AVX instructions when computing the distances.

Indeed, to compute the distance between two points, the algorithm perform the same instructions (difference and square) for every pair of attributes.

DS	S1	S2	S3	S4	Total
A2	$11\mathrm{ms}$	$46.922\mathrm{s}$	$32.362\mathrm{s}$	$21.116\mathrm{s}$	$1\mathrm{m}\ 40\mathrm{s}$
A4	$11\mathrm{ms}$	$34.329\mathrm{s}$	$35.250\mathrm{s}$	$23.791\mathrm{s}$	$1\mathrm{m}\ 33\mathrm{s}$
A8	$9\mathrm{ms}$	$27.252\mathrm{s}$	$35.007\mathrm{s}$	$23.633\mathrm{s}$	$1\mathrm{m}\ 26\mathrm{s}$
A12	$10\mathrm{ms}$	$27.328\mathrm{s}$	$37.252\mathrm{s}$	$24.346\mathrm{s}$	$1\mathrm{m}\ 29\mathrm{s}$
A16	$12\mathrm{ms}$	$28.596\mathrm{s}$	$47.285\mathrm{s}$	$26.313\mathrm{s}$	$1\mathrm{m}\ 42\mathrm{s}$
G2	$14\mathrm{ms}$	$4\mathrm{m}\ 29\mathrm{s}$	$16.732\mathrm{s}$	$14.733\mathrm{s}$	$5\mathrm{m}~00\mathrm{s}$
G4	$15\mathrm{ms}$	$2\mathrm{m}~33\mathrm{s}$	$19.175\mathrm{s}$	$16.085\mathrm{s}$	$3\mathrm{m}~08\mathrm{s}$
G8	$13\mathrm{ms}$	$1\mathrm{m}\ 39\mathrm{s}$	$19.319\mathrm{s}$	$15.894\mathrm{s}$	$2\mathrm{m}\ 15\mathrm{s}$
G12	$13\mathrm{ms}$	$1\mathrm{m}\ 43\mathrm{s}$	$18.766\mathrm{s}$	$15.457\mathrm{s}$	$2\mathrm{m}\ 18\mathrm{s}$
G16	$14\mathrm{ms}$	$1\mathrm{m}\ 39\mathrm{s}$	$21.918\mathrm{s}$	$15.986\mathrm{s}$	$2\mathrm{m}\ 17\mathrm{s}$

Table 3: Execution times of the implementation using SSE

¹https://archive.ics.uci.edu/ml/datasets/Accelerometer

DS	S1	S2	S3	S4	Total
A2	$8\mathrm{ms}$	$39.106\mathrm{s}$	$38.321\mathrm{s}$	$23.127\mathrm{s}$	$1\mathrm{m}\ 41\mathrm{s}$
A4	$10\mathrm{ms}$	$24.656\mathrm{s}$	$37.981\mathrm{s}$	$23.659\mathrm{s}$	$1\mathrm{m}\ 26\mathrm{s}$
A8	$8\mathrm{ms}$	$17.814\mathrm{s}$	$37.826\mathrm{s}$	$23.772\mathrm{s}$	$1\mathrm{m}\ 19\mathrm{s}$
A12	$8\mathrm{ms}$	$18.416\mathrm{s}$	$40.067\mathrm{s}$	$24.198\mathrm{s}$	$1\mathrm{m}\ 23\mathrm{s}$
A16	$10\mathrm{ms}$	$19.824\mathrm{s}$	$52.452\mathrm{s}$	$26.367\mathrm{s}$	$1\mathrm{m}\ 39\mathrm{s}$
G2	$13\mathrm{ms}$	$4\mathrm{m}~34\mathrm{s}$	$14.860\mathrm{s}$	$14.236\mathrm{s}$	$5\mathrm{m}~03\mathrm{s}$
G4	$15\mathrm{ms}$	$2\mathrm{m}\ 37\mathrm{s}$	$15.111\mathrm{s}$	$15.457\mathrm{s}$	$3\mathrm{m}~07\mathrm{s}$
G8	$14\mathrm{ms}$	$1\mathrm{m}\ 42\mathrm{s}$	$15.143\mathrm{s}$	$15.660\mathrm{s}$	$2\mathrm{m}\ 13\mathrm{s}$
G12	$14\mathrm{ms}$	$1\mathrm{m}\ 49\mathrm{s}$	$17.418\mathrm{s}$	$15.516\mathrm{s}$	$2\mathrm{m}\ 22\mathrm{s}$
G16	$14\mathrm{ms}$	$1\mathrm{m}\ 44\mathrm{s}$	$20.112\mathrm{s}$	$15.970\mathrm{s}$	$2\mathrm{m}\ 20\mathrm{s}$

Table 4: Execution times of the implementation using AVX

We expected this implementation to work faster than the previous one but, as the results show, this implementation generally works slightly slower than the previous one.

Investigating using the *perf* tool, we discovered that most of the time the CPU is stalled due to a high rate of cache misses (about 80% of all the data memory accesses).

2.4 A new data structure

Therefore, we decided to change the data structure holding the data samples to cluster in favour of a more a contiguous one.

In particular, we chose to organize the data as a unique heap-allocated array, where the data samples, as well as the attributes of each of them, are stored one after the other.

DS	S1	S2	S3	S 4	Total
A4 A8 A12	4 ms 4 ms 4 ms	11.770 s 7.531 s 8.277 s	$34.453 \mathrm{s}$ $35.604 \mathrm{s}$ $37.068 \mathrm{s}$	$23.509 \mathrm{s}$ $23.674 \mathrm{s}$ $24.300 \mathrm{s}$	1 m 10 s 1 m 07 s 1 m 10 s
G4 G8 G12	9 ms 8 ms 8 ms	44.391 s 37.005 s 37.132 s	14.319 s 14.162 s 15.271 s	15.880 s 15.937 s 15.902 s	1 m 15 s 1 m 07 s 1 m 08 s

Table 5: Execution times of the implementation using a contiguous data structure and SSE (2 and 16 threads tests omitted)

DS	S1	S2	S 3	S4	Total
A4 A8 A12	$4 \mathrm{ms}$ $4 \mathrm{ms}$ $4 \mathrm{ms}$	$12.462\mathrm{s}$ $8.315\mathrm{s}$ $8.368\mathrm{s}$	42.709 s 42.649 s 42.744 s	$23.633 \mathrm{s}$ $23.517 \mathrm{s}$ $23.809 \mathrm{s}$	1 m 18 s 1 m 14 s 1 m 15 s
G4 G8 G12	10 ms 8 ms 8 ms	46.830 s 38.992 s 39.331 s	14.452 s 14.454 s 15.580 s	$15.770\mathrm{s} \\ 16.222\mathrm{s} \\ 15.969\mathrm{s}$	1 m 17 s 1 m 10 s 1 m 11 s

Table 6: Execution times of the implementation using a contiguous data structure and AVX(2 and 16 threads tests omitted)

As we can see from these results, making the data samples contiguous in memory improves significantly the performance of the algorithm.

Indeed, when a thread loads one data sample, it is very likely that, with the same memory transaction, more data samples are brought to the cache. Consequently, when the thread accesses the following data samples, they are already in the cache.

2.5 Make the insertion of a new point parallel

After having made parallel the computation of the distances, we focused on parallelizing the next most expensive step of the algorithm, which is the insertion of a new point to the dendrogram (i.e., the step 3 of the pseudo-code).

However, there is no chance to make it parallel because, when the element at index i is analyzed, the algorithm requires that all the elements before i have already been analyzed.

Therefore, if we give to each thread an element to analyze in a round-robin fashion, then there will be always a thread working while all the other threads will be blocked waiting for the active thread to complete its work.

2.6 Make the rearrangement of the structure of the dendrogram parallel

Therefore, we decided to try to parallelize the third most expensive step of the algorithm, which is the rearrangement of the structure of the dendrogram after a new point is added (i.e., the step 4 of the pseudo-code).

Since each iteration of the for loop is independent from the others, making it parallel is almost trivial if we use the *OpenMP* library.

Like the previous implementation, data is supplied as a unique heap-allocated array.

For space reasons, we omitted the results of the implementation when using SSE instructions.

DS	S1	S2	S3	S4	Total
A4	$4\mathrm{ms}$	$10.599\mathrm{s}$	$42.538\mathrm{s}$	$8.939\mathrm{s}$	$1\mathrm{m}~02\mathrm{s}$
A8	$4\mathrm{ms}$	$5.836\mathrm{s}$	$43.255\mathrm{s}$	$5.659\mathrm{s}$	$54.760\mathrm{s}$
A12	$4\mathrm{ms}$	$6.895\mathrm{s}$	$45.326\mathrm{s}$	$4.489\mathrm{s}$	$56.720\mathrm{s}$
G4	$9\mathrm{ms}$	$46.341\mathrm{s}$	$15.961\mathrm{s}$	$6.888\mathrm{s}$	$1\mathrm{m}~09\mathrm{s}$
G8	$8\mathrm{ms}$	$38.337\mathrm{s}$	$16.110\mathrm{s}$	$3.792\mathrm{s}$	$58.254\mathrm{s}$
G12	$8\mathrm{ms}$	$39.039\mathrm{s}$	$17.603\mathrm{s}$	$3.408\mathrm{s}$	$1\mathrm{m}~00\mathrm{s}$

Table 7: Execution times of the implementation executing in parallel the computation of the distances (using threads and AVX instructions) as well as the rearrangement of the structure of the dendrogram (2 and 16 threads tests omitted)

2.7 Devising a new algorithm

The last most expensive step of the algorithm is the initialization π and λ (i.e., the step 1 of the pseudo-code). However, as the results show, even if we make it parallel, we will gain only few milliseconds.

Therefore, to improve further the performance of the algorithm, we tried devising a new one. This new algorithm builds incrementally the dendrogram like the pseudo-code does, but in a different way.

In particular, when the algorithm adds to the dendrogram a new data sample d, it first computes the closest sample c, and their distance l. Then, the algorithm traverses the path on the dendrogram from the leaf representing c to the root.

Indeed, since c is the closest sample to d, then d must be linked to one of the clusters containing c, and such a cluster can be found only on that path. In particular, this cluster is represented by the last edge ε in the path with height smaller

than l. Therefore, the algorithm just needs to add a new edge linking ε to d, and then to link the new edge to the parent of ε .

Then, the algorithm continues traversing the path so to possibly update the height of the remaining edges, because adding d to a cluster may have made it closer to the cluster it joins to.

Representing the dendrogram as a binary tree using a linked structure, each edge of the path to traverse is just a pair of nodes. Since the dendrogram is not rearranged, each of these pairs can be analyzed independently. This implies that several threads may analyze such pairs in parallel requiring almost no synchronization. One of these threads will add the new edge, while all the others will possibly update the heights of the edges.

However, there are some cases where the dendrogram is actually rearranged. Indeed, suppose that the cluster $\{a,b\}$ joins the cluster $\{e,f\}$. If the data sample to add d is closer to b, then it will be added to the first cluster, and this may make the data sample e closer to the cluster $\{a,b,d\}$ more than to f.

Thus, adding the data sample d to the dendrogram may require to rearrange some of the edges. This implies that the edges in the path to traverse may change while the threads are traversing it. To avoid inconsistencies, we require much more synchronization between the threads, which may force them to work sequentially, hence reducing the performance.

Therefore, we abandoned this algorithm, and we tried to implement some micro-optimizations so to improve the performance the algorithm we have used so far.

2.8 Partial sums micro-optimization

To compute the distance between two data samples, the previous implementations using SSE and AVX instructions first load from memory 2 and 4 attributes respectively. Then, they compute the square of the pairwise attributes difference, and they accumulate the result on a stack-allocated variable, which requires to horizontally sum all the values in the extended register. All these four operations are repeated until no more attributes are left.

The first micro-optimization we have implemented accumulates the partial sums in an extended register instead of in a stack-allocated variable, in order to perform the horizontal sum only once at the end of the computation, and to avoid writing to memory each partial result.

The new implementation combines this micro-optimization with all the parallelization strategies we have implemented so far. The data is still supplied as a unique heap-allocated array.

Also in this case, for space reasons, we omitted the results of the implementation when using SSE instructions.

DS	S1	S2	S3	S4	Total
A4	$4\mathrm{ms}$	$10.174\mathrm{s}$	$41.862\mathrm{s}$	$8.887\mathrm{s}$	$1\mathrm{m}~01\mathrm{s}$
A8	$4\mathrm{ms}$	$5.820\mathrm{s}$	$42.804\mathrm{s}$	$5.592\mathrm{s}$	$54.227\mathrm{s}$
A12	$4\mathrm{ms}$	$6.868\mathrm{s}$	$45.697\mathrm{s}$	$4.526\mathrm{s}$	57.102 s
G4	11 ms	$41.854\mathrm{s}$	$17.229\mathrm{s}$	$5.854\mathrm{s}$	$1\mathrm{m}~05\mathrm{s}$
G8	$10\mathrm{ms}$	$37.064\mathrm{s}$	$16.846\mathrm{s}$	$3.590\mathrm{s}$	$57.515\mathrm{s}$
G12	$9\mathrm{ms}$	$37.005\mathrm{s}$	$17.897\mathrm{s}$	$3.414\mathrm{s}$	$58.330\mathrm{s}$

Table 8: Execution times of the first micro-optimized implementation using AVX (2 and 16 threads tests omitted)

2.9 Square roots micro-optimization

The last micro-optimization we have implemented extends the previous one by using the square of the distances instead of the distances themselves. The computation of the square roots is performed only at end of the execution of the algorithm and only on the computed values of λ .

Indeed, ignoring possible rounding errors, this solution works because the following property holds for non-negative values (as the distances are):

$$\sqrt{n^2} > \sqrt{m^2} \quad \iff \quad n^2 > m^2$$

DS	S1	S2	S 3	S4	S5	Total
A2	$4\mathrm{ms}$	$10.961\mathrm{s}$	$37.824\mathrm{s}$	$12.933\mathrm{s}$	$190\mu\mathrm{s}$	$1\mathrm{m}02\mathrm{s}$
A4	$4\mathrm{ms}$	$6.641\mathrm{s}$	$41.826\mathrm{s}$	$8.989\mathrm{s}$	$112\mu\mathrm{s}$	$57.466\mathrm{s}$
A8	$4\mathrm{ms}$	$3.903\mathrm{s}$	$42.693\mathrm{s}$	$5.592\mathrm{s}$	$58\mu\mathrm{s}$	$52.199\mathrm{s}$
A12	$5\mathrm{ms}$	$4.603\mathrm{s}$	$45.701\mathrm{s}$	$4.549\mathrm{s}$	$69\mu\mathrm{s}$	$54.864\mathrm{s}$
A16	$5\mathrm{ms}$	$4.067\mathrm{s}$	$51.38\mathrm{s}$	$4.222\mathrm{s}$	$52\mu\mathrm{s}$	$59.340\mathrm{s}$
G2	$9\mathrm{ms}$	$1\mathrm{m}\ 5\mathrm{s}$	$15.592\mathrm{s}$	$9.995\mathrm{s}$	$113\mu\mathrm{s}$	$1\mathrm{m}30\mathrm{s}$
G4	$9\mathrm{ms}$	$43.109\mathrm{s}$	$16.769\mathrm{s}$	$6.865\mathrm{s}$	$66\mu\mathrm{s}$	$1\mathrm{m}~07\mathrm{s}$
G8	$9\mathrm{ms}$	$36.077\mathrm{s}$	$16.618\mathrm{s}$	$3.834\mathrm{s}$	$35\mu\mathrm{s}$	$56.544\mathrm{s}$
G12	$9\mathrm{ms}$	$36.747\mathrm{s}$	$17.697\mathrm{s}$	$3.430\mathrm{s}$	$48\mu\mathrm{s}$	$57.890\mathrm{s}$
G16	$5\mathrm{ms}$	$36.383\mathrm{s}$	$20.554\mathrm{s}$	$3.100\mathrm{s}$	$38\mu\mathrm{s}$	$1\mathrm{m}01\mathrm{s}$

Table 9: Execution times of the second micro-optimized implementation using SSE

DS	S1	S2	S 3	S 4	S5	Total
A2	$4\mathrm{ms}$	$11.439\mathrm{s}$	$37.881\mathrm{s}$	$13.146\mathrm{s}$	$216\mu\mathrm{s}$	$1\mathrm{m}02\mathrm{s}$
A4	$4\mathrm{ms}$	$7.106\mathrm{s}$	$42.571\mathrm{s}$	$8.888\mathrm{s}$	$115\mu\mathrm{s}$	$58.576\mathrm{s}$
A8	$4\mathrm{ms}$	$4.147\mathrm{s}$	$42.281\mathrm{s}$	$5.607\mathrm{s}$	$61\mu\mathrm{s}$	$52.046\mathrm{s}$
A12	$5\mathrm{ms}$	$5.271\mathrm{s}$	$44.500\mathrm{s}$	$4.595\mathrm{s}$	$71\mu\mathrm{s}$	$54.376\mathrm{s}$
A16	$5\mathrm{ms}$	$4.386\mathrm{s}$	$50.834\mathrm{s}$	$4.346\mathrm{s}$	$52\mu\mathrm{s}$	$59.578\mathrm{s}$
G2	$9\mathrm{ms}$	$53.895\mathrm{s}$	$16.303\mathrm{s}$	$10.436\mathrm{s}$	$124\mu\mathrm{s}$	$1\mathrm{m}21\mathrm{s}$
G4	$10\mathrm{ms}$	$41.312\mathrm{s}$	$16.961\mathrm{s}$	$6.453\mathrm{s}$	$67\mu\mathrm{s}$	$1\mathrm{m}~05\mathrm{s}$
G8	$8\mathrm{ms}$	$37.104\mathrm{s}$	$16.829\mathrm{s}$	$3.715\mathrm{s}$	$35\mu\mathrm{s}$	$57.663\mathrm{s}$
G12	$8\mathrm{ms}$	$37.015\mathrm{s}$	$17.697\mathrm{s}$	$3.430\mathrm{s}$	$47\mu\mathrm{s}$	$58.427\mathrm{s}$
G16	$9\mathrm{ms}$	$37.781\mathrm{s}$	$20.467\mathrm{s}$	$3.212\mathrm{s}$	$38\mu\mathrm{s}$	$1\mathrm{m}01\mathrm{s}$

Table 10: Execution times of the second micro-optimized implementation using AVX

In the previous tables, S5 reports the time taken to compute the square roots only.

2.10 A new data structure for the sequential implementation

To compute the speed-ups of all the previous parallel implementations, we need to also execute the sequential implementation with the data supplied as a unique heap-allocated array.

_	\mathbf{DS}	S1	S2	S3	S4	Total
	A	$4\mathrm{ms}$	$27.978\mathrm{s}$	$28.764\mathrm{s}$	$20.663\mathrm{s}$	$1\mathrm{m}\ 17\mathrm{s}$
	G	$16\mathrm{ms}$	$2\mathrm{m}\ 24\mathrm{s}$	$16.307\mathrm{s}$	$13.705\mathrm{s}$	$2\mathrm{m}~54\mathrm{s}$

Table 11: Execution times of the sequential implementation using a contiguous data structure to store the data samples

As we can see, this memory layout drastically improves the performance of the sequential implementation, even without using any parallelization technique.

Indeed, storing all the data samples contiguously in memory greatly improves the use of the cache, as we observed using the *perf* tool.

2.11 Data layouts comparison

Finally, we report the results of the tests we have performed on the last parallel implementation of the clustering algorithm, but supplying the data as a std::vector<double *> (i.e., a vector of heap allocated arrays each holding the attributes of one data sample) like in the first parallel implementation.

In the following tables, S5 reports the time taken to compute the square roots only.

DS	S1	S2	S 3	S 4	S5	Total
A2	$9\mathrm{ms}$	$42.708\mathrm{s}$	$41.007\mathrm{s}$	$17.475\mathrm{s}$	$170\mu\mathrm{s}$	$1\mathrm{m}41\mathrm{s}$
A4	$6\mathrm{ms}$	$34.021\mathrm{s}$	$42.132\mathrm{s}$	$9.814\mathrm{s}$	$97\mu\mathrm{s}$	$1\mathrm{m}\ 26\mathrm{s}$
A8	$6\mathrm{ms}$	$29.379\mathrm{s}$	$42.149\mathrm{s}$	$6.243\mathrm{s}$	$51\mu\mathrm{s}$	$1\mathrm{m}18\mathrm{s}$
A12	$6\mathrm{ms}$	$29.705\mathrm{s}$	$43.549\mathrm{s}$	$5.491\mathrm{s}$	$72\mu\mathrm{s}$	$1\mathrm{m}18\mathrm{s}$
A16	$7\mathrm{ms}$	$30.903\mathrm{s}$	$48.340\mathrm{s}$	$5.206\mathrm{s}$	$54\mu\mathrm{s}$	$1\mathrm{m}\ 24\mathrm{s}$
G2	$9\mathrm{ms}$	$2\mathrm{m}52\mathrm{s}$	$17.576\mathrm{s}$	$10.547\mathrm{s}$	$118\mu\mathrm{s}$	$3\mathrm{m}20\mathrm{s}$
G4	$9\mathrm{ms}$	$1\mathrm{m}47\mathrm{s}$	$19.144\mathrm{s}$	$6.986\mathrm{s}$	$66\mu\mathrm{s}$	$2\mathrm{m}\ 13\mathrm{s}$
G8	$9\mathrm{ms}$	$1\mathrm{m}\ 24\mathrm{s}$	$18.963\mathrm{s}$	$4.404\mathrm{s}$	$35\mu\mathrm{s}$	$1\mathrm{m}48\mathrm{s}$
G12	$10\mathrm{ms}$	$1\mathrm{m}31\mathrm{s}$	$20.374\mathrm{s}$	$4.026\mathrm{s}$	$49\mu\mathrm{s}$	$1\mathrm{m}56\mathrm{s}$
G16	$10\mathrm{ms}$	$1\mathrm{m}31\mathrm{s}$	$22.532\mathrm{s}$	$3.765\mathrm{s}$	$39\mu\mathrm{s}$	$1\mathrm{m}57\mathrm{s}$

Table 12: Execution times of the second micro-optimized implementation using SSE and std::vector<double*>

DS	S1	S2	S3	S4	S5	Total
A2	$7\mathrm{ms}$	$33.418\mathrm{s}$	$39.571\mathrm{s}$	$15.797\mathrm{s}$	$181\mu\mathrm{s}$	$1\mathrm{m}\ 29\mathrm{s}$
A4	$7\mathrm{ms}$	$20.032\mathrm{s}$	$40.853\mathrm{s}$	$9.820\mathrm{s}$	$95\mu\mathrm{s}$	$1\mathrm{m}11\mathrm{s}$
A8	$7\mathrm{ms}$	$13.387\mathrm{s}$	$42.220\mathrm{s}$	$5.761\mathrm{s}$	$61\mu\mathrm{s}$	$1\mathrm{m}01\mathrm{s}$
A12	$8\mathrm{ms}$	$16.225\mathrm{s}$	$44.615\mathrm{s}$	$4.966\mathrm{s}$	$68\mu\mathrm{s}$	$1\mathrm{m}~05\mathrm{s}$
A16	$9\mathrm{ms}$	$19.212\mathrm{s}$	$50.916\mathrm{s}$	$4.934\mathrm{s}$	$52\mu\mathrm{s}$	$1\mathrm{m}15\mathrm{s}$
G2	$14\mathrm{ms}$	$2\mathrm{m}\ 27\mathrm{s}$	$16.368\mathrm{s}$	$10.641\mathrm{s}$	$126\mu\mathrm{s}$	$2\mathrm{m}54\mathrm{s}$
G4	$13\mathrm{ms}$	$1\mathrm{m}35\mathrm{s}$	$17.435\mathrm{s}$	$7.007\mathrm{s}$	$69\mu\mathrm{s}$	$1\mathrm{m}59\mathrm{s}$
G8	$13\mathrm{ms}$	$1\mathrm{m}\ 22\mathrm{s}$	$17.152\mathrm{s}$	$4.432\mathrm{s}$	$35\mu\mathrm{s}$	$1\mathrm{m}44\mathrm{s}$
G12	$13\mathrm{ms}$	$1\mathrm{m}30\mathrm{s}$	$20.023\mathrm{s}$	$4.109\mathrm{s}$	$53\mu\mathrm{s}$	$1\mathrm{m}54\mathrm{s}$
G16	$14\mathrm{ms}$	$1\mathrm{m}\ 29\mathrm{s}$	$21.576\mathrm{s}$	$3.801\mathrm{s}$	$42\mu\mathrm{s}$	$1\mathrm{m}55\mathrm{s}$

Table 13: Execution times of the second micro-optimized implementation using AVX and std::vector<double*>

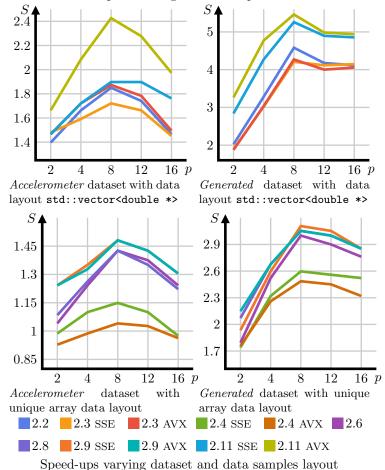
2.12 Speed-up

When storing the data samples in a std::vector<double*>, we reached a speed-up of 2.43 for the *Accelerometer* dataset and 5.46 for the *Generated* one, both executing the AVX parallel implementation described in 2.11 with 8 threads.

Instead, when storing the data samples in a unique array, we obtained a speed-up of 1.48 for the *Accelerometer* dataset exeuting the AVX parallel implementation described in 2.9 with 8 threads, while we obtained a speed-up of 3.08 for the *Generated* dataset executing the SSE parallel implementation described in 2.9 with 8 threads.

All the parallel techniques we have applied achieve better speed-ups when the data samples are composed of many attributes, since we exploit the parallelization of the computation of the distances, which is the most expensive step of the algorithm. Moreover, we get the highest speed-ups when using 8 threads, since we are exploiting exactly all the available CPUs.

In conclusion, among all the scheduling policies offered by the *OpenMP* library, we have found that the **static** one is the most effective in parallelizing the **for** loops.



3 How to use the library

We organized all the sequential and parallel implementations of the clustering algorithm into two different classes, namely the SequentialClustering and ParallelClustering classes.

All these implementations are able to process data samples with any number of attributes, as long it is the same for all the samples.

The main-sample.cpp file in the test/src directory contains an example illustrating how to use the library.

3.1 ParallelClustering class

This class, defined in ParallelClustering.h header file, offers several utility methods and constants allowing the programmer to easily deal with aligned memory. In particular, the computeSseDimension and computeAvxDimension static methods allow to compute the number of double elements each data sample must be composed of, taking into account also possible paddings.

However, the cluster static method is the most important one offered by this class. It clusters the given data samples using the specified distance computation algorithm (see 3.1.3 for the list of available algorithms), which is specified as a template argument so to compile only the code needed to execute the requested algorithm. The method allows also to specify the number of threads to use in each of the steps that can be executed in parallel, namely:

- 1. The computation of the distance between two data samples;
- 2. The operations needed to rearrange the structure of the dendrogram after a new point is added (i.e., the step 4 of the pseudo-code);
- 3. The computation of the square roots of the distances stored in λ .

Note that the parallelization of such steps can be enabled or disabled at compile time by using the template parameters of the ParallelClustering class.

3.1.1 Data samples memory layout

The cluster static method allows a certain flexibility in the memory layout of the data samples. In particular, the method accepts two main organizations:

- 1. A contiguous region of memory, where the data samples, as well as the attributes of each sample, are stored one after the other.
 - The method accepts either an iterator iterating over that region of memory, or directly the data structure holding it. In this case, the cluster method takes care of extracting the iterator by calling the begin method or, if present, the cbegin one;
- 2. A data layout organized in two levels.

The first level is merely a container that holds the data samples to cluster (or the iterators iterating over them). This container is required to be randomly accessible, but it is not required to be contiguous in memory.

Each element of the second level, instead, is a data structure (or an iterator iterating over it) holding a contiguous region of memory that stores the attributes of one data sample.

For both levels, the cluster method accepts either the iterator over the data structure, or the data structure itself. In this last case, the method takes care of calling the begin method, or the cbegin one if present, to obtain an iterator iterating over the data structure.

3.1.2 π and λ memory layouts

The cluster static method allows also a certain flexibility in the memory layout of π and λ .

In particular, each of them can be any randomly accessible data structure, or an iterator iterating over it. In the former case, the method takes care of calling begin method to retrieve the random access iterator iterating over the specified data structure.

Both the data structures holding the values of π and λ must be properly sized so that all the values computed by the cluster method can fit into them.

3.1.3 Distance computation algorithms

The cluster static method allows the programmer to use different algorithms to compute the distance between two data samples. In particular:

- CLASSICAL computes the Euclidean distances without using any SIMD instruction;
- SSE and AVX compute the distances using SSE and AVX instructions respectively;
- SSE_OPTIMIZED and AVX_OPTIMIZED compute the distances using SSE and AVX instructions respectively, and they keep the partial sums in an extended register instead of storing them into a stack-allocated variable (see 2.8);
- SSE_OPTIMIZED_NO_SQUARE_ROOT and AVX_OPTIMIZED_NO_SQUARE_ROOT, which act like SSE_OPTIMIZED and AVX_OPTIMIZED respectively, but they compute the square of the distances, without applying the square root operation (see 2.9).

All these constants are defined in the DistanceComputers enumeration, defined in the DistanceComputers.h header file.

3.2 SequentialClustering class

This class, defined in the SequentialClustering.h header file, provides only one method, the cluster static method, which executes the sequential implementation of the clustering algorithm on the specified data samples.

3.2.1 Data samples memory layout

The cluster static method supports the same data layouts as the cluster method of the parallel implementations (see 3.1.1).

In addition, if the specified data structure (or iterator) is organized in two levels, then this method does not require the first level to be randomly accessible, but only to be at least input iterable (or an input iterator).

3.2.2 π and λ memory layouts

The cluster static method supports exactly the same data layouts for both π and λ as the cluster method of the parallel implementations (see 3.1.2).

3.3 Timer class

If the TIMERS macro is defined, both the sequential and parallel implementations of the clustering algorithm will measure, using a timer, the time taken to execute each step of the algorithm.

In particular, each step is measured by a different timer with a specific identifier, where 0 measures the time taken to execute all the service operations, while 1 to 4 measure the time taken to execute the corresponding steps of the pseudo-code.

In addition, only for the parallel implementations run with the distance computation algorithms

 $SSE_OPTIMIZED_NO_SQUARE_ROOT$ and

AVX_OPTIMIZED_NO_SQUARE_ROOT, the timer with identifier 5 measures the time taken to compute the square root of each value stored in λ .

The Timer class, defined in the Timer.h header file, offers the print and printTotal static methods to print to the console the times measured by each of these timers.