Homework 2: Operational Semantics for WHILE

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```
e ::=
                                                                     Expressions
                                                             variables/addresses
            \boldsymbol{x}
                                                                            values
            v
                                                                      assignment
            x := e
                                                          sequential expressions
            e; e
                                                               binary operations
            e op e
            e\ AND\ e
                                                                 AND operation
            e\ OR\ e
                                                                   OR operation
            e\ NOT\ e
                                                                 NOT operation
            \mathtt{if}\ e\ \mathtt{then}\ e\ \mathtt{else}\ e
                                                         conditional\ expressions
                                                               while expressions
            while (e) e
v ::=
                                                                           Values
            i
                                                                   integer values
            b
                                                                  boolean values
            + | - | * | / | > | >= | < | <=
                                                                Binary operators
op ::=
```

Figure 1: The WHILE language

```
Runtime Syntax:
                                                              \sigma \in Store
                                                                                                          variable \rightarrow v
                                             e, \sigma \to e', \sigma'
Evaluation Rules:
                                                                      x \in domain(\sigma) \quad \sigma(x) = v
                     [SS-ACCESS-RED]
                                                                                    x, \sigma \to v, \sigma
                                                                               e,\sigma \to e',\sigma'
            [SS-ASSIGN-CONTEXT]
                                                                     \overline{x := e, \sigma \to x := e', \sigma'}
                      [SS-ASSIGN-RED]
                                                                     \overline{x:=v,\sigma\to v,\sigma[x:=v]}
                                                                     \frac{e_1, \sigma \to e'_1, \sigma'}{e_1; e_2, \sigma \to e'_1; e_2, \sigma'}
                  [SS-SEQ-CONTEXT]
                            [SS-SEQ-RED]
                                                                     v; e, \sigma \to e, \sigma
                                                                      \cfrac{e_1,\sigma\to e_1',\sigma'}{\texttt{if }e_1 \texttt{ then }e_2 \texttt{ else }e_3,\sigma\to \texttt{if }e_1' \texttt{ then }e_2 \texttt{ else }e_3,\sigma'}
                     [SS-IF-CONTEXT]
                      [SS-IFTRUE-RED]
                                                                      if true then e_1 else e_2, \sigma \rightarrow e_1, \sigma
                     [SS-IFFALSE-RED]
                                                                      \overline{\text{if false then }}e_1 \; 	ext{else } e_2, \sigma 
ightarrow e_2, \sigma
                                                                                e_1, \sigma \to e_1', \sigma'
                [SS-OP-CONTEXT-1]
                                                                      \overline{e_1 \ op \ e_2, \sigma \rightarrow e_1' \ op \ e_2, \sigma'}
                                                                             e, \sigma \rightarrow e', \sigma'
                [SS-OP-CONTEXT-2]
                                                                      v \ op \ e, \sigma \xrightarrow{\longrightarrow} v \ op \ e', \sigma'
                                                                          v = v_1 \ op \ v_2
                              [SS-OP-RED]
                                                                      v_1 \ op \ v_2, \sigma \to v, \sigma
                                                                     \frac{e_1, \sigma \to e'_1, \sigma'}{e_1 \ AND \ e_2, \sigma \to e'_1 \ AND \ e_2, \sigma'}
                 [SS-AND-CONTEXT]
                                                                                e, \sigma \to e', \sigma'
                        [SS-AND-RED-1]
                                                                      true AND \ e, \sigma \rightarrow e', \sigma'
                        [SS-AND-RED-2]
                                                                      \overline{\mathtt{false}\ AND\ e, \sigma \to \mathtt{false}, \sigma'}
                                                                     \frac{e_1, \sigma \to e'_1, \sigma'}{e_1 \ OR \ e_2, \sigma \to e'_1 \ OR \ e_2, \sigma'}
                    [SS-OR-CONTEXT]
                                                                     \frac{e,\sigma \rightarrow e',\sigma'}{\texttt{false} \ or \ e,\sigma \rightarrow e',\sigma'}
                          [SS-OR-RED-1]
                          [SS-OR-RED-2]
                                                                      \overline{\text{true } OR \ e, \sigma \rightarrow \text{true}, \sigma'}
                                                                      \frac{e, \sigma \to e', \sigma'}{NOT \ e, \sigma \to NOT \ e', \sigma'}
                 [SS-NOT-CONTEXT]
                        [SS-NOT-RED-1]
                                                                      \overline{NOT \; \mathsf{true}, \sigma \to \mathsf{false}, \sigma'}
                        [SS-NOT-RED-2]
                                                                      \overline{NOT} false, \sigma \to \mathtt{true}, \sigma'
                                [SS-WHILE]
                                                                      while (e_1) e_2, \sigma \rightarrow if e_1 then e_2; while (e_1) e_2 else false, \sigma
```

Figure 2: Small-step semantics for WHILE