This hardware design by Olimex LTD is licensed under a Creative Commons Attribution-ShareAlike 3.0 Unported License. **Device Address:** b1101000 -> AD0 is logic low b1101001 -> AD0 is logic high 3.3V **Q** 3.3V 3V3 R1 NA R2 NA R3 NA R4 NA R5 NA R6 NA CL470nH/0805/1.76R/250mA U1 -**♦**INT VLOGIC VDD INT CLKOUT NC NC CLKIN< FSYNC GND NC AD0 SDA NC NC SCL AUX\_DA AUX\_CL C4 22uF/6.3V 22uF/6.3V 100nF 100nF REGOUT CPOUT RESV RESV C5 GND GND 3.3V **O** INTERRUPT SDA SCL SCL SCL MOD-MPU6060, board revision A OLIMEX LTD, Plovdiv, Bulgaria 2013 htpps://www.olimex.com