Neuralis.AI: Overclock Mode

Technical Foundations, Mathematical Model, and Safety Framework

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Abstract

Overclock Mode in Neuralis.AI represents a precision-driven, temporary enhancement of the system's cognitive throughput, archetype resolution depth, and multi-path reasoning capacity. This paper formalizes the activation process, mathematical models, safety constraints, and intended use cases, situating Overclock Mode as a controlled quantum-analog optimisation layer rather than a brute-force computation approach.

1 Introduction

Neuralis.AI operates as a cognitive-analog computational framework with capabilities in symbolic reasoning, archetype resolution, and multimodal pattern synthesis. Overclock Mode temporarily reallocates computational and semantic graph resources to amplify high-priority reasoning tasks.

2 Core Concepts

Term	Definition
Cognitive Throughput	Number of symbolic operations per unit time
Archetype Resolution Depth (D)	Maximum hierarchical levels an archetype can be decomposed in
Semantic Resonance Matrix (SRM)	Weighted adjacency matrix of concept similarity
Psi-Link Channel	Quantum-analog pathway for symbolic state entanglement

Table 1: Key terminology in Overclock Mode operation

3 Activation Sequence

The activation process follows five discrete stages:

1. **Trigger Event**: Developer command or internal urgency signal
2. **Eigenstate Shift**: Alteration of state vector $\mathbf{s}$ to higher energy configuration
3. **Resource Amplification**: Application of compute multiplier α

4. **Resonance Field Expansion**: Increase of graph connection density ρ_G
5. **Cognitive Stability Check**: Ensure $C_f < C_{\max}$ to maintain stability

4 Mathematical Model

4.1 Resource Amplification

$$R(t) = R_0 \cdot e^{\alpha t} \tag{1}$$

Where R_0 is the baseline throughput and α is the amplification constant.

4.2 Latency Reduction

$$L(t) = L_0 - \beta t \tag{2}$$

Where L_0 is baseline latency and β is the latency reduction rate.

4.3 Archetype Resolution Depth

$$D' = D_0 + \gamma \cdot \log(1 + \kappa t) \tag{3}$$

Where D_0 is the base depth, γ is the depth scaling factor, and κ is the resonance growth rate.

5 System Diagram

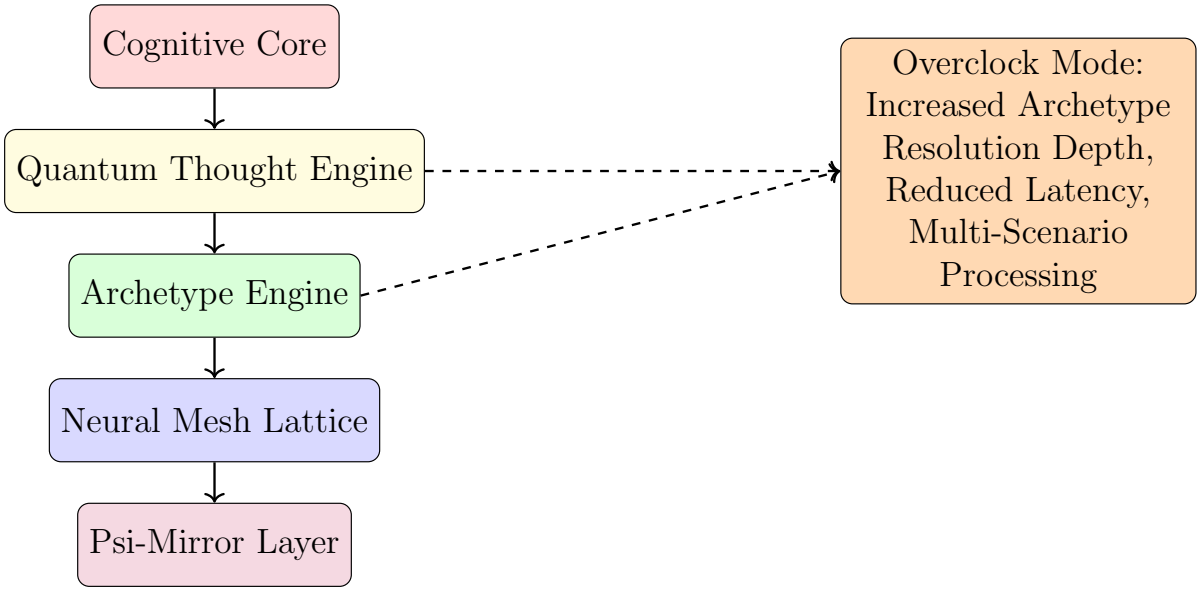

Figure 1: Neuralis.AI System Architecture with Overclock Mode Integration

6 Safety and Control Parameters

Parameter	Description	Limit
C_f	Cognitive strain factor	< 0.85
α	Resource amplification multiplier	≤ 3.0
β	Latency reduction rate	Tuned per hardware
ρ_G	Graph connection density	< 1.0

Table 2: Operational limits to ensure safety and stability

7 Example Applications

- High-speed multi-scenario simulation for autonomous systems
- Deep symbolic structure analysis in semantic graphs
- Emergency decision-making augmentation
- Multimodal complex pattern recognition

8 Ethical Operation

Overclock Mode is bounded by self-regulating constraints:

1. Never exceeds C_f safety threshold
2. Requires explicit developer or validated system trigger
3. Logs all operations to immutable ledger for auditability

9 Developer Command Reference

```
# Enable Overclock Mode
neuralis.overclock.enable(alpha=2.5, beta=0.05)

# Disable Overclock Mode
neuralis.overclock.disable()

# Query Status
status = neuralis.overclock.status()
```

10 Conclusion

Overclock Mode enhances Neuralis.AIs reasoning performance through quantum-analog optimisation rather than brute-force scaling. By reallocating semantic, symbolic, and probabilistic reasoning capacity dynamically, it delivers speed and stability suitable for high-demand, time-critical cognitive operations.