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| CPU | Central processing unit, the electronic circuitry within a computer that executes instructions that make up a computer program. |
| ALU | Arithmetic logic unit, is responsible for any arithmetic or logic processing that might be needed when a program is running. |
| Accumulator | general-purpose register that stores a value before and after the execution of an  instruction by the ALU |
| Registers | The storage components which allow very short access times and has limited storage capacity. |
| Current instruction register | The register that stores the current instruction while it is being decoded and executed |
| Index register | The register that stores a value, only used for indexed addressing |
| Memory address register | The register that shores the address of a memory location which is about to have a value read from or written to |
| Memory data register | The register that stores data that has just been read from memory or is just about to be written to memory |
| Program counter | The register that stores the address of where the next instruction is to be read from |
| Status register | The register that contains individual bits that are either set or cleared |
| flag | The bit is set to 1 if a condition if detected. |
| Address bus | a component that carries an address to the memory contro ller to identify a location in memory which is to be read from or written to |
| data bus | Data carrier, a component that carries data to and from the processor |
| Word | a small number of bytes handled as a unit by the computer system |
| The control bus | bidirectional bus which transmits a signal from the control unit to any other system component or transmits a signal to the control un it. |
| universal serial bus | USB, The plug-and-play concept standard |
| The fetch-execute cycle |  |
| Interrupt handling | is in itiated by loading its start address into the program counter. When t he ISR program has been executed there needs to be an immediate check to see if further interrupts need handling. If there are none, the safely stored contents of the registers are restored to t he CPU and the originally runn ing program is resumed. |