



University School of Automation and Robotics
GURU GOBIND SINGH INDRAPRASTHA UNIVERSITY
East Delhi Campus, Surajmal Vihar
Delhi - 110092



Paper Code: ARI 253	Paper: Basic Electronics Lab	L	TIP	Credits
Paper ID: 025253		-	2	1

1. To study and plot the characteristics of a junction diode.
2. To study Zener diode as a voltage regulator.
3. To study diode based clipping and clamping circuits.
4. To study half wave, full wave and bridge rectifier with filters.
5. To study the input and output characteristics of a transistor, JFET and MOSFET in its various configurations.
6. Realize all gates using NAND & NOR gates
7. Realize Half Adder, Full Adder, Half subtracter, Full subtracter
8. Realize a Serial and parallel Adder
9. Realize Master-Slave J K Flip-Flop using NAND/NOR gates
10. Realize Multiplexer and De-Multiplexer