CS & IT



ENGINEERING

DIGITAL LOGIC

Combinational Circuit

Lecture No. 05



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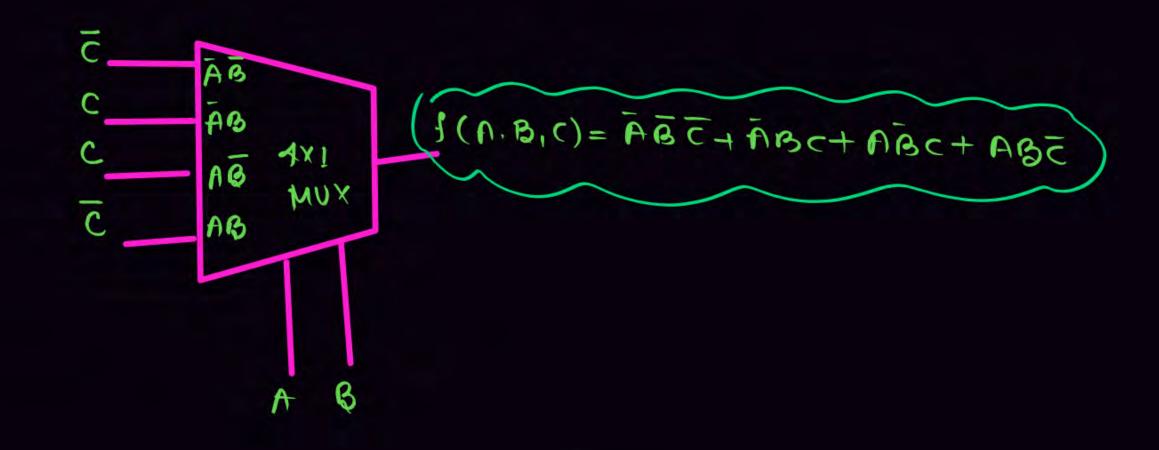
TOPICS TO BE COVERED **01** MULTIPLEXER

02 QUESTION PRACTICE

03 DISCUSSION

TYPE (5) IMPLEMENTATION OF FUNCTION :>

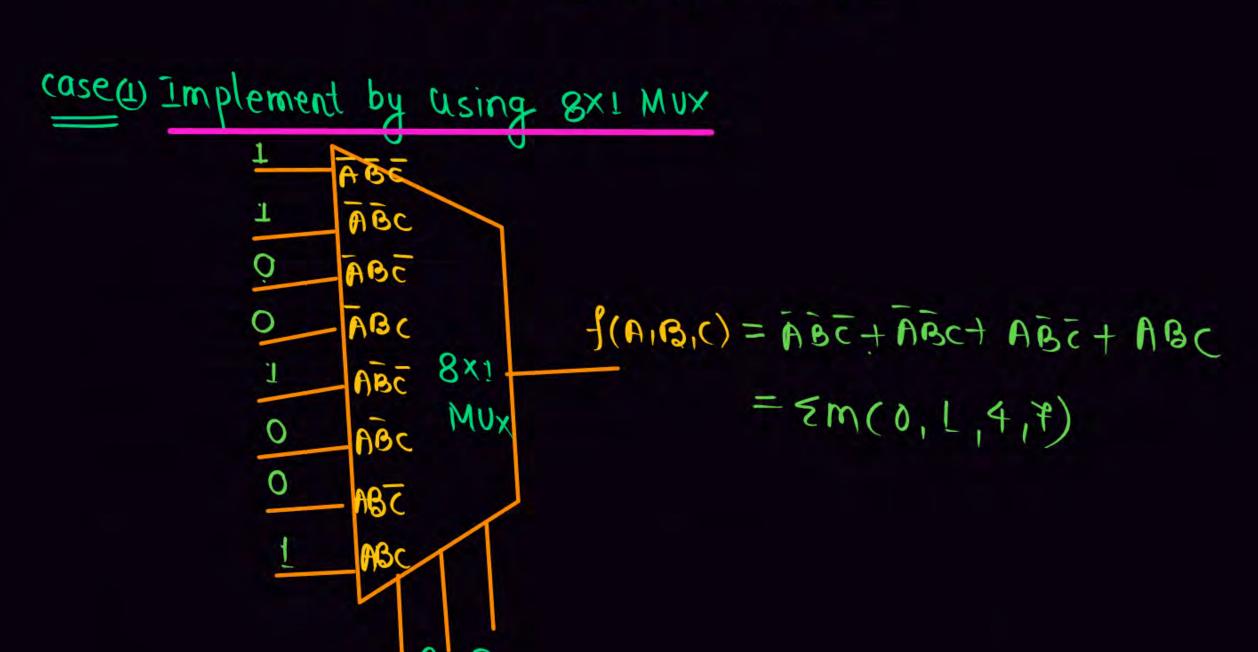






$$E \times 1 \quad f(A_1B_1C) = Em(o,1,4,7)$$

$$= \overline{ABC} + \overline{ABC} + \overline{ABC} + \overline{ABC}$$

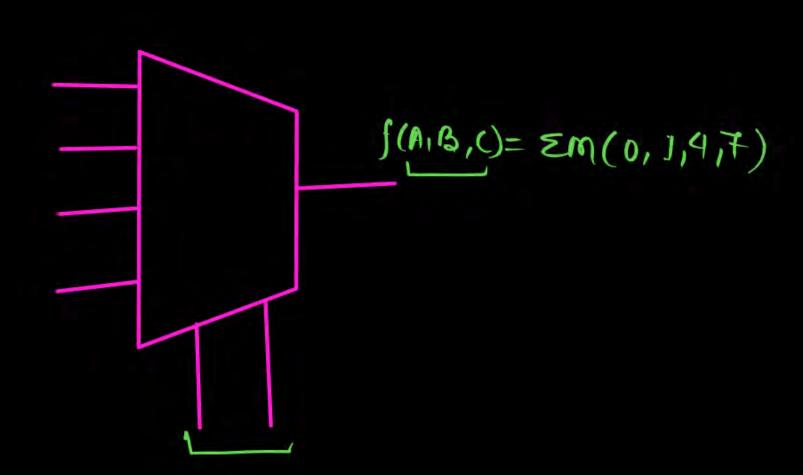


By using 4 × 1 MUX



$$f(A, B, C) = \bar{A}\bar{B}\bar{C} + \bar{A}\bar{B}C + A\bar{B}\bar{C} + ABC = \text{Im}(0,1,4,7)$$

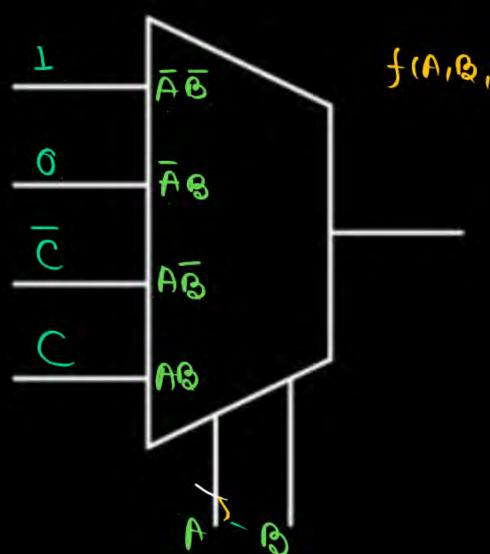
- AB as a select line
- BC as a select line
- 3. AC as a select line



AB is select line

$f(A, B, C) = \bar{A}\bar{B}\bar{C} + \bar{A}\bar{B}C + A\bar{B}\bar{C} + ABC$ $= \leq m(0, 1, 4, 7)$





$$f(A,Q,C) = \overline{A}\overline{B} \cdot 1 + A\overline{B}\overline{C} + ABC$$

$$= \overline{A}\overline{B}(\overline{C} + C) + A\overline{B}\overline{C} + ABC$$

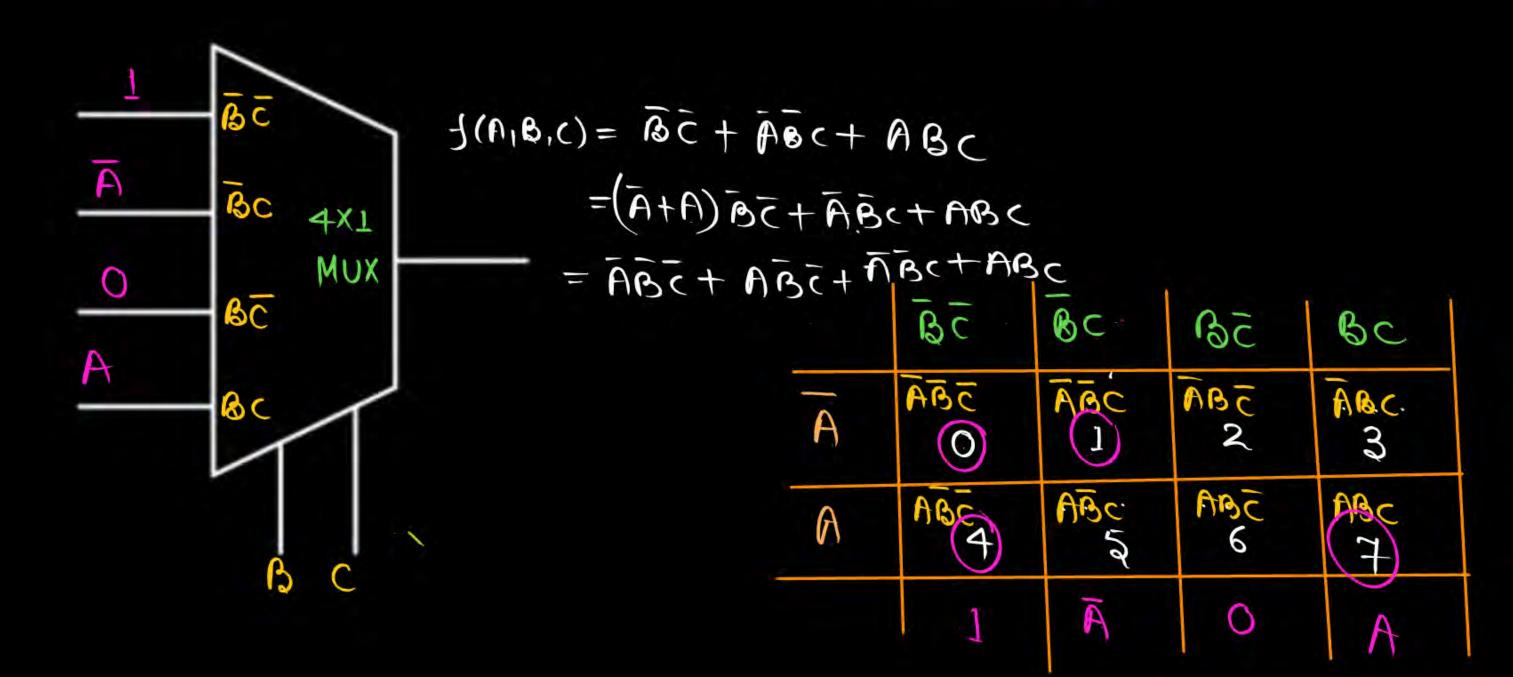
$$= \overline{A}\overline{B}\overline{C} + \overline{A}\overline{B}C + AB\overline{C} + ABC$$

	AB	AB	AB	AB
Ċ	A G G	ABC 2	4	ABC
C	AA (1)	yec.	ABC 5	7
	1	0	Ċ	C

BC is select line

$f(A,B,C)=\bar{A}\bar{B}\bar{C}+\bar{A}\bar{B}C+A\bar{B}\bar{C}+ABC$



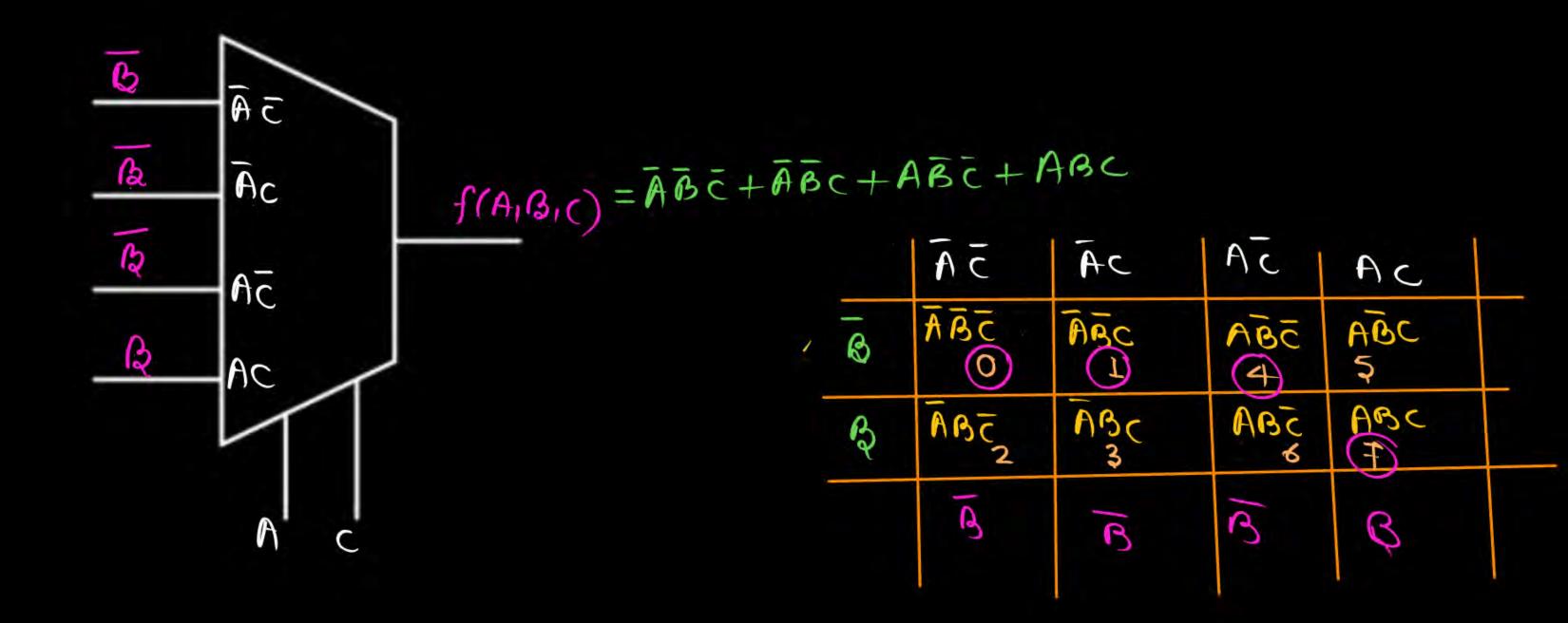


3. AC is select line

$$f(A, B, C) = \bar{A}\bar{B}\bar{C} + \bar{A}\bar{B}C + A\bar{B}\bar{C} + ABC$$

$$= \leq m(0, 1, 4, 7)$$





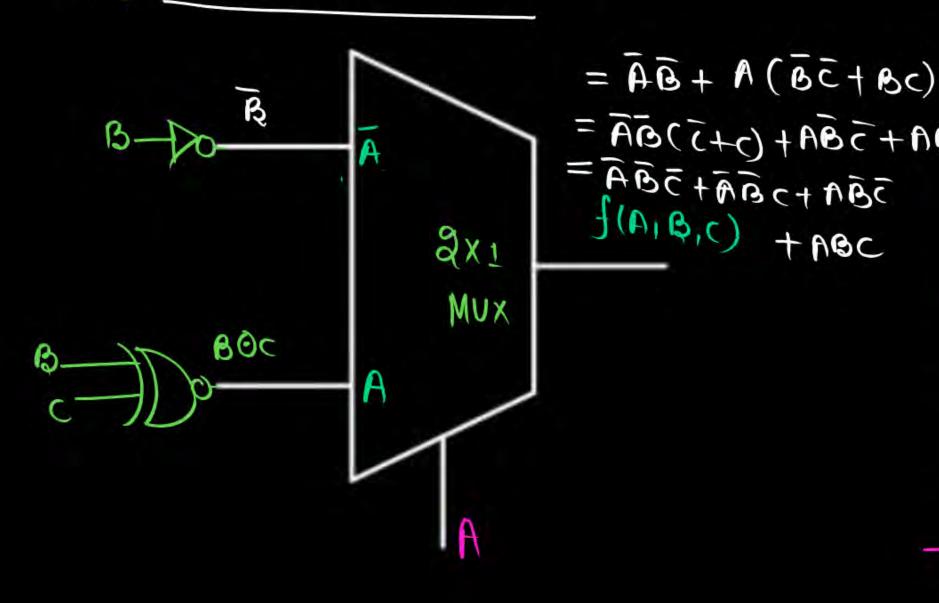
By using 2 × 1 MUX

f(AB,O)= ABC+ABC+ABC+ABC



tij "A" as a select line 3-

= Zm (ó,1,4,7)



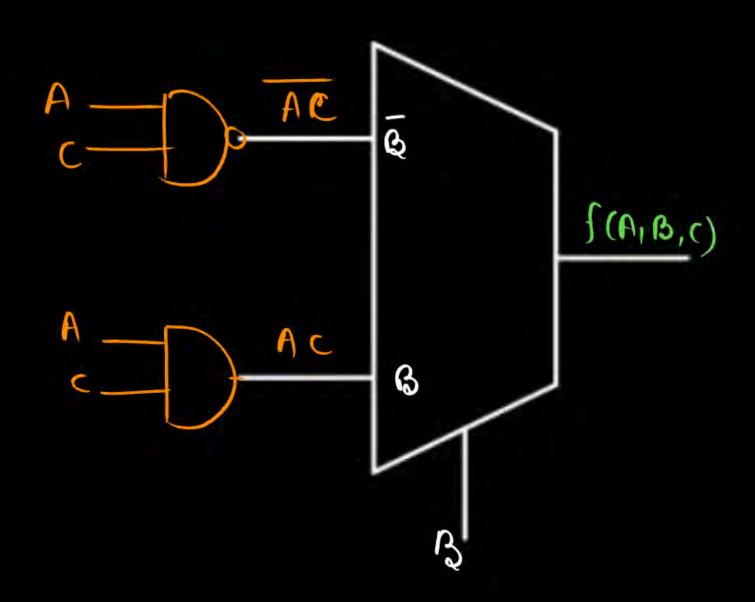
BC	<u></u>	A
BC	ABCO	ABE4
BC	AB CO	ABC 5
B-C	ĀBC 2	ABCE
BC	ABC 3	ABCT
	一百二十百つ 一百二十百つ	BC+BC BOC

By using 2 × 1 MUX

f(A,B,C)= Zm(0,1,4,7)



rij "B" as a select line



	B	B
A C	ABO	ĀBĒ
Ac	ABCI	ABC
ΑĒ	AB CA	ABC
AC	ABC 5	ABC
	AC+AC+A A(Z+C)+A	1,0
	A+AC A+C AC	

HW

f(A,B,C)= Zm(0,1,4,7)

Pw

tiij "c" as a select line





$$f(A, B, C, D) = \sum m(0, 1, 3, 5, 7, 9, 12, 15)$$

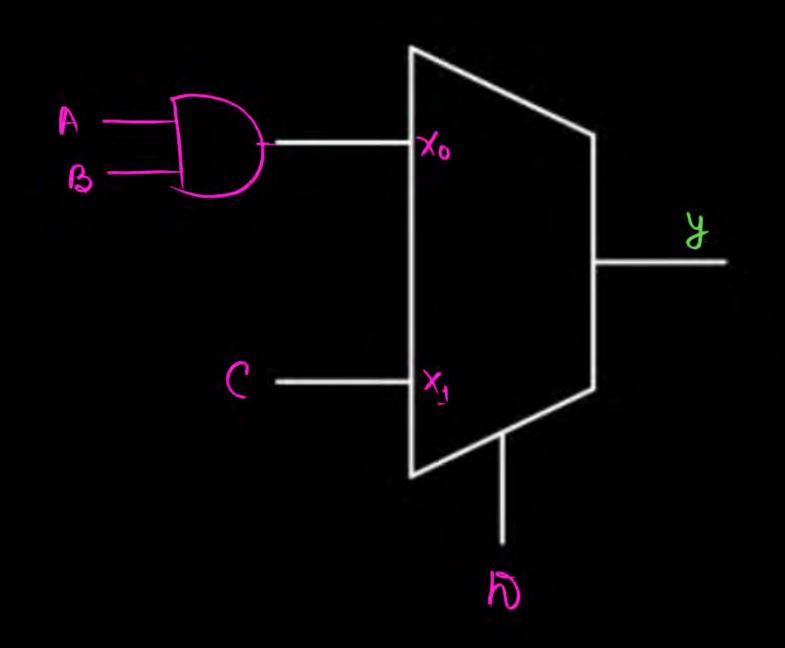
- ABD as a select line
- ACD as a select line

```
6X1 MUX
        8X1 MUX
   4) 2XIMYX
            TIJ AB select line
                 Ac Select line
Ai)A as a
             (iii) AA Select line
    selectline
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Type-6 Delay in MVX







Case(1)
$$\mathcal{R} = 0$$

$$T = T_{AND} + T_{MUX}$$

$$T = J_{MS} + 2J_{MS}$$

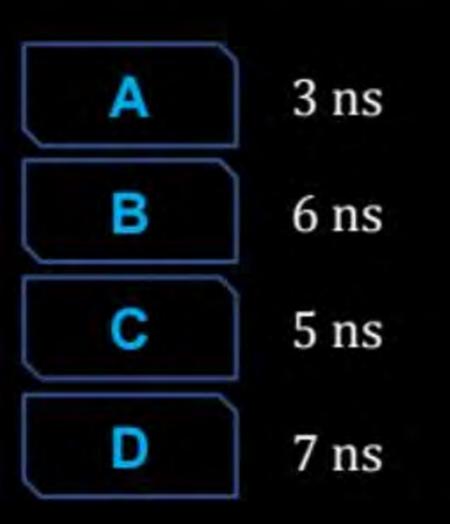
$$T = 3J_{MS}$$

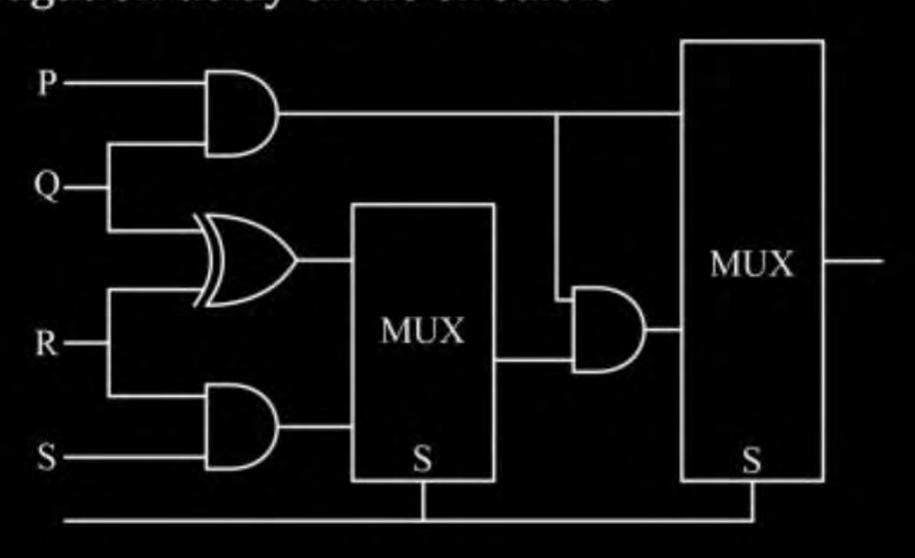
$$T = T_{MUX}$$

$$T = J_{MUX}$$



The propagation delay of the XOR gate, AND gate multiplexer (MUX) in the circuit shown in the figure are 4 ns, 2 ns and 1 ns, respectively. If all the inputs P, Q, R, S and T are applied simultaneously and held constant the maximum propagation delay of the circuit is







t.me/cJsIR

Thank you

GW Seldiers!

