CS & IT ENGINEERING

OPERATING SYSTEM

Intro and Background

Lecture No. 3

By- Dr. Khaleel Khan Sir
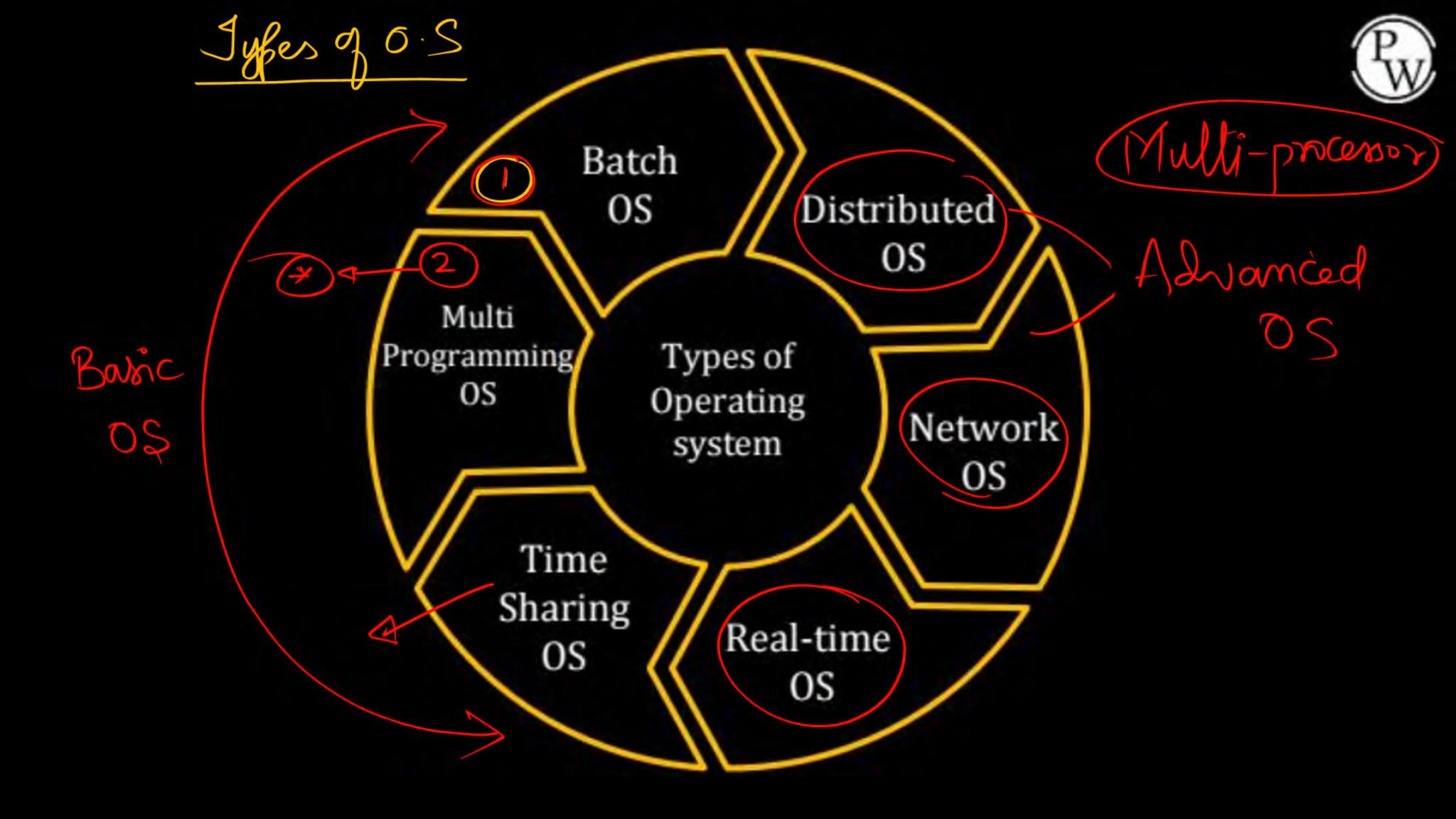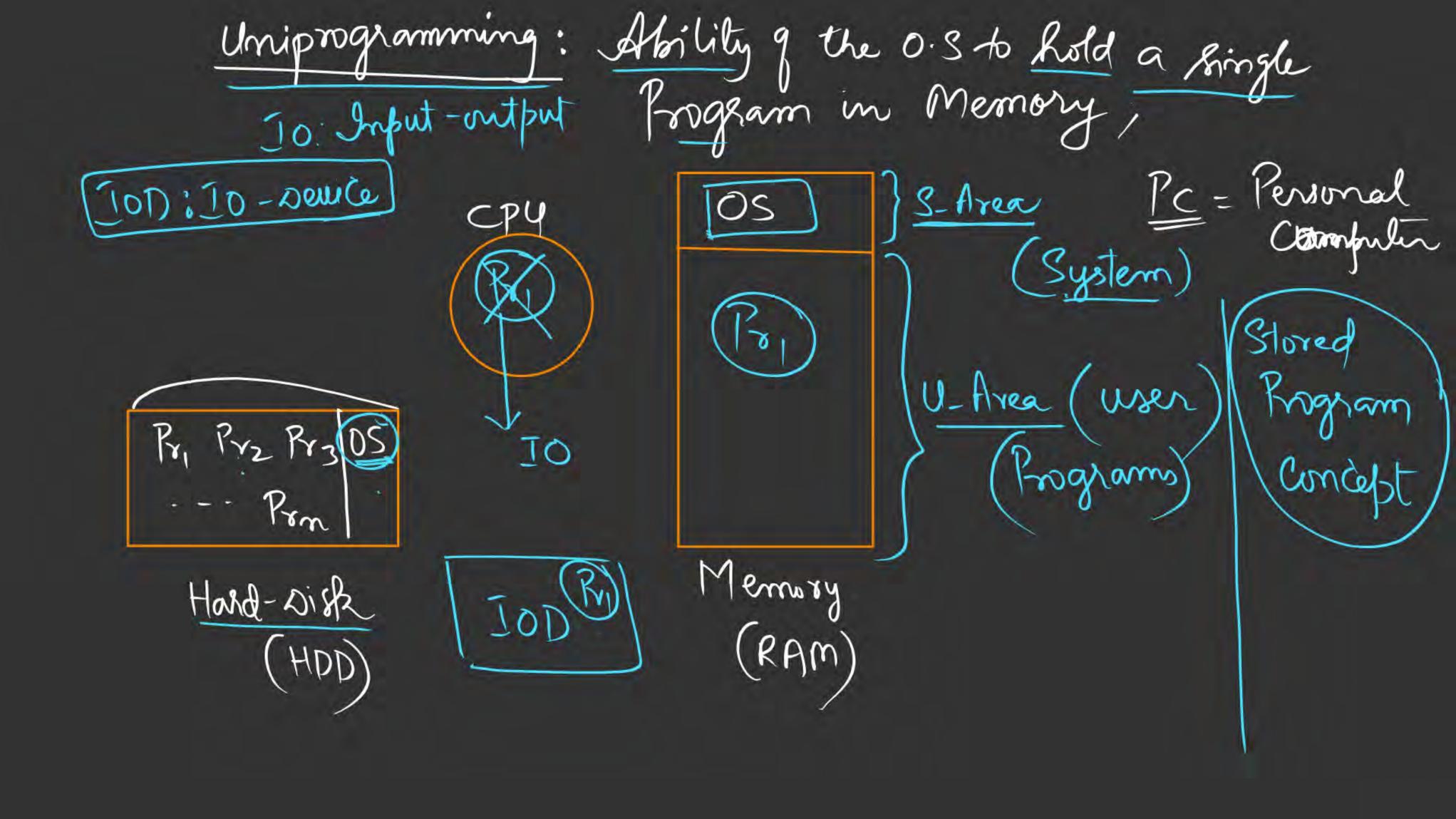
Types of O.S

Batch OS

Distributed OS

Multi Programming OS

Types of Operating system

Network OS

Time Sharing OS

Real-time OS

Multi-processor

Advanced OS

Basic OS
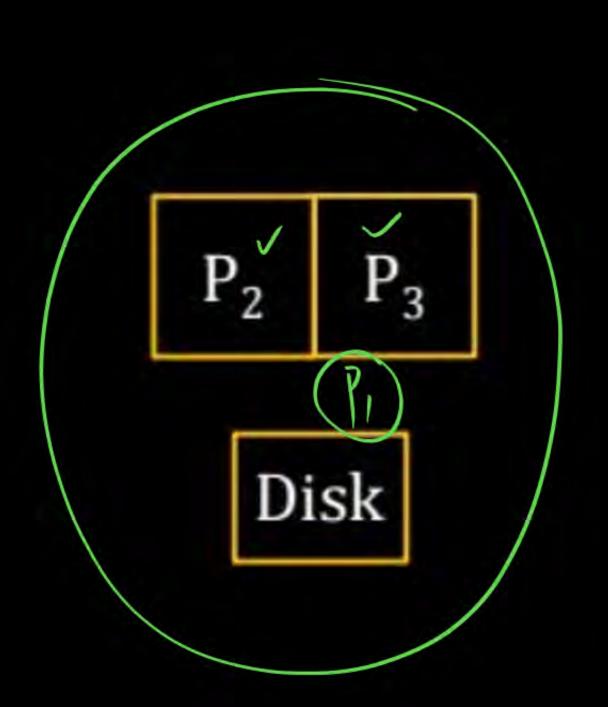
1st Gen of Computers (1930-40's) : No O.S

2nd Gen " " (1940-50's) : Magnetic Tapes :

3rd Gen of Computers (1950's - 60's)

$\longrightarrow$ No OS

$\downarrow$

Magnetic Disk (Hand Disk ; Floppy Disk)

(P.C)

✓
UNI-PROGRAMMED   MULTIPROGRAMMED

Uniprogramming : Ability of the O.S to hold a single Program in Memory,

JO : Input - output

IOD : IO - Device

CPU

IO

Hard - Disk
(HDD)

Pr_1 Pr_2 Pr_3 OS
$\cdots$ Pr_m

IOD Pr_1

OS } S-Area
(System)

Pr_1

U-Area (User)
(Programs)

Memory
(RAM)

PC = Personal Computer

Stored Program Concept

Drawback of uniprogramming: Ex: MS-DOS (Non-GUI)

(Disk O.S)
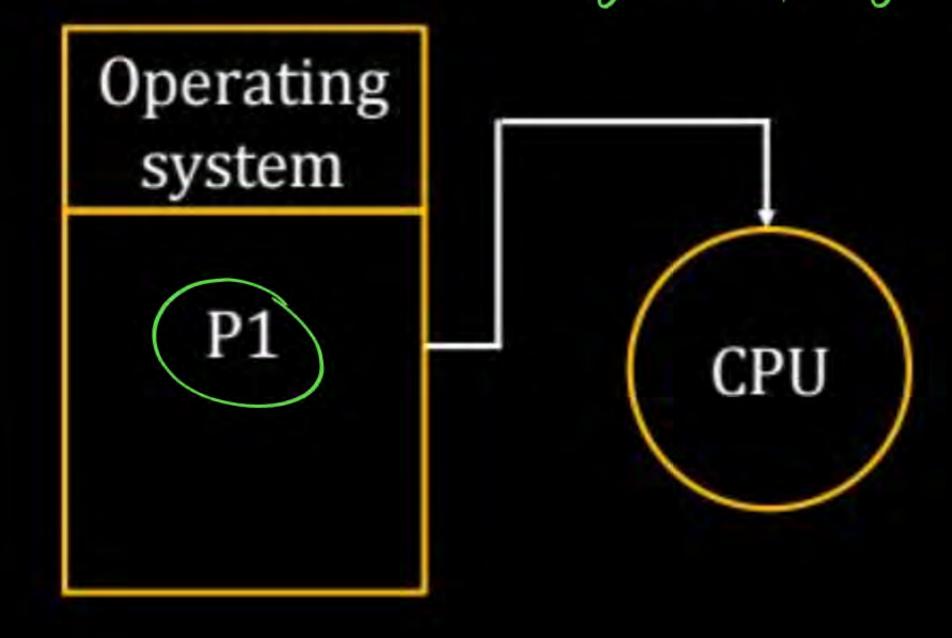
Single Program in Memory is not enough (Sufficient) to keep the cpu always busy;

Idleness of CPU

10 progs — 60's

? — 1 s

$\frac{10}{60} \rightarrow \left(\frac{1}{6}\right)$

→ Thruput will be less

(Efficiency)

Thruput = $\dfrac{No. \text{ of Programs Completed}}{Unit \cdot time}$

120 m — 20 topics

1 m — ?

$\left(\dfrac{20}{120}\right)$

Schematic View of Uni programmed O.S

Operating system

P1

CPU

P2 ✓ P3 ✓

P1

Disk
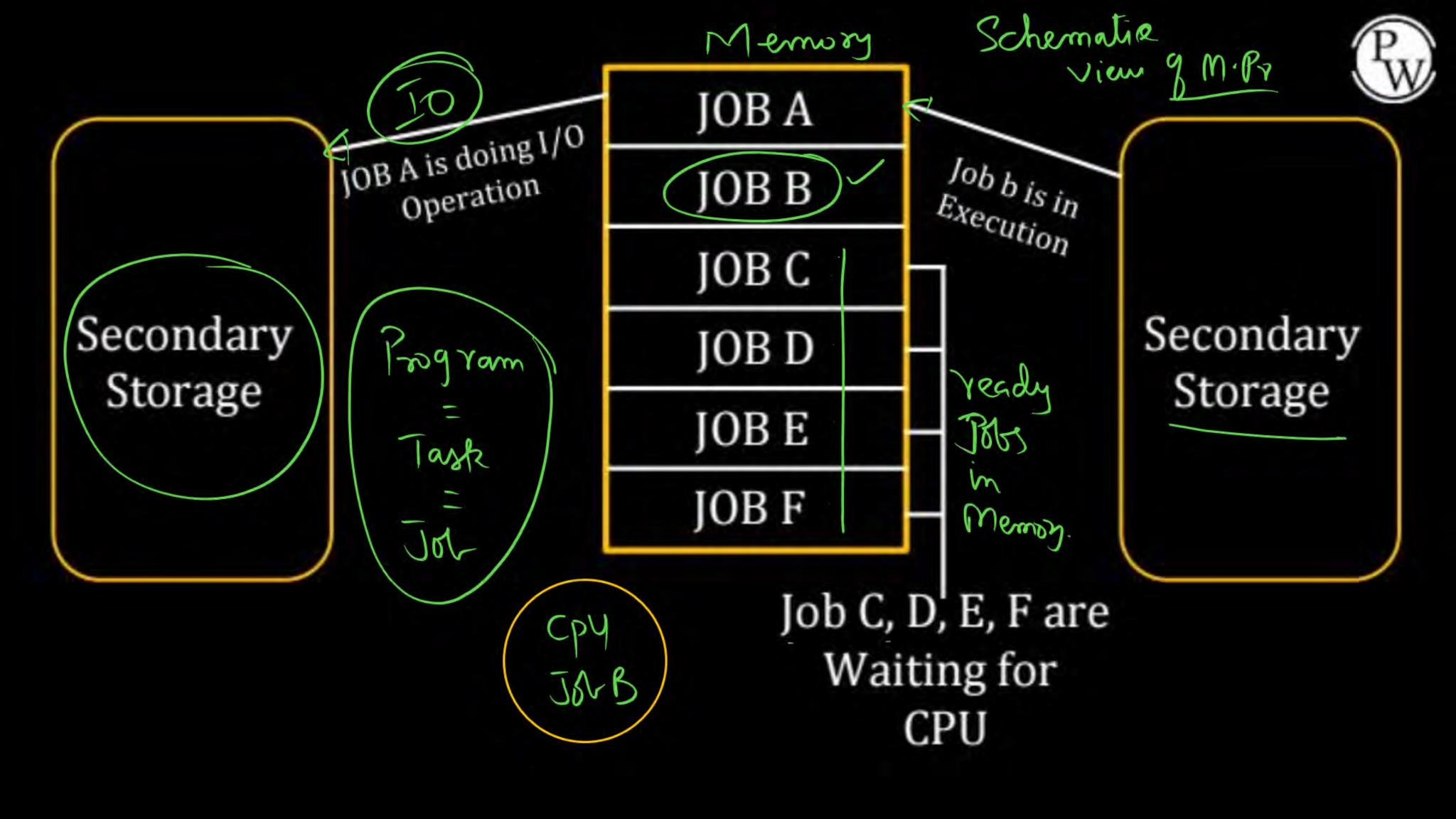
Main Memory

Multiprogramming : Ability q the O.S to hold (manage) multiple Ready to run Programs in Memory.

↓
(Multiplexing q CPU among different Programs in Memory)

S-Area.   OS ✓ ———→ M.Pr

| Pr₁ | } U-Area
| Pr₂ |
| Pr₃ |
| Pv₄ |

Wait {

Memory

objective:
(Max cpu utiliz)
Increase
Thruput
(efficiency)

disk

Pr₁ Pr₂ Pr₃ | OS
·· Pvₙ

CPU
(Pr₂)

Multi-Task
↓
(Program)

JOD (Pr₁)

Memory

Schematic view of M.Pr

I O

JOB A is doing I/O Operation

Secondary Storage

Program = Task = Job

Memory
JOB A
JOB B ✓
JOB C
JOB D
JOB E
JOB F

Job b is in Execution

ready Jobs in Memory.

Secondary Storage

CPU Job B

Job C, D, E, F are Waiting for CPU

* Types of M.Pr.

(WIN 3.0)

1) **Non-PreEmptive** (N.Pr)

2) **PreEmptive** (Pr)

No forceful dealloc.

Drawback:
→ Starvation to other waiting Programs
→ Lack of Interactiveness

OS
Pr₁
Pr₂
Pr₃

CPU Pr₁

disk
Pr₁, Pr₂ ...

I/O

PreEmptive : Forceful deallocation

Non-Pr: running Program on cpu will not be forced to leave the cpu. But it will release the cpu voluntarily (on its own)

(i) Complete its execution

(ii) Needs IO/Sys. Call

PreEmptive M. Pr. O.S

WIN-95
(WIN-10)
UNIX
LINUX
MAC

: Program running on cpu, can get forcefully deallocated from cpu (Pre Emption), (OS)

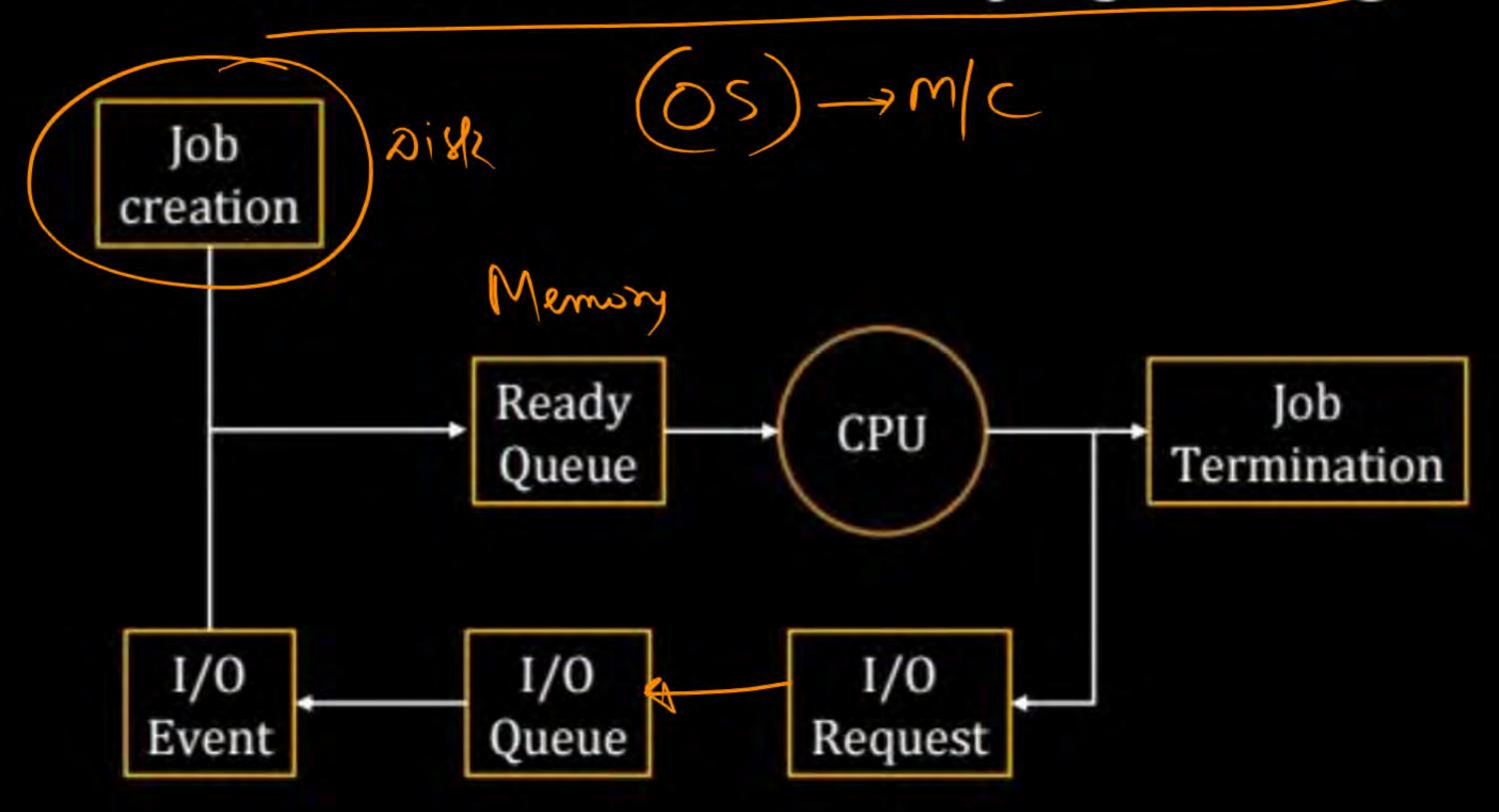So that other waiting Programs can get their chance to run on cpu

(How is PreEmption done)

→ Priority
Time

Pry
y
R_x
x

cpu
(OS)

$(y > x)$

(M.Pr. Timeshared OS)

⟨UNIX⟩   ⟨WIN-OMS⟩
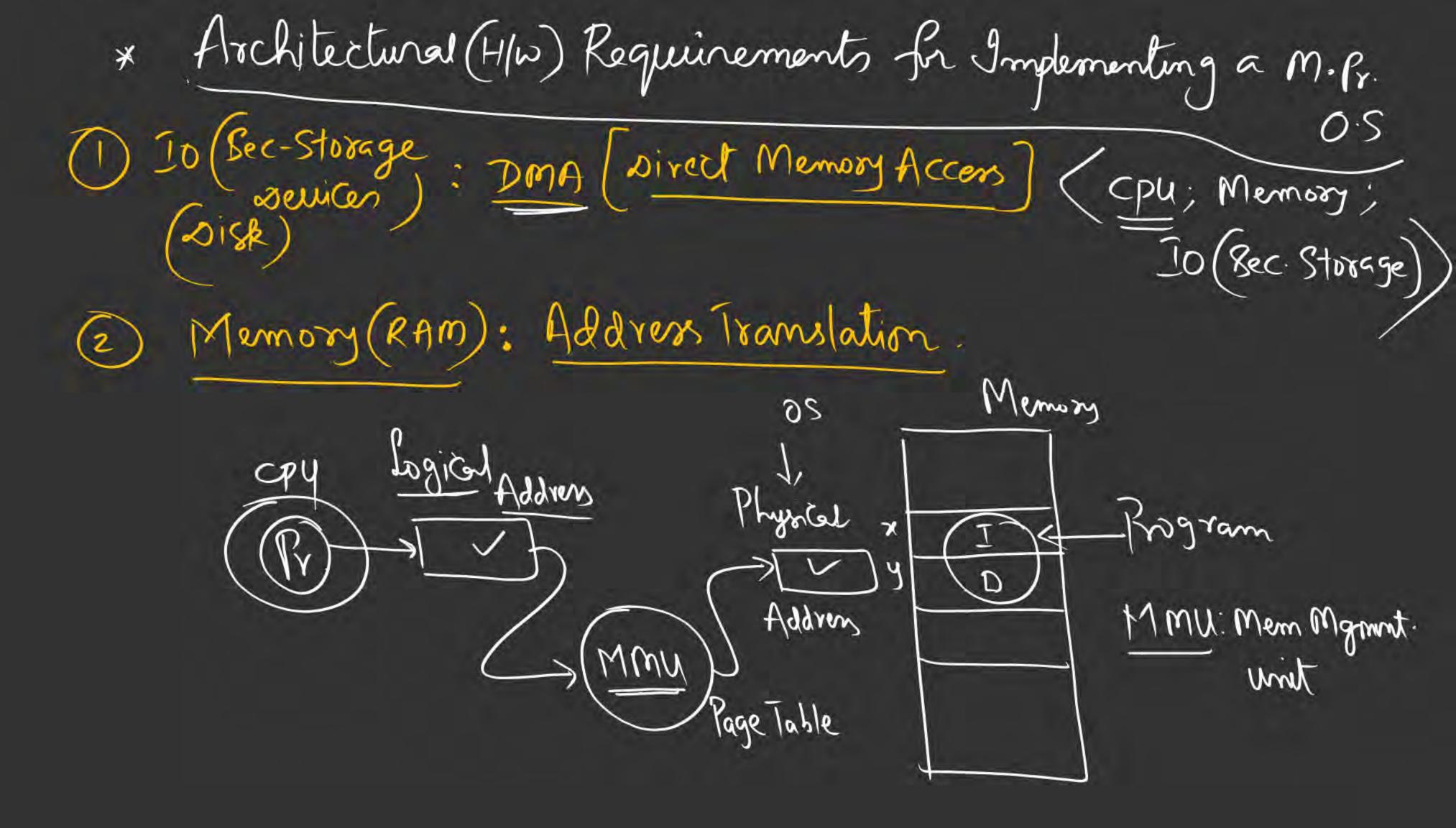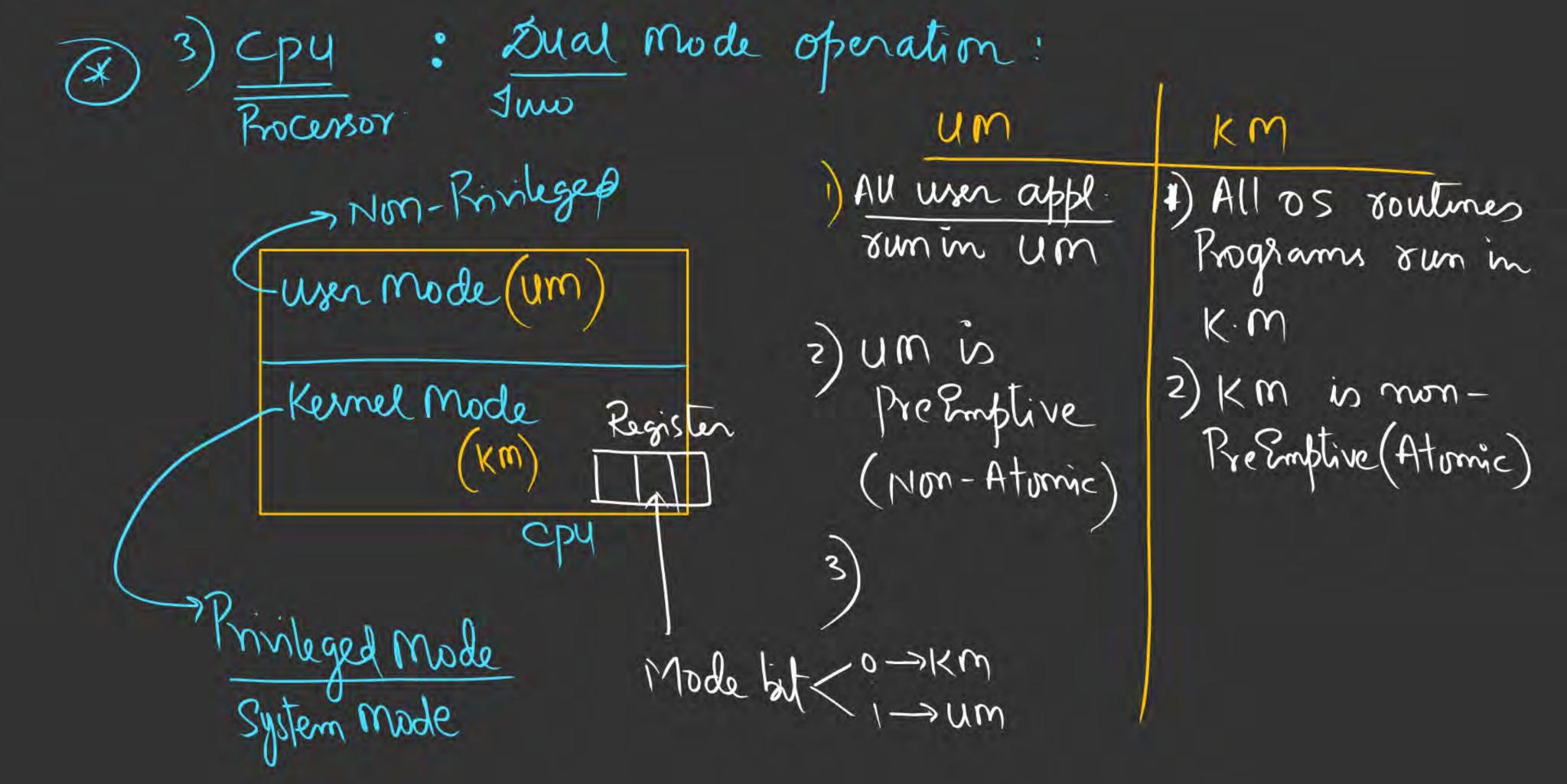
Multi-programming vs Multi-Tasking

objective

Study the design &   Pre-Emptive based
Implementation / Policies   M. Pr. O. S

of a Pre-Emptive based

M. Pr. O. S (like UNIX/LINUX/WIN/MAC)

Multiprogramming
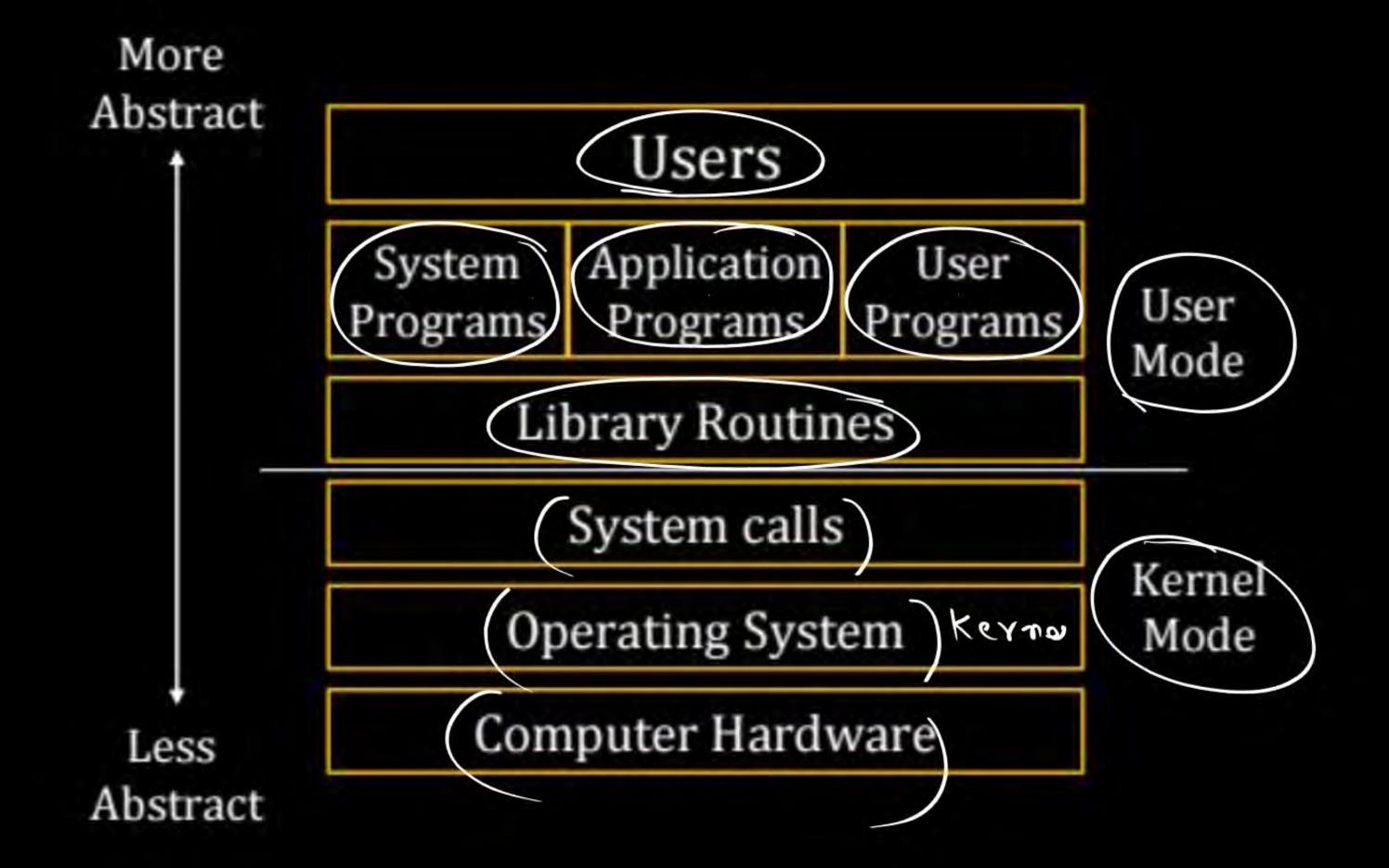## A Schematic view of Multiprogramming

$(OS) \rightarrow m/c$

Job creation — Disk

Memory

```
Job          →   Ready      →   CPU    →   Job
creation         Queue                     Termination
   ↓                                           ↓
I/O       ←   I/O        ←   I/O
Event         Queue          Request
```

* **Architectural (H/W) Requirements for Implementing a M.Pr.**

① IO (Sec-Storage devices) (Disk) : DMA [Direct Memory Access]

O.S
⟨ CPU ; Memory ; IO (Sec. Storage) ⟩

② Memory (RAM) : Address Translation

CPU → Logical Address □ ✓ ⟶ MMU (Page Table) ⟶ Physical Address □ ✓ ⟶ Memory

OS ↓

x, y

I / D

Program

MMU : Mem Mgmnt unit

✳ 3) CPU : **Dual** Mode operation :
‾‾‾‾   Two
Processor

→ Non-Privileged

⎡ User Mode (um)

  Kernel Mode
  (km)   Register
⎦   ▢▢▢▢
       CPU   ↑

→ Privileged Mode
System Mode

Mode bit ⟨ 0 → km
         ⟨ 1 → um

um	km
1) All user appl. run in um	1) All OS routines Programs run in k.m
2) um is PreEmptive (Non-Atomic)	2) km is non-PreEmptive (Atomic)
3)	

$\longrightarrow$ Many a Times, it becomes necessary to shift
the Mode from $U \longrightarrow K$ & back from
$$K \longrightarrow U \; ;$$

(**) Mode Shifting

is needed to avail
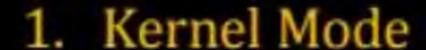
OS - Services

(??)

my.c

Um   U.A   u

k.m   OS   k
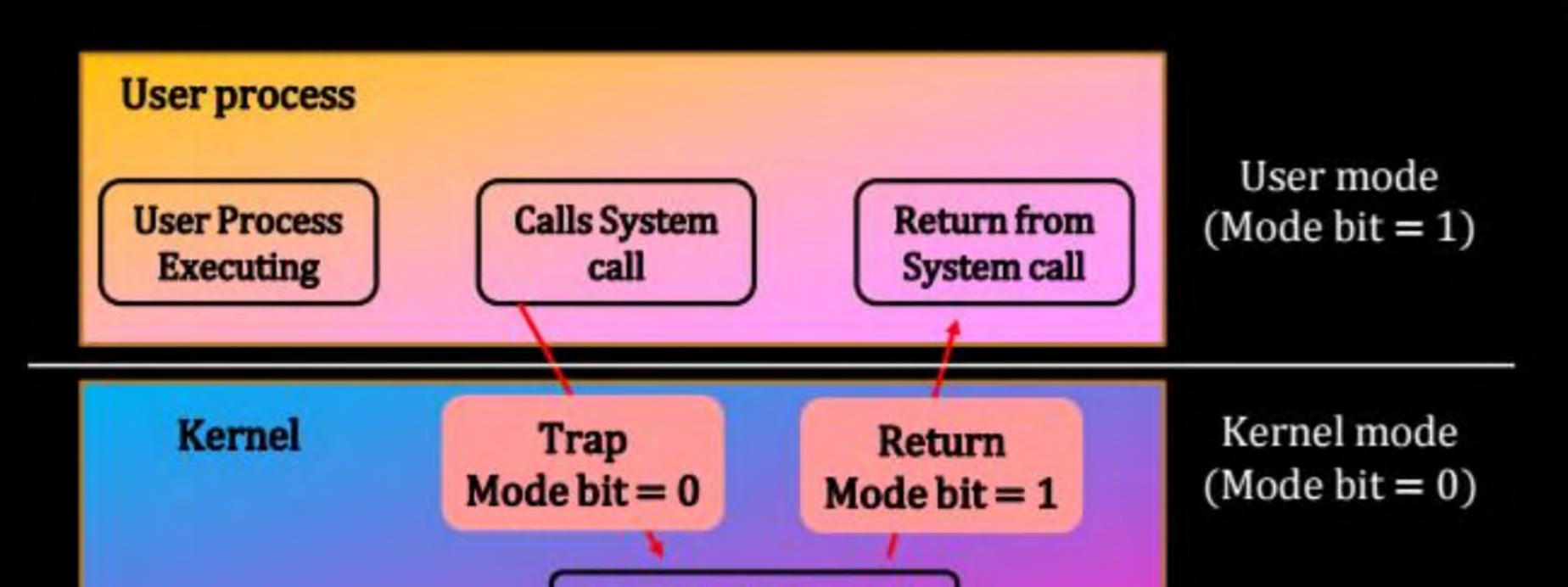
CPU

1. **Kernel Mode**

   In Kernel mode, the executing code has complete and unrestricted access to the underlying hardware. It can execute any CPU instruction and reference any memory address. Kernel mode is generally reserved for the lowest-level, most trusted functions of the operating system. Crashes in kernel mode are catastrophic; they will halt the entire PC.

2. **User Mode**

   In User mode, the executing code has no ability to directly access hardware or reference memory. Code running in user mode must delegate to system APIs to access hardware or memory. Due to the protection afforded by this sort of isolation, crashes in user mode are always recoverable. Most of the code running on your computer will execute in user mode.