CS & IT



ENGINEERING



Sequential Circuit

Lecture No. O



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TOPICS TO BE COVERED 01 Registers, Basic of Counters

02 PRACTICE

03 DISCUSSION

COUNTER



- Counters are used to count Number of clock.
- Counter are used as Frequency Divider Circuit.
- Counter are also used in ADC.
- 4. Counters are also known as pulse stretcher circuit.
- Counters also used in RADAR for detection of Range.

TYPES OF COUNTER

Pw

- Asynchronous Counter.
- Synchronous Counter.

TYPES OF COUNTER

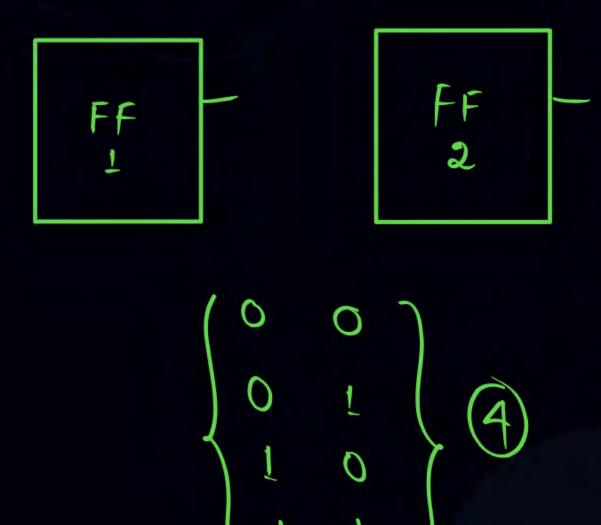


 Asynchronous Counter: Only one flip flop having external clock and output of that flip flop will become clock for the next flip flop.
 So when clock applied only one flip flop work at that time.

 Synchronous Counter: All Flip Flops are connected with the same clock. Hence when clock is applied all the flip flops work simultaneously.









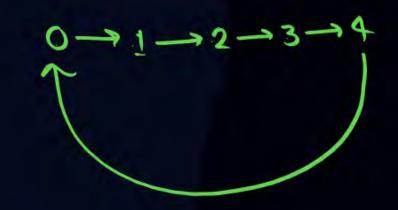
Maximum number of states = 2h

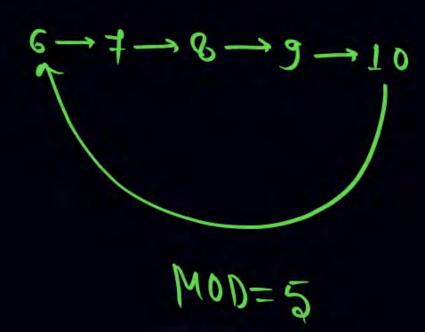
n -> no. of FF's.



MODULUS OF COUNTER

Total no. of states used by the counter are called mod of the counter.





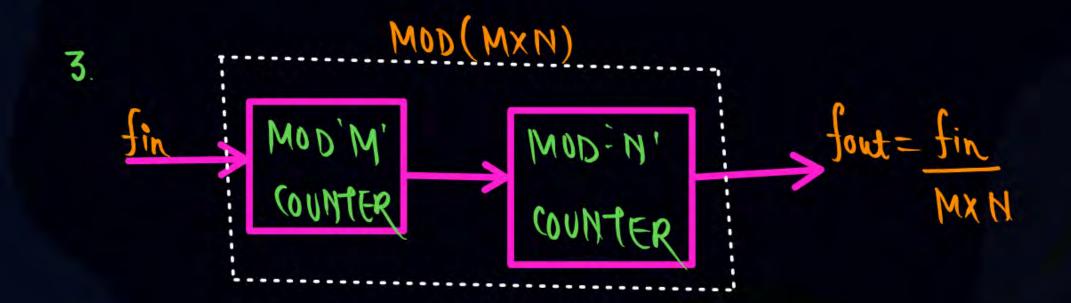


HOTE: 1.

fin MOD"M"

fout= fin

COUNTER





Bco counter

$$0 \to 00000$$

$$1 \to 00001$$

$$2 \to 0000$$

$$3 \to 0001$$

$$4 \to 0000$$

$$5 \to 0100$$

$$6 \to 0110$$

$$7 \to 0111$$

$$8 \to 1000$$

$$9 \to 1000$$

MOD 7 10

M < 2h

N7/092M

N7/092L0

N7/3. something

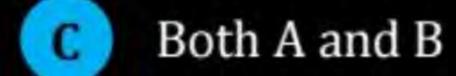


If mode-5 counter is cascaded with mod-2 counter, then it will become?

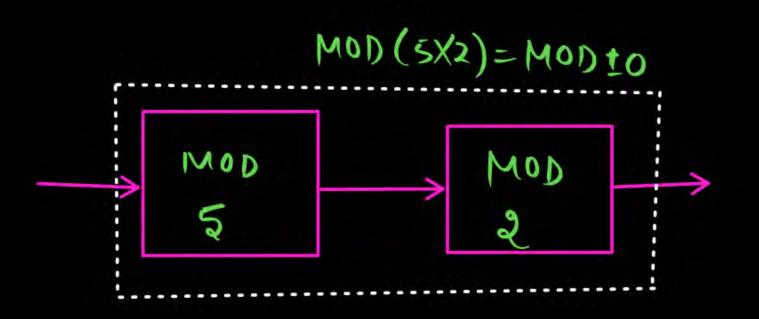


Mod-10 counter











y MOD-100

No. of FF?

 $M \leq a^n$

n 7/09214

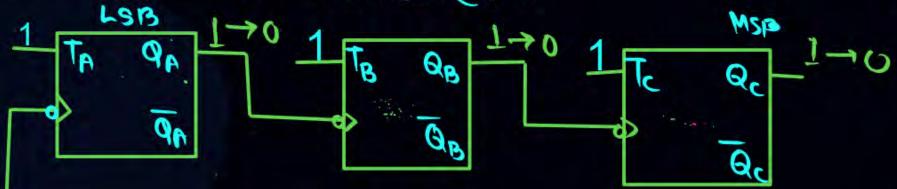
> log_100
> 6. something

Asynchronous counter

- All the FF's are used in toggle mode.

3 bit Ripple counter

MOD-8 UP RIPPLE COUNTER:-



CLK

O B B B B B

→ an will toggle when -ve edge of the external clk appear.

→ an will toggle when an goes from 1 to 0.

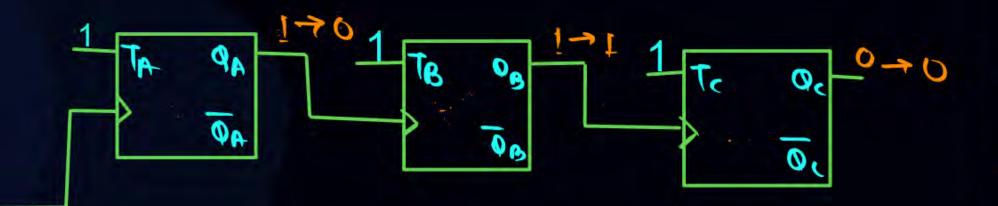
- ac will toggle When ab goes from 1 to 0.

	L L	OK /	SI E
CLOCK	Qc	as	QA
O	0	0	0
1	0	0	1
_2	0	k	0
_3	0	1	1
V 4	1	0	0
√ 5	Ţ	0	1
6	1	L	0
ーノす	1	l.	L
8	b	0	0
~9	0	6	1
16	0	1	0

MOD(8

MOD-8 DOWN RIPFLE COUNTER

TII- 110-101-100-1011-010-001-000

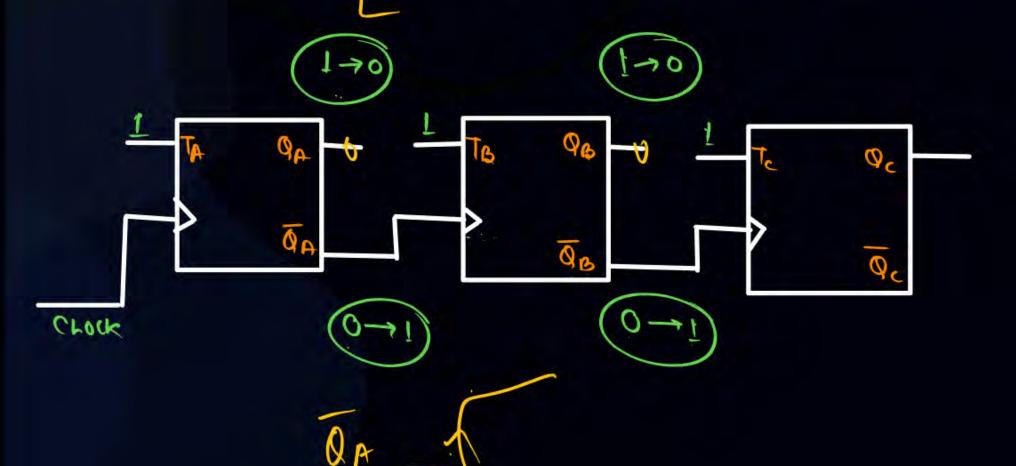


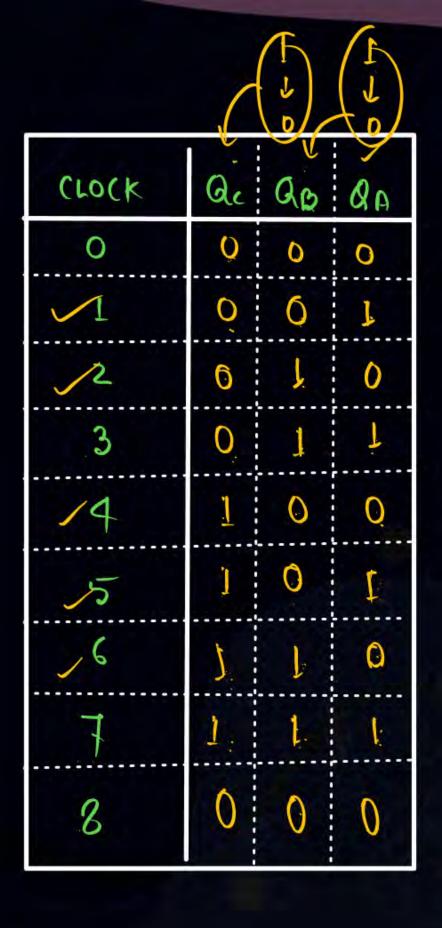
和和和和新的

cck

CLOCK	Qc	as	-	
0	0	O,	0	
1	1	l	J.	
12];	Ŀ	0	
3	1	0	1	
V 4	1.	0	0	
15	0	L	L	
1	0	l.	0	
7	0	0	1	
V 8	0	0	0	
V 9)	1	L	
	L	1	0	

MOD-8 UP RIPPLE COUNTER

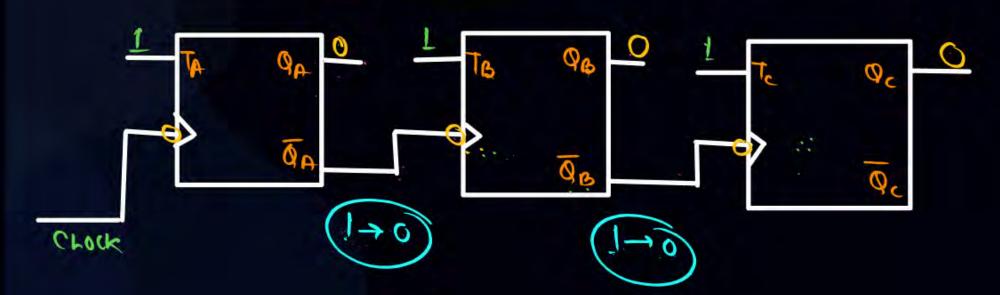






time/cjsiR

MOD-8 DOWN RIPPLES TOUNTER

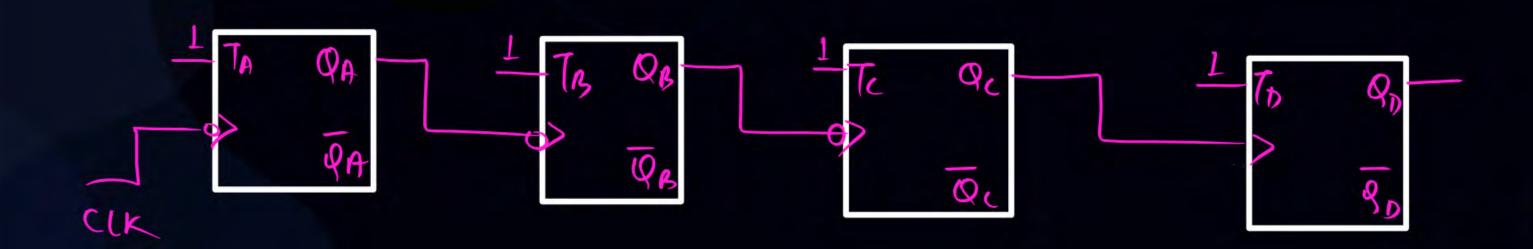




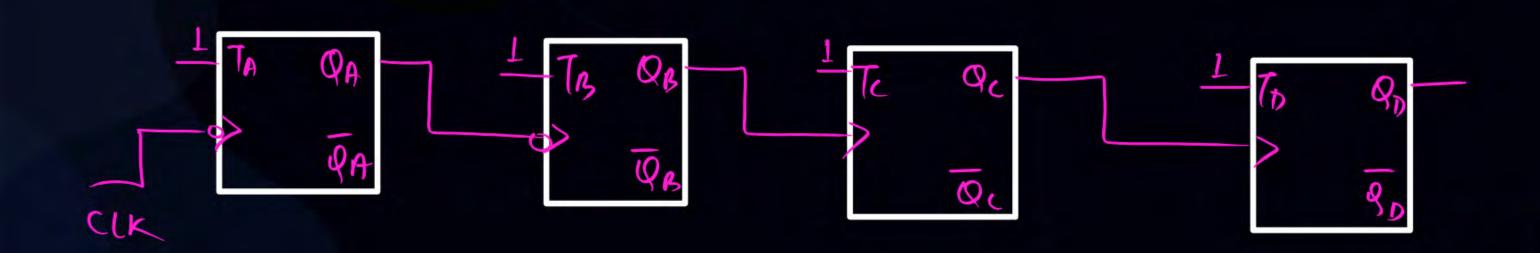




Crock		QB	QA
0	0		0
1	1.	b	L
9	I,	I.	0.
3	1	0	F
4	1		Ò
5	0	1	t
6	Ö	Ţ	0
7	0		1
6	0	0	

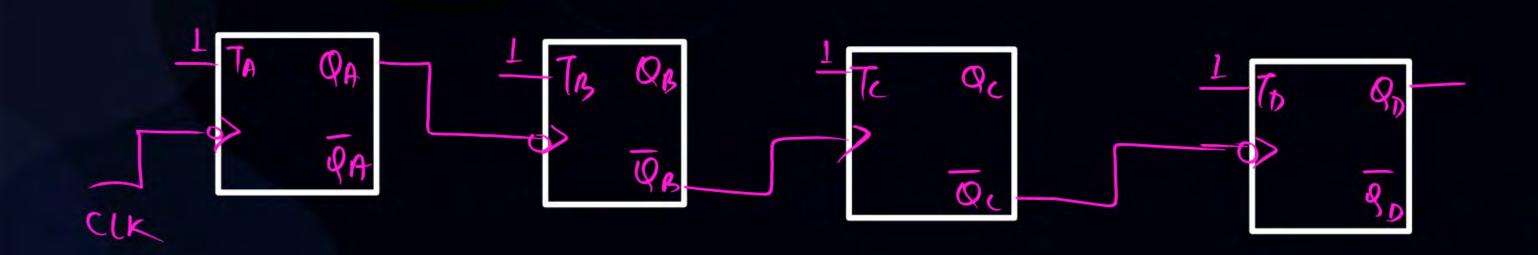


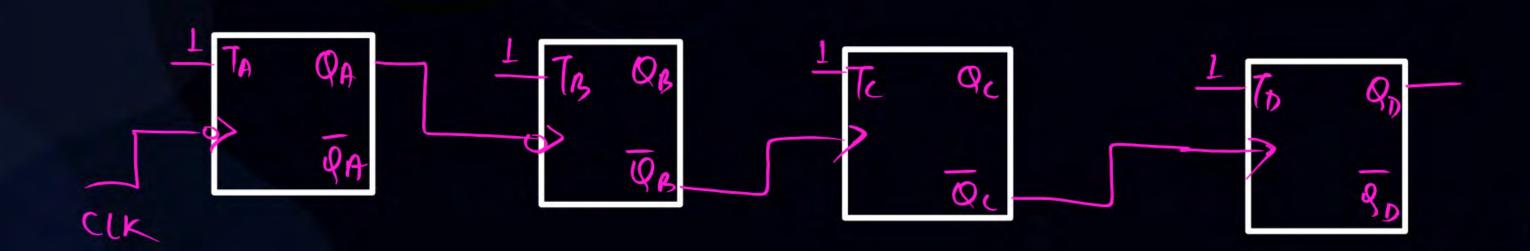
YO



YO







0



Thank you

Seldiers!

